

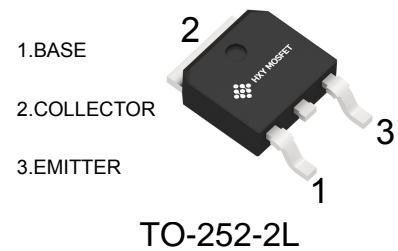


## Features

- Medium Power Complementary Silicon Transistors

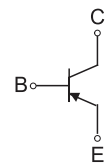
## Package Marking and Ordering Information

Product ID	Pack	Packing Method	Qty(PCS)
MJD42C	TO-252-2L	Tape and Reel	2500



## Maximum Ratings (Ta=25°C unless otherwise noted)

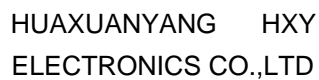
Symbol	Parameter	Value	Unit
$V_{CBO}$	Collector-Base Voltage	-100	V
$V_{CEO}$	Collector-Emitter Voltage	-100	V
$V_{EBO}$	Emitter-Base Voltage	-6	V
$I_C$	Collector Current -Continuous	-3	A
$P_C$	Collector Power Dissipation	1	W
$T_J, T_{stg}$	Operation Junction and Storage Temperature Range	-55~+150	°C



## Electrical Characteristics (Ta=25°C unless otherwise specified)

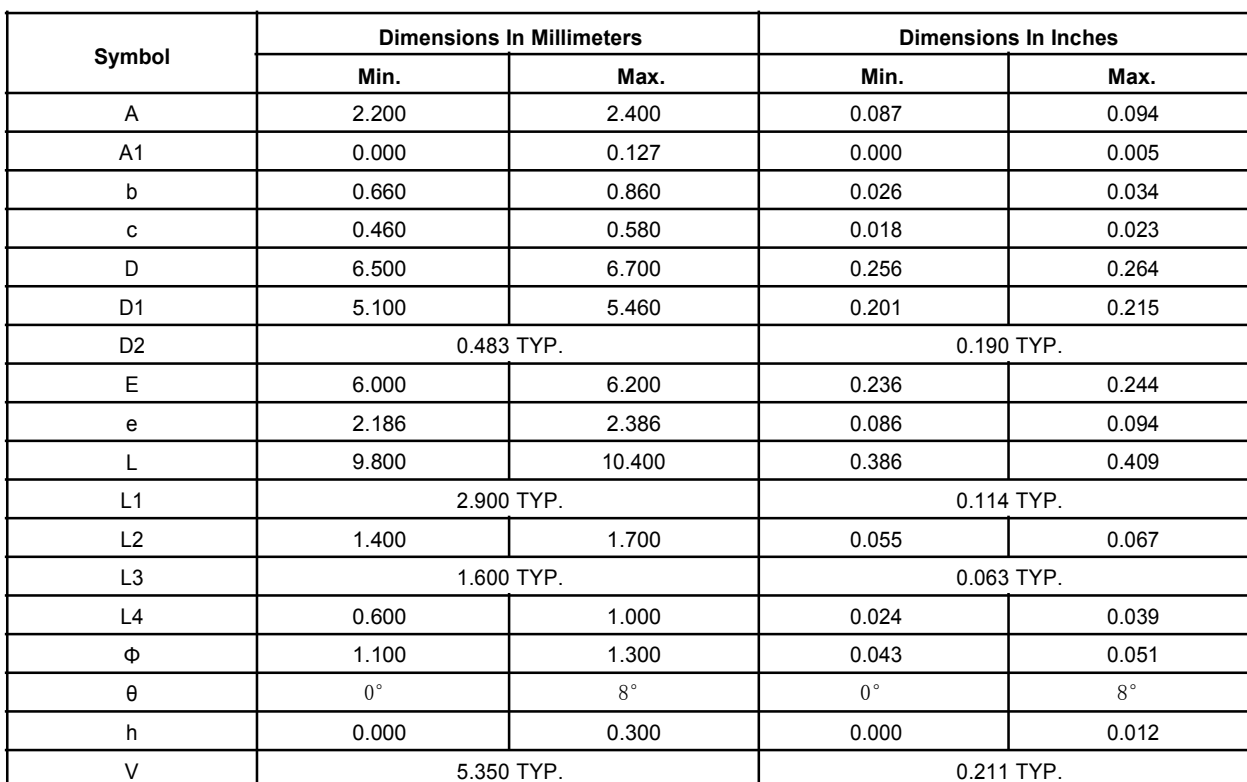
Parameter	Symbol	Test conditions	Min	Max	Unit
Collector-base breakdown voltage	$V_{(BR)CBO}$	$I_C = -1mA, I_E = 0$	-100		V
Collector-emitter breakdown voltage	$V_{(BR)CEO}^*$	$I_C = -1mA, I_B = 0$	-100		V
Emitter-base breakdown voltage	$V_{(BR)EBO}$	$I_E = -1mA, I_C = 0$	-6		V
Collector cut-off current	$I_{CBO}$	$V_{CB} = -100V, I_E = 0$		0.1	uA
Collector cut-off current	$I_{CEO}$	$V_{CE} = -100V, I_B = 0$		0.5	uA
Emitter cut-off current	$I_{EBO}$	$V_{EB} = -6V, I_C = 0$		0.1	uA
DC current gain	$h_{FE(1)}$	$V_{CE} = -5V, I_C = -0.2A$	100	200	
	$h_{FE(2)}$	$V_{CE} = -5V, I_C = -5mA$	25		
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_C = -1.5A, I_B = 0.15A$		-0.5	V
Transition frequency	$f_T$	$V_{CE} = -5V, I_C = -0.1A$	50		MHz

\* Pulse test



MJD42C  
PNP Plastic-Encapsulate Transistors

## TO-252-2L Package Outline Dimensions





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