



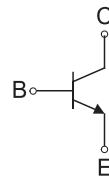
Features

- Medium Power Complementary Silicon Transistors



Package Marking and Ordering Information

Product ID	Pack	Packing Method	Qty(PCS)
MJD41C	TO-252-2L	Tape and Reel	2500



Maximum Ratings (Ta=25°C unless otherwise noted)

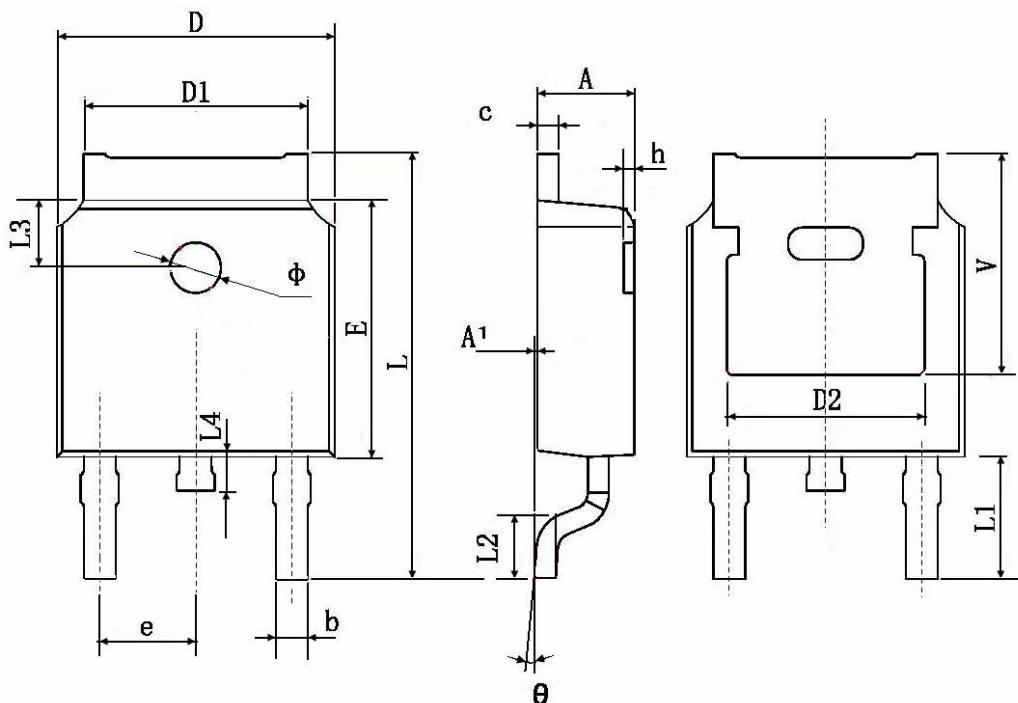
Symbol	Parameter	Value	Unit
V _{CBO}	Collector-Base Voltage	100	V
V _{CEO}	Collector-Emitter Voltage	100	V
V _{EBO}	Emitter-Base Voltage	6	V
I _C	Collector Current -Continuous	3	A
P _C	Collector Power Dissipation	1	W
T _J , T _{stg}	Operation Junction and Storage Temperature Range	-55~+150	°C

Electrical Characteristics (Ta=25°C unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Max	Unit
Collector-base breakdown voltage	V _{(BR)CBO}	I _C = 1mA, I _E =0	100		V
Collector-emitter breakdown voltage	V _{CEO(sus)}	I _C = 1mA, I _B =0	100		V
Emitter-base breakdown voltage	V _{(BR)EBO}	I _E = 1mA, I _C =0	6		V
Collector cut-off current	I _{CBO}	V _{CB} =100V, I _E =0		0.1	uA
Collector cut-off current	I _{CEO}	V _{CE} = 100V, I _B = 0		0.5	uA
Emitter cut-off current	I _{EBO}	V _{EB} =6V, I _C =0		0.1	uA
DC current gain	h _{FE(1)}	V _{CE} =5V, I _C = 0.01A	100	200	
	h _{FE(2)}	V _{CE} =5V, I _C = 0.2A	25		
Collector-emitter saturation voltage	V _{CE(sat)}	I _C =1.5 A, I _B =0.15A		0.5	
Transition frequency	f _T	V _{CE} =-5V, I _C =-0.1A f=10MHz	50		MHz



TO-252-2L Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.660	0.860	0.026	0.034
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	0.483 TYP.		0.190 TYP.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.800	10.400	0.386	0.409
L1	2.900 TYP.		0.114 TYP.	
L2	1.400	1.700	0.055	0.067
L3	1.600 TYP.		0.063 TYP.	
L4	0.600	1.000	0.024	0.039
phi	1.100	1.300	0.043	0.051
theta	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.350 TYP.		0.211 TYP.	



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