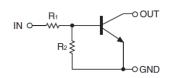


Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input.
 They also have the advantage of almost completely eliminating parasitic effects.
- Only the on/off conditions need to be set for operation,
 making device design easy



SOT-23-3L



Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
HDTC144EKAT146	SOT-23-3L	26	3000

Maxmim Ratings (Ta=25 unless otherwise noted)

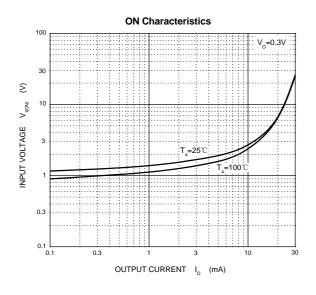
Symbol	Parameter	Limits	Unit
V _{cc}	Supply Voltage	50	V
V _{IN}	Input Voltage	-10 <i>∼</i> +40	V
lo	Output Current	30	mA
I _{CM}	Peak Collector Current	100	mA
P _D	Power Dissipation	200	mW
T_J, T_{stg}	Operation Junction and Storage Temperature Range	-55∼+150	°C

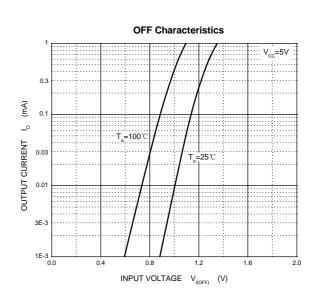
Electrcal Charcteristics (Ta=25 unless otherwise specified)

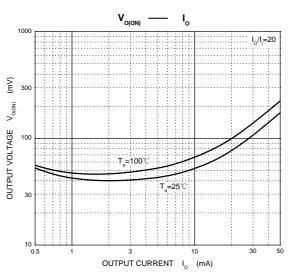
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Input voltage	V _{I(off)}	V _{CC} =5V,I _O =100μA	0.5			V
	V _{I(on)}	V _O =0.3V,I _O =2mA			3	V
Output voltage	V _{O(on)}	I _O /I _I =10mA/0.5mA			0.3	V
Input current	I _I	V _I =5V			0.18	mA
Output current	I _{O(off)}	V _{CC} =50V,V _I =0			0.5	μΑ
DC current gain	Gı	V _O =5V,I _O =5mA	68			
Input resistance	R ₁		32.9	47	61.1	kΩ
Resistance ratio	R ₂ /R ₁		0.8	1	1.2	
Transition frequency	f _T	V _O =10V,I _O =5mA,f=100MHz		250		MHz

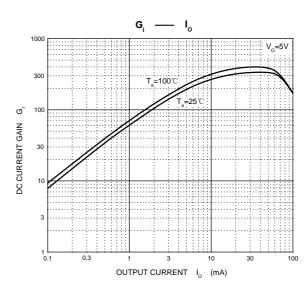


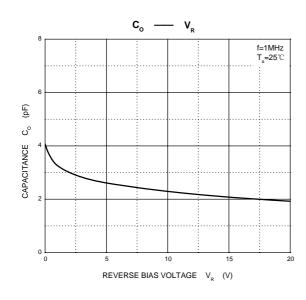
Typical Characteristics

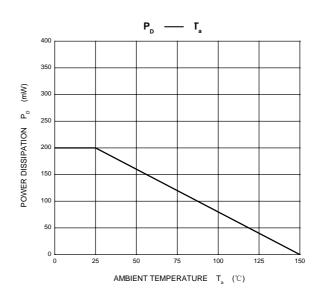






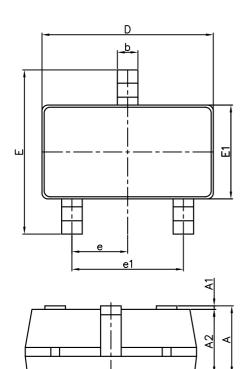


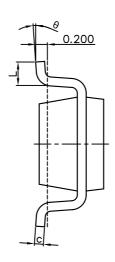






SOT-23-3L Package Outline Dimensions





Symbol	Dimensions In Millimeters		Dimensions In Inches	
Symbol	Min.	Max.	Min.	Max.
Α	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
С	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E1	1.500	1.700	0.059	0.067
E	2.650	2.950	0.104	0.116
е	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°



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