



DESCRIPTION

IC MAX232EIDR is purposed for application in high-performance information processing systems and control devices of wide application. Input voltage levels are compatible with standard CMOS levels.

PIN 1

SOP-16

APPLICATIONS

- Portable Computers
- Battery-Powered RS-232 Systems
- Interface Translation
- Low-Power Modems
- Terminals

FEATURES

- Output voltage levels are compatible with input levels of C-MOS, N-MOS and TTL integrated circuits.
- Supply voltage range from 2.0 to 6.0 V.
- Low input current: 1.0 mkA; 0.1 mkA at T = 25 °C.
- Output current 24 mA.
- Latching current not less than 450 mA at T = 25°C
- Tolerable value of static potential not less than 2000V

FUNCTION TABLE

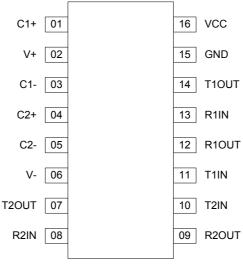
INPUT	OUTPUT
(RIN, TIN)	(ROUT, TOUT)
L (Low Level)	H (High Level)
H (High Level)	L (Low Level)

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MIN.	MAX.	UNIT
Supply Voltage	V _{cc}	-0.3	6.0	V
Transmitter High Output Voltage	V ₊	V _{CC} -0.3	14	V
Transmitter Low Output Voltage	V ₋	-0.3	-14	V
Transmitter Input Voltage	V _{TIN}	-0.3	V ₊ +0.3	V
Receiver Input Voltage	V_{RIN}	-30	30	V
Voltage Applied to Transmitter Output	V _{TOUT}	V ₋ -0.3	V ₊ +0.3	V
Voltage Applied to Receiver Output	V_{ROUT}	-0.3	V _{CC} +0.3	V
Storage Temperature Range	T _{STG}	-65	150	°C



PIN CONFIGURATION



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PIN DESCRIPTION

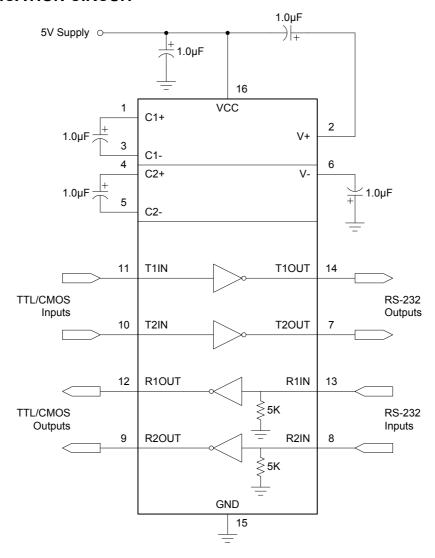
Pin No.	Pin Name	Pin Description	
1	C1+	Terminal for Positive Charge-Pump C1 Capacitor	
2	V+	Positive Voltage Generated by the Charge-Pump	
3	C1-	Terminal for Negative Charge-Pump C1 Capacitor	
4	C2+	Terminal for Positive Charge-Pump C2 Capacitor	
5	C2-	Terminal for Negative Charge-Pump C2 Capacitor	
6	V-	Negative Voltage Generated by the Charge-Pump	
7	T2OUT	RS-232 Driver Output (Levels RS-232)	
8	R2IN	RS-232 Receiver Input (Levels RS-232)	
9	R2OUT	RS-232 Receiver Output (Levels TTL/CMOS)	
10	T2IN	RS-232 Driver Input (Levels TTL/CMOS)	
11	T1IN	RS-232 Driver Input (Levels TTL/CMOS)	
12	R10UT	RS-232 Receiver Output (Levels TTL/CMOS)	
13	R1IN	RS-232 Receiver Input (Levels RS-232)	
14	T1OUT	RS-232 Driver Output (Levels RS-232)	
15	GND	Ground	
16	VCC	Supply Voltage Input	



RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	MIN.	MAX.	UNIT
Supply Voltage	V _{CC}	4.5	5.5	V
Transmitter Input Voltage	V _{TIN}	0	V _{CC}	V
Receiver Input Voltage	V_{RIN}	-30	30	V
Output Current of Transmitter Short Circuit	I _{SC}	-	±60	mA
Ambient Temperature Range	T _A	-40	+85	°C

TYPICAL APPLICATION CIRCUIT







ELECTRICAL CHARACTERISTICS

(Limits in standard typeface are for T_A =25°C, and the limits in boldface type apply over full operating temperature range.)

PARAMETER	SYMBOL	TEST CON	DITIONS	MIN.	TYP.	MAX.	UNIT
Supply Current	I _{CC}	V _{CC} = 5.5V V _{IL} = 0V		-	_	10.0 14.0	mA
Receiver Parameters							
Hysteresis Voltage	V _h	V _{CC} = 5.0V		0.2 0.2	-	0.9 1.0	٧
On (Operation) Voltage	V _{on}	$V_0 \le 0.1 V, I_{OL} \le 2$.0μΑ	-	-	2.4 2.3	V
Off (Dropout) Voltage	V _{off}	$V_O \ge V_{CC} - 0.1V$ $I_{OH} \le -20\mu A$		0.8 0.9	-	-	V
Output Low Voltage	V _{OL}	I _L = 3.2mA, V _{CC} = V _{IH} = 2.4V	4.5V,	-	-	0.3 0.4	V
Output High Voltage	V _{OH}	$I_{OH} = -1.0 \text{mA}, V_{CO}$ $V_{IL} = 0.8 \text{V}$	c = 4.5V,	3.6 3.5	-	-	V
Input Resistance	Rı	V _{CC} = 5.0V		3.0 3.0	-	7.0 7.0	kΩ
Transmitter Parameters							
Output Low Voltage	V _{OL}	$V_{CC} = 4.5V, V_{IH} = 2.0V,$ $R_{I} = 3.0k\Omega$		-	-	-5.2 -5.0	V
Output High Voltage	V _{OH}	$V_{CC} = 4.5V, V_{IL} = R_L = 3.0k\Omega$	0.8V,	5.2 5.0	-	-	V
Input Low Current	I _{IL}	V _{CC} = 5.5V, V _{IL} = 0V		-	-	-1.0 -10.0	μΑ
Input High Current	I _{IH}	V _{CC} = 5.5V, V _{IH} = V _{CC}		-	-	1.0 10.0	μΑ
Speed Of Output Front Charge	SR	$V_{CC} = 5.0V, C_L = R_L = 3.0 - 7.0k\Omega$	50 - 1000pF,	3.0 2.7	-	30 27	V/µs
Output Resistance	Ro	$V_{CC} = V + = V - = 0$ $V_{O} = \pm 2V$)V	350 300	-	-	Ω
		V _{CC} = 5.5V	V _I = V _{CC}	-	-	-50 -60	- mA
Short Circuit Output Current	I _{SC}	$V_0 = 0V$	V _I = 0	-	-	50 60	
Speed Of Information Transmission	ST	V_{CC} = 4.5V, C_L = 1000pF, R_L = 3.0k Ω , t_W = 7 μ s (for extreme, t_W = 8 μ s)		140 120	-	-	kbit/s
Dynamic Parameters							
Signal Propagation Delay Time When Switching On (Off)	t _{PHLR} (t _{PLHR})	$V_{CC} = 4.5V, C_L = 150pF,$ $V_{IL} = 0V, V_{IH} = 3.0V,$ $t_{LH} = t_{HL} \le 10ns$		-	-	9.7 10.0	μs
Signal Propagation Delay Time When Switching On (Off)	t _{PHLT} (t _{PLHT})	$V_{CC} = 4.5V, C_L = 2500pF,$ $V_{IL} = 0V, V_{IH} = 3.0V,$ $R_L = 3k\Omega, t_{LH} = t_{HL} \le 10ns$		-	-	5.0 6.0	μs



Capacitance

Symbol	Parameter	V _{cc} ,	Rate	Unit
C _{IN}	Input capacitance	5.0	9.0	pF
C_{PD}	Dynamic capacitance		90	

Timing diagram when measuring IC dynamic parameters

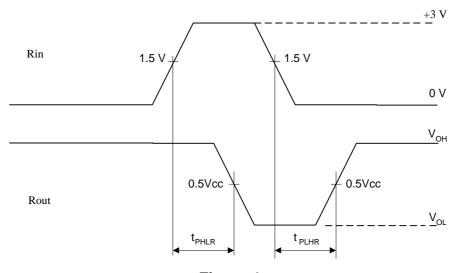
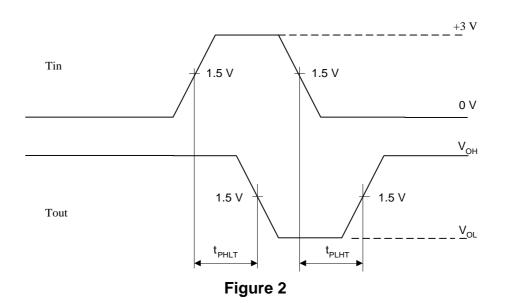
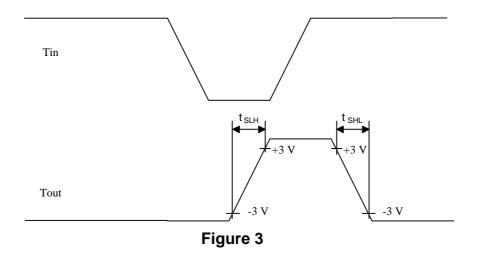


Figure 1





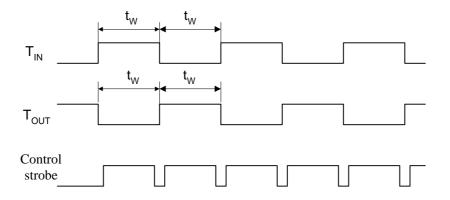
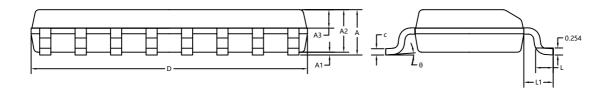
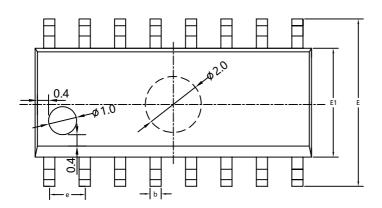


Figure 4

PACKAGE OUTLINE DIMENSIONS

SOP-16





SYMBOL	MILLIMETER			
STWIDOL	MIN	NOM	MAX	
А	1.50	1.60	1.70	
A1	0.10	0.15	0.25	
A2	1.40	1.45	1.50	
А3	0.60	0.65	0.70	
b	0.30	0.40	0.50	
С	0.15	0.20	0.25	
D	9.80	9.90	10.00	
E	5.80	6.00	6.20	
E1	3.85	3.90	3.95	
e	1.27BSC			
L	0.50 0.60 0.70		0.70	
L1	1.05BSC			
θ	0°	4°	8°	



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