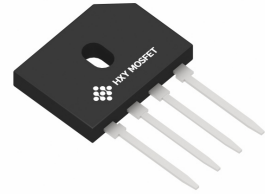




Features

- Ideal for P.C. Board mounting
- High surge current capability
- This series is UL listed under the Recognized Component Index, file number E142814
- The plastic material used carries Underwriters Laboratory flammability recognition 94V-0
- High temperature soldering guaranteed 265°C /10 seconds at 5 lbs (2.3kg) tension



GBU

Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
GBU1500-GBU1510	GBU	GBU15xx	500

xx: From 00-10

Maximum Ratings (Ta=25°C unless otherwise noted)

Parameter	Symbol	GBU 1500	GBU 1501	GBU 1502	GBU 1504	GBU 1506	GBU 1508	GBU 1510	Unit
Maximum repetitive peak reverse voltage	VRRM	50	100	200	400	600	800	1000	V
Maximum RMS bridge input voltage	VRMS	35	70	140	280	420	560	700	v
Maximum DC blocking voltage	VDC	50	100	200	400	600	800	1000	v
Maximum average forward rectified output current at TA=100 °C	IF(AV)	15							A
Peak forward surge current single sine-wave superimposed on rated load (JEDEC Method)	IFSM	220							A
Rating for fusing (t<8.3ms)	I ² t	200							A ² sec
Typical thermal resistance per element(1)	ReJA	2.2							°C/W
Operating junction and storage temperature range	TJ, TSTG	-55 to + 150							°C

Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified. Resistive or Inductive load, 60Hz.
For Capacitive load derate by 20 %.

Parameter	Symbol	GBU 1500	GBU 1501	GBU 1502	GBU 1504	GBU 1506	GBU 1508	GBU 1510	Unit
Maximum instantaneous forward voltage drop per leg at 10.0A	VF	1.05							V
Maximum DC reverse current at rated TA =25°C DC blocking voltage per element TA =125°C	IR	5.0 500							μA

Notes: (1)Thermal resistance from Junction to Ambient on P.C.board mounting.



Rating and Characteristic Curves (TA=25°C Unless otherwise noted)

Fig. 1 Derating Curve for Output Rectified Current

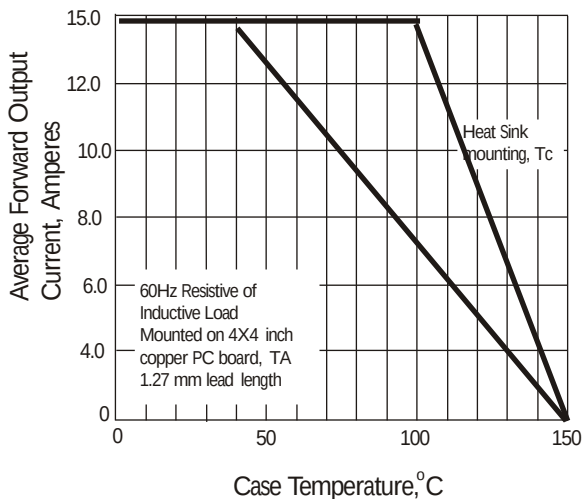


Fig. 2 Maximum Non-repetitive Peak Forward Surge Current

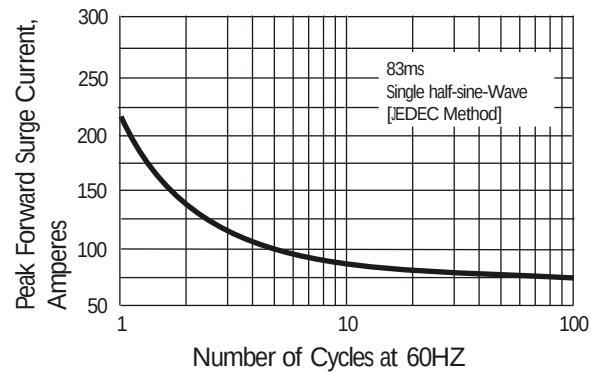


Fig. 3 Typical Instantaneous Forward Characteristics

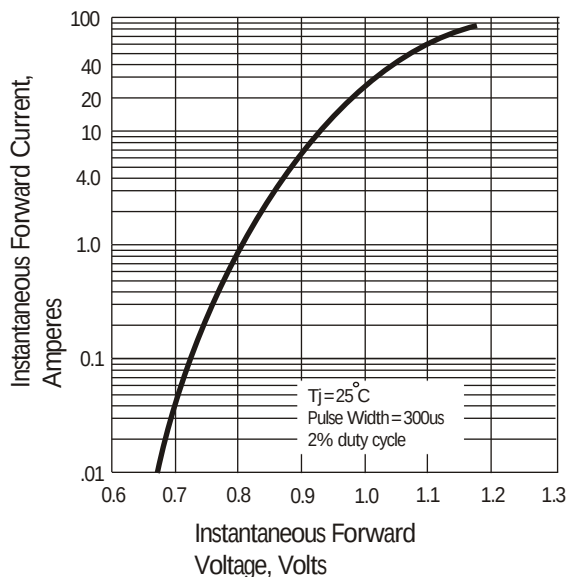


Fig. 4 Typical Reverse Characteristics

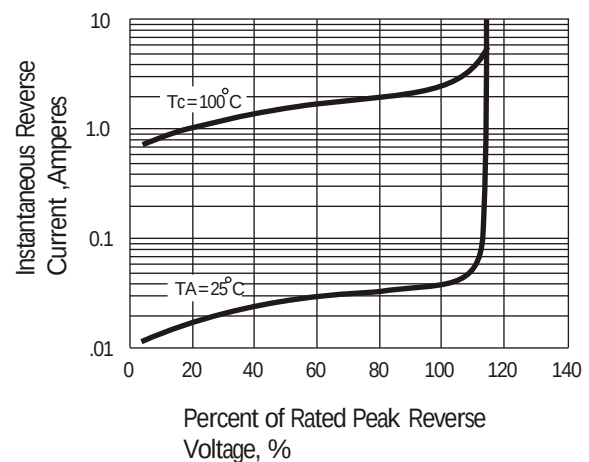
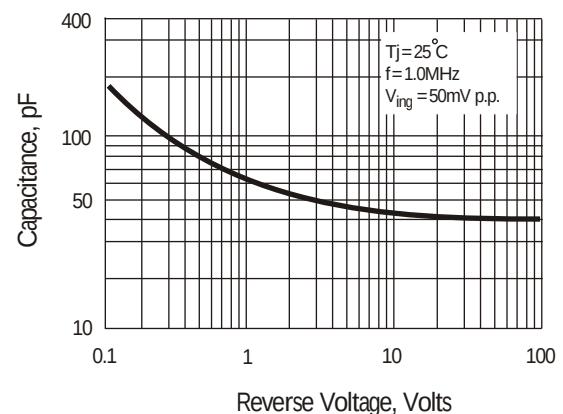


Fig. 5 Typical Junction Capacitance





Package Outline Dimensions

[illegible]



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