

Descriptions

The NLAS3257AMU3TCG is a single SPDT low on-resistance analog switch. It can operate from a single 1.5V to 5.5V power supply. The device offers low ON-state resistance and excellent ON-state resistance matching with break-before -make feature to prevent signal distortion during the transferring of a signal from one channel to another. The device is capable of truly isolation. Even when A overrides VCC, very little current will flow back to the supply.

Order Information

Package		Part Number	Top-Side Marking		
DFN1x1(UDFN-6(1x1))	Tape and Reel	NLAS3257AMU3TCG	A52 TYW		

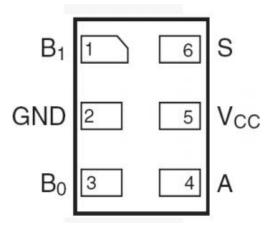
Features

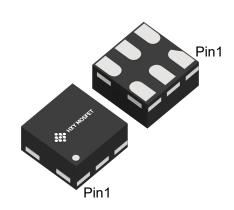
- Pin-to-Pin NLAS3357AMU3TCG, SN74LVC1G3157DRL, DFN1X1(UDFN-6(1x1))Package
- Low On-resistance, Ron=1.5Ω when A=5V
- 1.8V Logic Compatible Control Pin
- A Overrides VCC to Achieve True Isolation Even When Supply Is Dead
- High Off-Isolation: -100dB @ 100KHz
- Low Channel-to-Channel Crosstalk: -97dB @ 100KHz
- High Bandwidth (-3dB @700MHz) Suitable For USB2.0 High-Speed Routing
- Low Quiescent Current (<2uA) With Very Wide Supply Range (1.5V ~ 5.5V)

Applications

- Audio, Video, UART, USB2.0 Signal and Supply Routing
- Cell phones and TWS headset

Pin Configuration







Functions and Pin Configuration

Pin Number	Symbol	Descriptions		
1	B1	Analog/Digital Signal Port (Normally open)		
2	GND	Ground		
3	В0	Analog/Digital Signal Port (Normally closed)		
4	A	Common Signal Port		
5	VCC	Single Power Supply		
6	S	Logic Input Control		

Function Descriptions

Logic Input	Function
S=0	B0=A
S=1	B1=A

Absolute Maximum Ratings (1)

Parameter	Symbol	Value	Unit	
Supply Voltage	Vcc	-0.3 ~ 6.5	V	
Control Input Voltage	Vs	-0.3 ~ 6.5	V	
Continuous Current Through A, B0, B1		±100	mA	
Peak Current Through A, B0, B1 (pulsed at 1ms 50% duty cycle)		±200	mA	
Storage Temperature Range	T _{STG}	-55 ~ 150	°C	
Junction Temperature under Bias	TJ	150	°C	
Lead Temperature (Soldering, 10 seconds)	TL	260	°C	
Thermal resistance	RөJA	350	°C/W	

Recommend operating ratings (2)

Parameter	Symbol	Value	Unit
Supply Voltage Operating	Vcc	1.5 ~ 5.5	V
Control Input Voltage	Vs	-0.3 ~ 5.5	V
Input Signal Voltage	V _A	-0.3 ~ 5.5	V
Operating Temperature	T _A	-40 ~ 85	°C

Note:

"Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only
rating and operation of the device at these or any other conditions beyond those indicated in the
operational sections of this specification is not implied.



DC Electronics Characteristics (Ta=25°C, VCC=3.3V, unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
la and la sia biah laval	\ /	VCC: 3.3 ~ 5.5V	1.6			V
Input logic high level	V_{IH}	VCC: 1.5 ~ 3.3V	1.4			V
Input logic low level	.,,	VCC: 3.3 ~ 5.5V			0.6	V
input logic low level	V_{IL}	VCC: 1.5 ~ 3.3V			0.4	V
Supply quiescent current	Icc	$I_A=0$, $V_S=0$ or $V_S=VCC$			1.0	uA
Increase in I _{CC} per input	l	I _A =0, VCC=4.5V			1.0	uA
increase in icc per input	Ісст	V _S >1.8 or V _S <0.5				
Off state leakage from	I _A	$V_A = 5.5V$, $V_{B0(or\ B1)} = 0V$			±2.0	uA
A to B0 (or B1)	IA	VA - 3.3V , VB0(or B1) - 0V			12.0	uA
	R _{ON1}	$V_A=0 \sim 0.5V$, $I_A=30mA$		3.0	3.5	Ω
On-Resistance	R _{ON2}	V _A =0.5 ~ 2.0V, I _A =30mA		3.6	3.9	Ω
On-ivesistance	R _{ON3}	V _A =2.0 ~ 4.0V, I _A =30mA		2.5	3.5	Ω
	R_{ON4}	V_A =4.0 ~ 5.5V, I_A =30mA		1.5	1.8	Ω
	R_{FLAT1}	$V_A=0 \sim 0.5V$, $I_A=30mA$		0.7		Ω
On Besistance Flatness	R _{FLAT2}	V _A =0.5 ~ 2.0V, I _A =30mA		0.5		Ω
On-Resistance Flatness	R _{FLAT3}	V _A =2.0 ~ 4.0V, I _A =30mA		1.6		Ω
	R _{FLAT4}	V _A =4.0 ~ 5.5V, I _A =30mA		0.3		Ω
On-Resistance	۸ D	V _A =0~5.5V, I _A =30mA,		0.1	0.2	Ω
Matching Between Channels	ΔR_{ON}	VA-0~3.5V, IA-30IIIA,		0.1	0.2	3.2

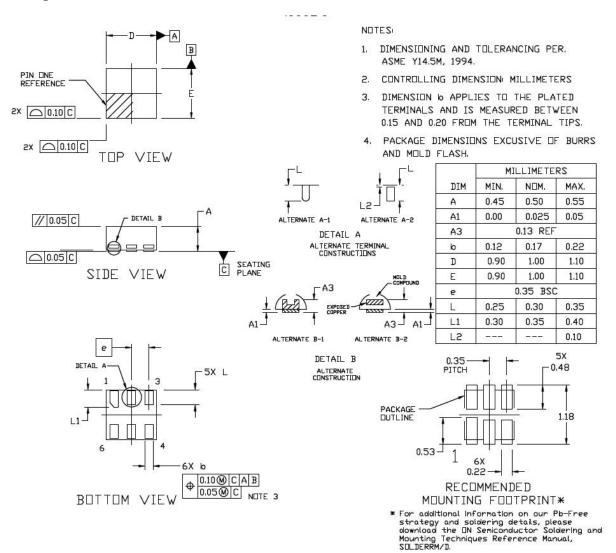
AC Electronics Characteristics (Ta=25°C, VCC=3.3V, unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Тур.	Max	Unit
Turn-On Time	Ton	V _A =1.5V, C _L =35pF, R _L =50Ω		200		ns
Turn-Off Time	T _{OFF}	V _A =1.5V, C _L =35pF, R _L =50Ω		200		ns
Break-Before-Make time	T _{BBM}	V_A =1.5V, C_L =35pF, R_L =50 Ω		500		ns
-3dB Bandwidth	BW	R _L =50Ω, C _L =0pF		700		MHz
Off isolation	OIRR	F=1KHz, R _L =50Ω		-81		dB
		F=10KHz, R _L =50Ω		-80		dB
Crosstalk	Xtalk	F=1KHz, R _L =50Ω		-83		dB
		F=10KHz, R _L =50Ω		-82		dB
Total Harmonic Distortion	THD	F=20Hz to 20KHz		-80		dB
		V_A =600m V p-p @ R_L =32 Ω ,				ub

Capacitance (Ta=25°C unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Off capacitance	C _{OFF}	F=100KHz, VCC=3.3		5		pF
On capacitance	Con	F=100KHz, VCC=3.3		7		pF

Package Outline Dimensions



NLAS3257AMU3TCG SPDT Low On-resistance Analog Switch

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