

### **Description**

The SUD35N10-26P-E3 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

# D street S

TO-252-2L (TO-252AA)

### **General Features**

 $V_{DS} = 100V I_{D} = 50 A$ 

 $R_{DS(ON)}$  < 28m $\Omega$  @  $V_{GS}$ =10V

### **Application**

Battery protection

Load switch

Uninterruptible power supply

# PIN1 G PIN3 S

N-Channel MOSFET

### **Package Marking and Ordering Information**

Product ID	Pack	Brand	Qty(PCS)
SUD35N10-26P-E3	TO-252-2L(TO-252AA)	HXY MOSFET	2500

### Absolute Maximum Ratings (T<sub>C</sub>=25°Cunless otherwise noted)

Symbol	Parameter	Rating	Units	
VDS	Drain-Source Voltage	100	V	
Vgs	Gate-Source Voltage	±20	V	
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	50	А	
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	30	Α	
Ірм	Pulsed Drain Current <sup>2</sup>	150	А	
EAS	Single Pulse Avalanche Energy <sup>3</sup>	62.6	mJ	
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation⁴	73	W	
Тѕтс	Storage Temperature Range	-55 to 150	°C	
TJ	Operating Junction Temperature Range	-55 to 150	°C	
R <sub>θ</sub> JA	Thermal Resistance Junction-ambient <sup>1</sup>	62	°C/W	
R <sub>θ</sub> JC	Thermal Resistance Junction-Case <sup>1</sup>	2.0	°C/W	



## Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	100	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V,	-	-	1.0	μΑ
I <sub>GSS</sub>	Gate to Body Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V	-	-	±100	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	2.0	2.7	4.0	V
Б	Static Drain-Source on-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	-	18	28	mΩ
$R_{DS(on)}$	note2	V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A	-	22	32	mΩ
C <sub>iss</sub>	Input Capacitance	\/ -25\/ \/ -0\/	-	3727	-	pF
Coss		V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1.0MHz		180	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	1 I=1.0IVIDZ	-	148	-	pF
Qg	Total Gate Charge	\/ -20\/ L -45A	-	40	-	nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =30V, I <sub>D</sub> =15A,	-	6.2	-	nC
$Q_{gd}$	Gate-Drain("Miller") Charge	V <sub>GS</sub> =10V	-	28	-	nC
t <sub>d(on)</sub>	Turn-on Delay Time		-	22	-	ns
t <sub>r</sub>	Turn-on Rise Time	V <sub>DS</sub> =30V, I <sub>D</sub> =15A, R <sub>G</sub> =1.8Ω, V <sub>GS</sub> =10V	-	182	-	ns
t <sub>d(off)</sub>	Turn-off Delay Time		-	80	-	ns
t <sub>f</sub>	Turn-off Fall Time		-	142	-	ns
ls	Maximum Continuous Drain to Source Diode Forward Current		-	-	50	Α
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	150	Α
$V_{SD}$	Drain to Source Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =30A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time		-	71	-	ns
Qrr	Body Diode Reverse Recovery Charge	IF=30A,dI/dt=100A/μs	1	145	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

- 2. EAS condition : T\_J=25  $^{\circ}\text{C}$  , V\_DD=50V, V\_G=10V, L=0.5mH, Rg=25  $\Omega$ , I\_AS=14.5A
- 3. Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%



### **Typical Performance Characteristics**

Figure1: Output Characteristics

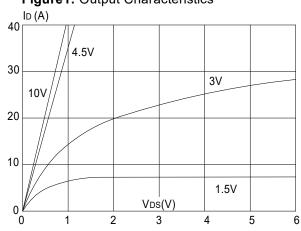


Figure 2: Typical Transfer Characteristics

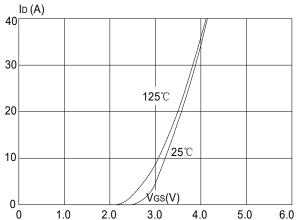


Figure 3:On-resistance vs. Drain Current

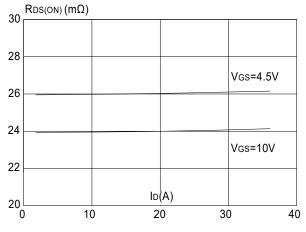


Figure 4: Body Diode Characteristics

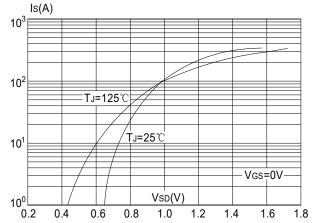


Figure 5: Gate Charge Characteristics

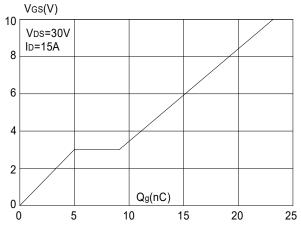
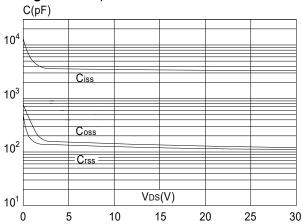


Figure 6: Capacitance Characteristics





**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature

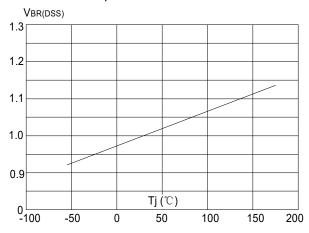
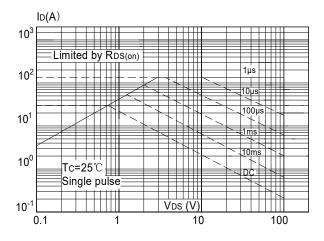
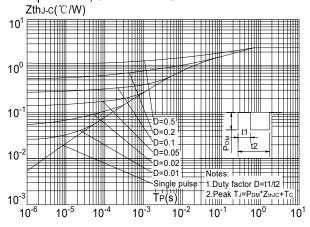


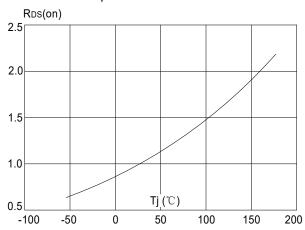
Figure 9: Maximum Safe Operating Area



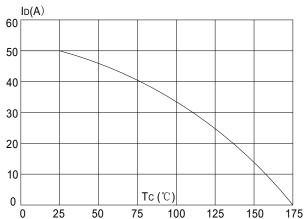
**Figure.11:** Maximum Effective Transient Thermal Impedance, Junction-to-Case



**Figure 8:** Normalized on Resistance vs. Junction Temperature

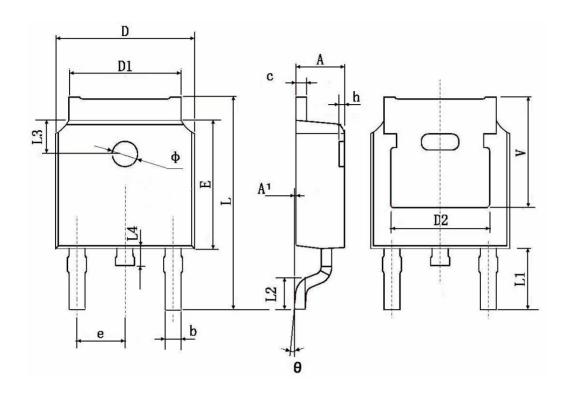


**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature





# TO-252-2L(TO-252AA) Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min.	Max.	Min.	Max.	
Α	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	0.483 TYP.		0.190 TYP.		
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.900 TYP.		0.114 TYP.		
L2	1.400	1.700	0.055	0.067	
L3	1.600	TYP. 0.063 TYP.		3 TYP.	
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.350	) TYP.	0.211 TYP.		



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