



Features

- Wide bandgap SiC MOSFET technology
- Low On-Resistance with High Blocking Voltage
- Low Capacitances with High-Speed switching
- Low reverse recovery(Qrr)
- Halogen free, RoHs compliant

Benefits

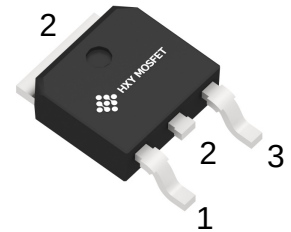
- Reduce switching losses
- Increased system Switching Frequency
- Increased power density
- Reduction of heat sink requirements

Applications

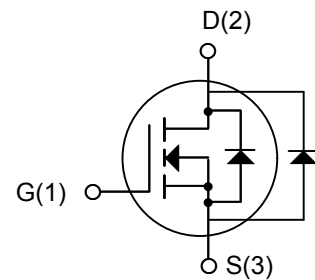
- Switch mode power supplies
- Renewable energy
- On Board Charger
- High Voltage DC/DC Converters



Ordering Part Number	Package	Brand
STDLED656	TO-252-2L	HXY MOSFET



TO-252-2L



Maximum Ratings (T_C = 25 °C unless otherwise specified)

Symbol	Parameter	Test conditions	Value	Unit	Note
V _{DSmax}	Drain-Source Voltage	V _{GS} = 0V, I _D = 100μA	650	V	
V _{GSmax}	Gate-Source voltage	AC (f > 1 Hz)	-8/+19	V	
V _{GSop}	Recommend Gate-Source Voltage	Static	-4/+15	V	
EAS	Single pulse avalanche energy	V _{DS} =650V, V _{DD} =50V, V _{GS} =15V, L=1mH, T _C =25°C	18	mJ	
I _D	Continuous Drain current	V _{GS} = 15V, T _C = 25°C	5.3	A	Fig. 14
		V _{GS} = 15V, T _C = 100°C	3.8		
I _{D,pulse}	Pulsed Drain Current	Pulse with t _p limited by T _{jmax}	10	A	
P _D	Power Dissipation	T _C = 25°C, T _j = 175°C	32	W	Fig. 16
T _j	Operating junction temperature		-55~175	°C	
T _{stg}	Storage temperature		-55~175	°C	



Thermal Characteristics

Symbol	Parameter	Value			Unit	Note
		Min.	Typ.	Max.		
$R_{th(jc)}$	Thermal resistance from Junction to Case		4.76		K/W	Fig. 15
$R_{th(ja)}$	Thermal resistance from Junction to Ambient		40		K/W	

Electrical Characteristics (T_c = 25°C unless other wise specified)

Static Characteristics

Symbol	Parameter	Test conditions	Value			Unit	Note
			Min.	Typ.	Max.		
$V_{(BR)DSS}$	Drain-Source Breakdown voltage	$V_{GS} = 0V, I_D = 100\mu A$	650			V	
$V_{GS(th)}$	Gate Threshold voltage	$V_{GS} = V_{DS}, I_D = 0.35mA$		2.6		V	Fig. 9
		$V_{GS} = V_{DS}, I_D = 0.35mA, T_j = 175^\circ C$		1.8			
I_{GSS}	Gate-Source Leakage current	$V_{GS} = 15V, V_{DS} = 0V$			250	nA	
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 650V, V_{GS} = 0V, T_j = 25^\circ C$		1	50	μA	
$R_{DS(on)}$	Drain-Source On-state Resistance	$V_{GS} = 15V, I_D = 2A$ $V_{GS} = 18V, I_D = 2A$		1080 820	1600	m Ω	Fig. 3, 4, 5
		$V_{GS} = 15V, I_D = 2A, T_j = 175^\circ C$ $V_{GS} = 18V, I_D = 2A, T_j = 175^\circ C$		1220 1130			
g_{fs}	Transconductance	$V_{DS} = 15V, I_D = 2A$		0.92		S	Fig. 6
		$V_{DS} = 15V, I_D = 2A, T_j = 175^\circ C$		0.27			



Gate Charge Characteristics

Symbol	Parameter	Test conditions	Value			Unit	Note
			Min.	Typ.	Max.		
Q_{GS}	Gate to Source Charge	$V_{DS} = 400V$ $I_D = 2A$ $V_{GS} = -4V/15V$		1.5		nC	Fig. 10
Q_{GD}	Gate to Drain Charge			3.6			
Q_G	Total Gate Charge			5.9			

AC Characteristics

Symbol	Parameter	Test conditions	Value			Unit	Note
			Min.	Typ.	Max.		
C_{iss}	Input Capacitance	$V_{GS} = 0V, V_{DS} = 600V$ $f = 1\text{ MHz}$ $V_{AC} = 25mV$		68		pF	Fig. 13
C_{oss}	Output Capacitance			12		pF	
C_{rss}	Reverse Transfer Capacitance			1.8		pF	
$R_{G(int)}$	Internal Gate Resistance	$f = 1\text{ MHz}, V_{AC} = 25mV$		10		Ω	



Reverse Diode Characteristics

Symbol	Parameter	Test conditions	Value			Unit	Note
			Min.	Typ.	Max.		
V_{SD}	Diode Forward Voltage	$V_{GS} = -4V, I_{SD} = 1A$		4.9		V	Fig. 7,8
		$V_{GS} = -4V, I_{SD} = 1A, T_j = 175^{\circ}C$		4.0			
I_S	Continuous Diode Forward Current	$V_{GS} = -4V, T_C = 25^{\circ}C$		4.7		A	
$I_{S, pulse}$	Diode pulse Current	$V_{GS} = -4V$, pulse width t_p limited by T_{jmax}		10		A	
t_{rr}	Reverse Recovery Time	$V_{GS} = -4V, I_{SD} = 2A, V_R = 400V$ $diff/dt = 1000A/us$		33		nS	
Q_{rr}	Reverse Recovery Charge			23		nC	
I_{rrm}	Peak Reverse Recovery Current			1.7		A	

Switching Characteristics

Symbol	Parameter	Test conditions	Value			Unit	Note
			Min.	Typ.	Max.		
$t_{d(on)}$	Turn-On Delay Time	$V_{DS} = 400V, V_{GS} = -4/+15V$ $I_D = 2A, R_{G(ext)} = 5\Omega$ $L = 833\mu H$		1		nS	Fig.20
t_r	Rise Time			6		nS	
$t_{d(off)}$	Turn-Off Delay Time			3		nS	
t_f	Fall Time			19		nS	
E_{on}	Turn-On Energy			6		μJ	Fig.19
E_{off}	Turn-Off Energy			7		μJ	
E_{tot}	Total switching energy			13		μJ	



Typical Performance

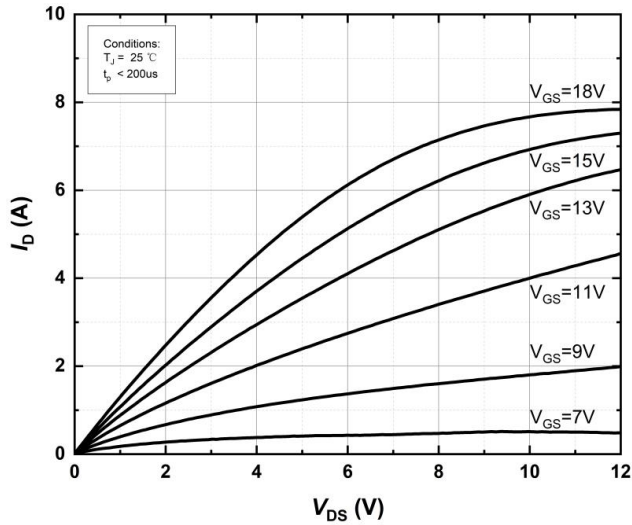


Figure 1. Output characteristics at $T_j=25^\circ\text{C}$

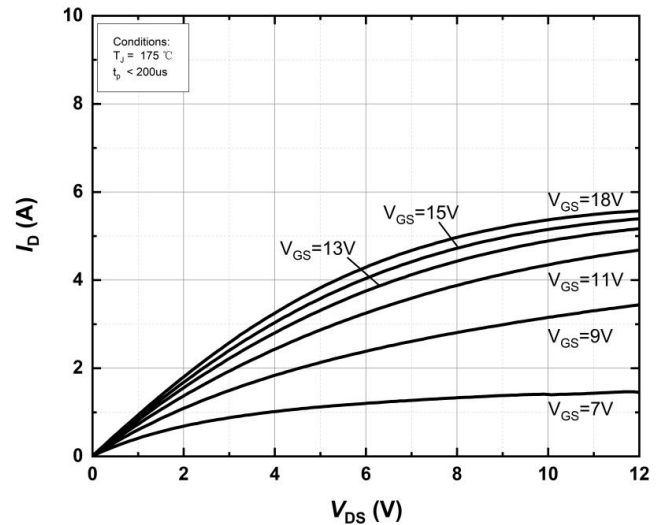


Figure 2. Output characteristics at $T_j=175^\circ\text{C}$

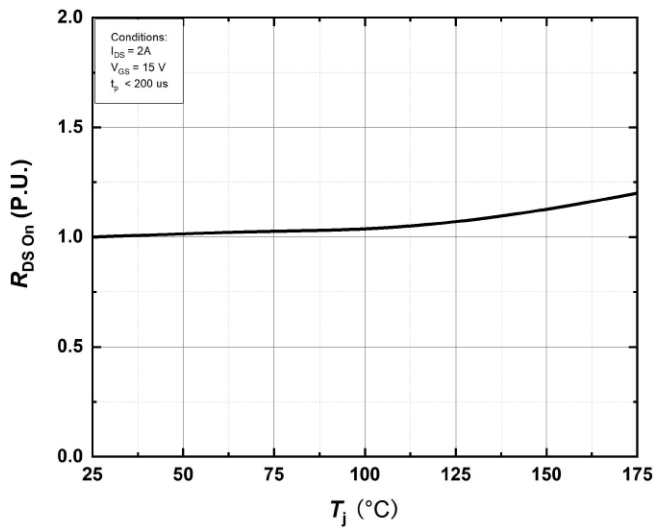


Figure 3. Normalized On-Resistance vs. Temperature

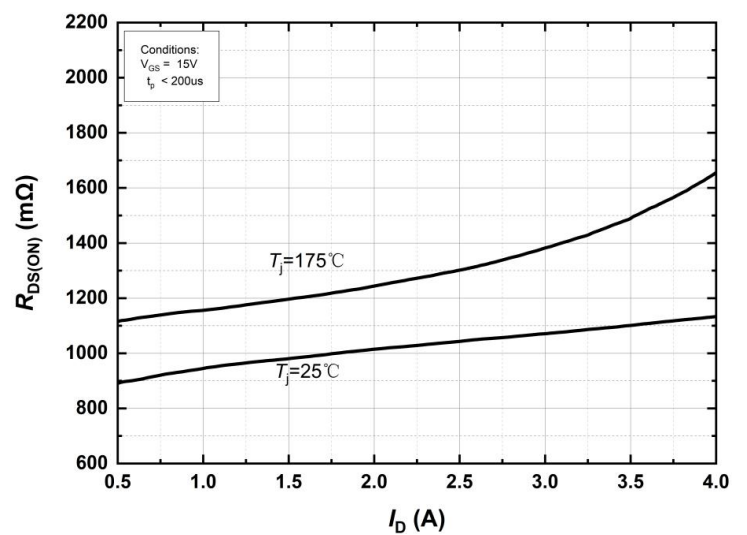


Figure 4. On-Resistance vs. Drain current for Various Temperature

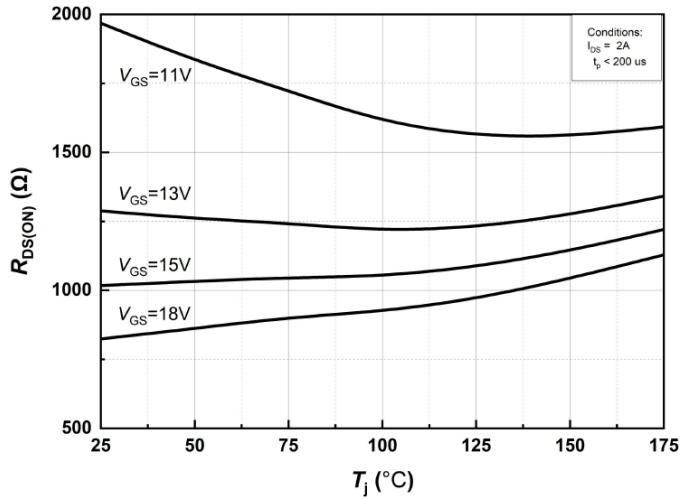


Figure 5. On-Resistance vs. Temperature for Various Gate Voltage

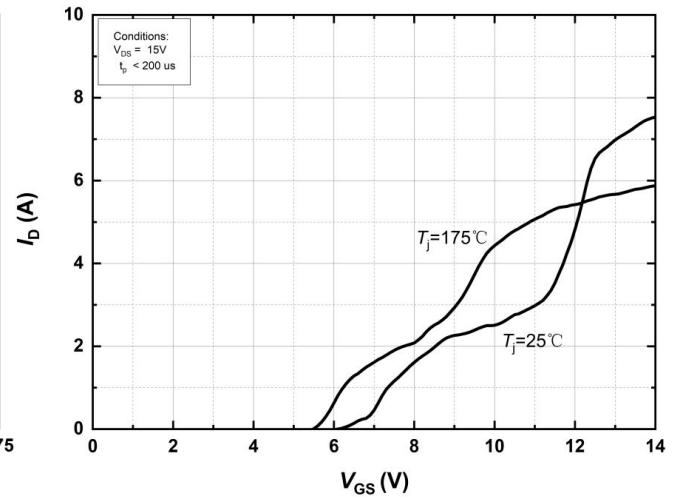


Figure 6. Transfer Characteristics for Various Junction Temperatures

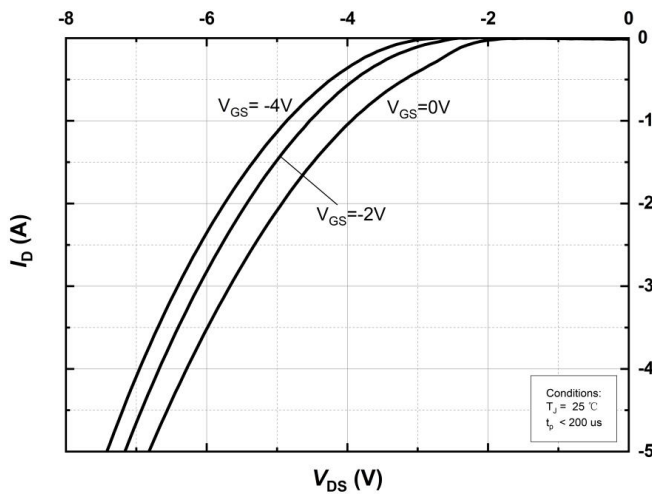


Figure 7. Body Diode Characteristics at $T_j=25^{\circ}\text{C}$

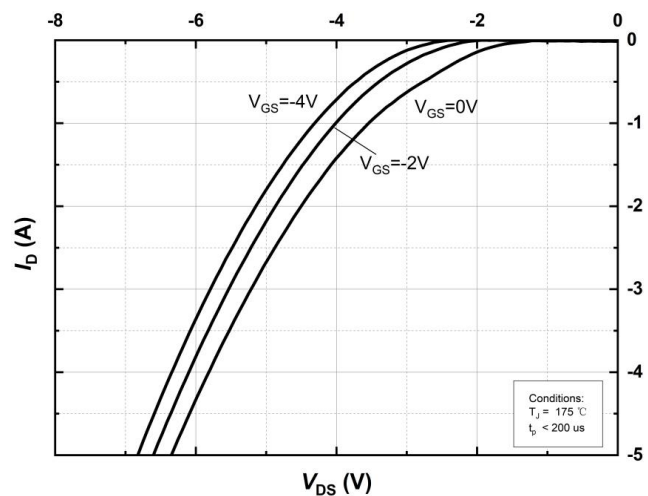


Figure 8. Body Diode Characteristics at $T_j=175^{\circ}\text{C}$

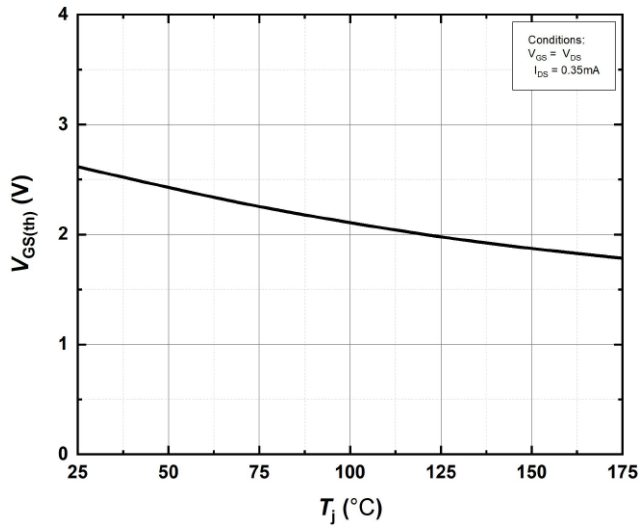


Figure 9. Threshold Voltage vs. Temperature

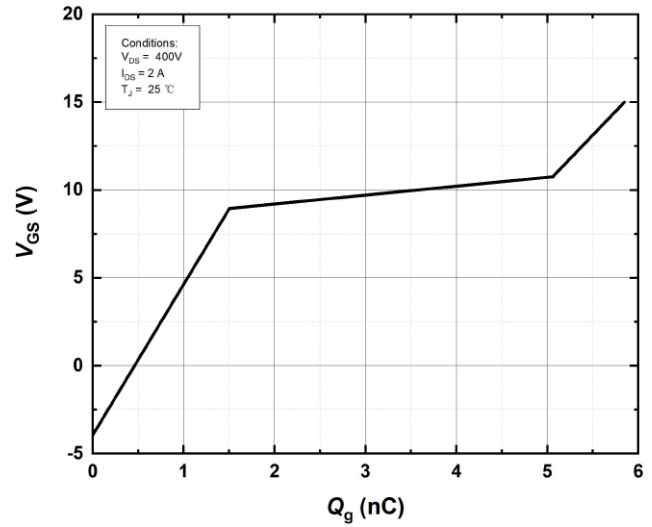


Figure 10 Gate Charge Characteristics

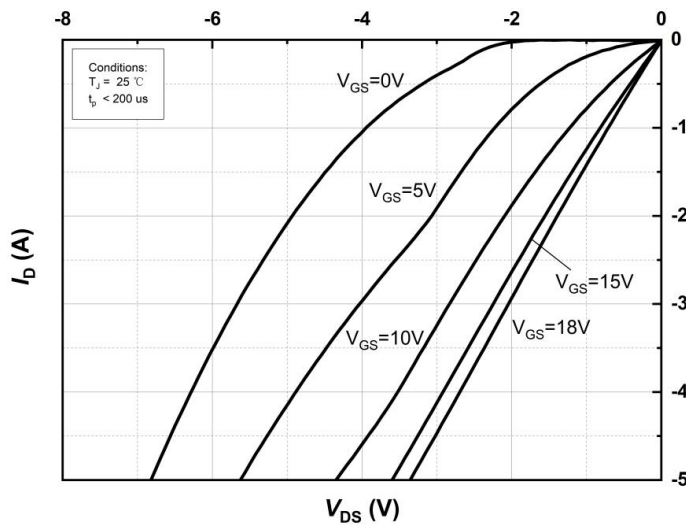


Figure 11. 3rd Quadrant Characteristic at $T_j=25^\circ C$

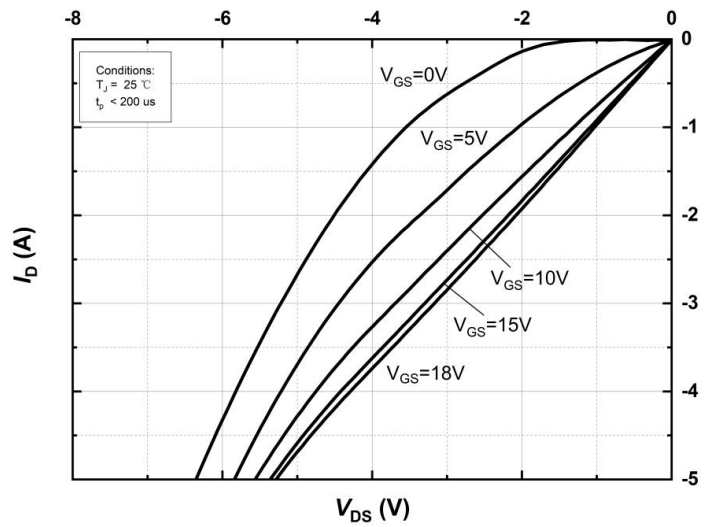


Figure 12. 3rd Quadrant Characteristic at $T_j=175^\circ C$

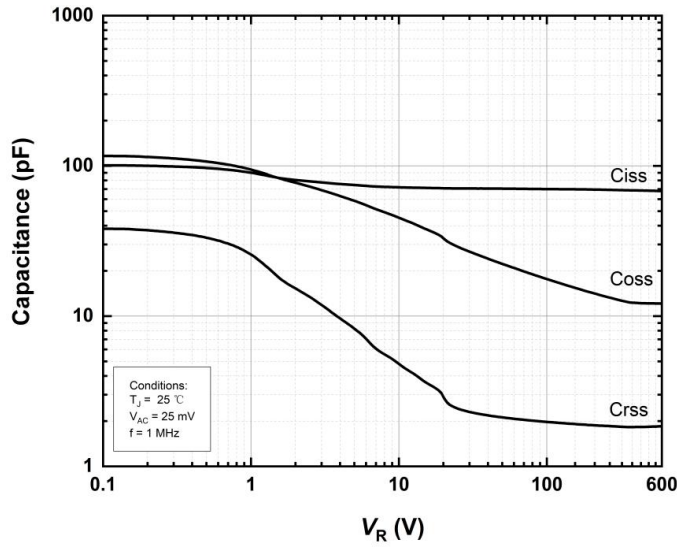


Figure 13. Capacitances vs. Drain-Source Voltage (0 – 600V)

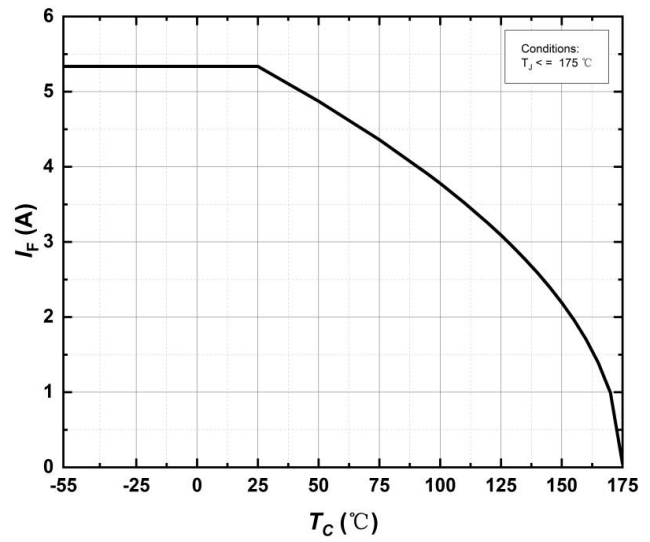


Figure 14. Continuous Drain Current Derating vs Case Temperature

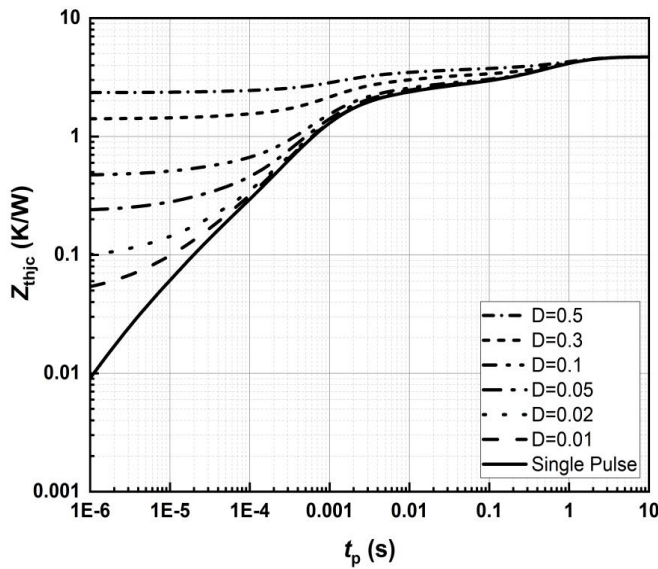


Figure 15. Transient Thermal Impedance (Junction – Case)

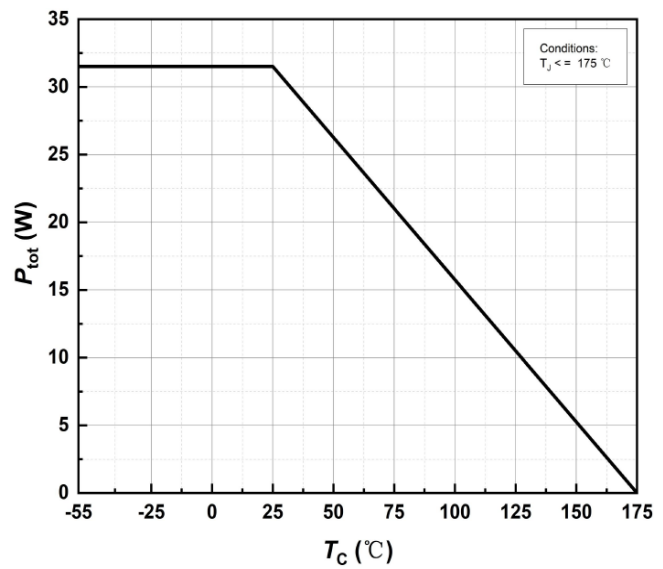


Figure 16. Maximum Power Dissipation Derating vs. Case Temperature

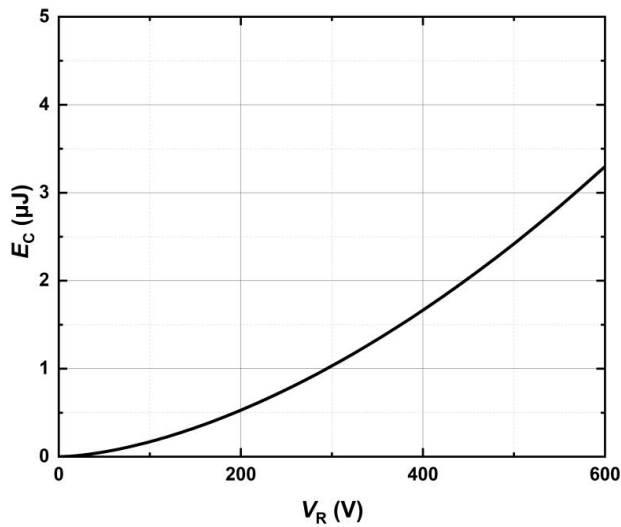


Figure 17. Output Capacitor Stored Energy

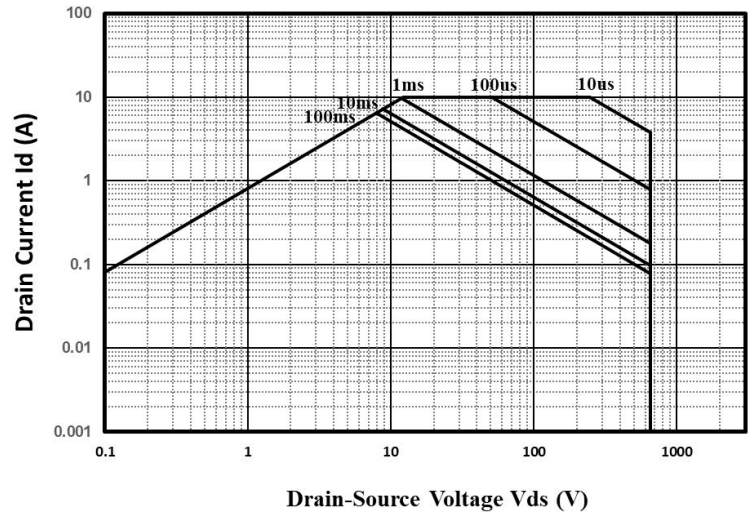


Figure 18. Safe Operating Area

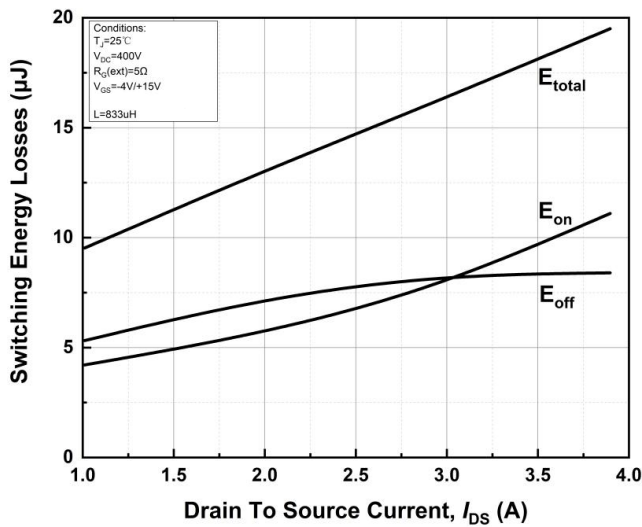


Figure 19. Clamped Inductive Switching Energy vs. Drain Current($V_{DD} = 400\text{V}$)

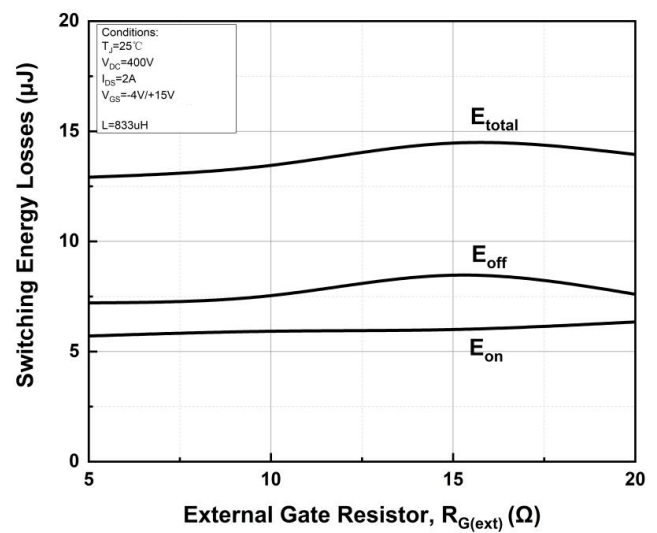


Figure 20. Clamped Inductive Switching Energy vs. $R_{G(ext)}$

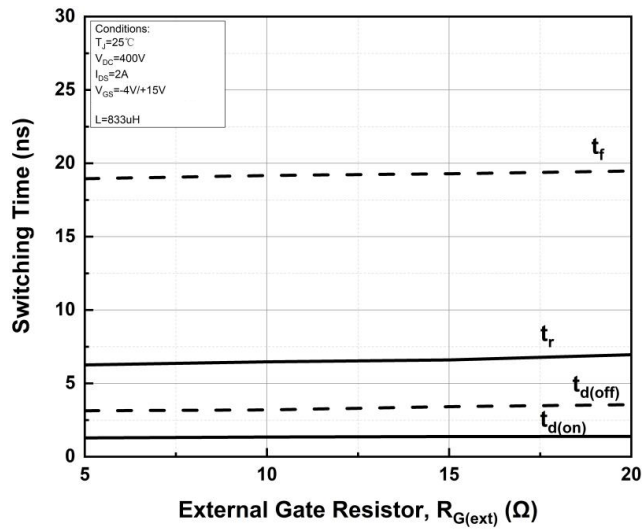
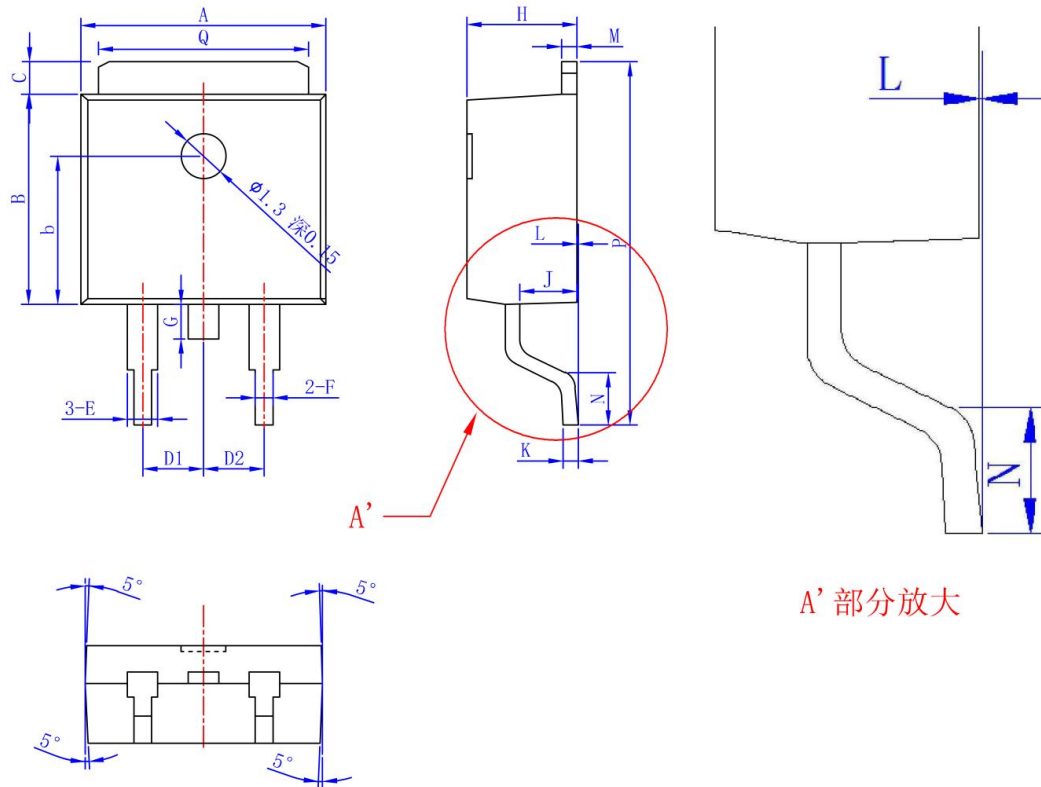


Figure 21. Switching Times vs. $R_{G(\text{ext})}$



Package Dimensions

Package TO-252-2L



A' 部分放大

SYMBOL	MM		
	MIN	NOM	MAX
*A	6.30	6.55	6.90
*B	5.90	6.10	6.30
b	4.10	4.30	4.50
C	0.90	1.00	1.10
*D1		2.29BSC	
*D2		2.29BSC	
*E	0.61	0.76	0.91
*F	0.50	0.60	0.70
G	0.60	0.80	1.00
*H	2.10	2.30	2.50
*J	0.90	1.00	1.10
*K	0.40	0.50	0.60
*L	0.00	0.05	0.127
*M	0.45	0.50	0.55
*N	1.39		1.77
*P	9.60	9.90	10.30
Q	5.10	5.30	5.50
带*为必须检验尺寸			



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