

# **Description**

The SPD30P06PG uses advanced trench technology

to provide excellent  $R_{DS(ON)}$ , low gate charge and

operation with gate voltages as low as 4.5V. This

device is suitable for use as a

Battery protection or in other Switching application.



TO-252-2L

## **General Features**

 $V_{DS} = -60V I_{D} = -15 A$ 

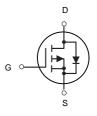
 $R_{DS(ON)}$  <  $82m\Omega$  @  $V_{GS}$ =10V

## **Application**

**Battery protection** 

Load switch

Uninterruptible power supply



P-Channel MOSFET

**Package Marking and Ordering Information** 

Product ID	Pack	Brand	Qty(PCS)
SPD30P06PG	TO-252-2L	HXY MOSFET	2500

## Absolute Maximum Ratings (T<sub>A</sub>=25<sup>°</sup>Cunless otherwise noted)

Symbol	Parameter	Rating	Units	
V <sub>DS</sub>	Drain-Source Voltage	-60	V	
Vgs	Gate-Source Voltage	±20	V	
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ -10V <sup>1</sup>	-15	Α	
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ -10V <sup>1</sup>	-11	Α	
Ірм	Pulsed Drain Current <sup>2</sup>	-36	А	
EAS	Single Pulse Avalanche Energy <sup>3</sup>	35.4	mJ	
las	Avalanche Current	-26.6	А	
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>4</sup>	34.7	W	
P <sub>D</sub> @T <sub>A</sub> =25°C	Total Power Dissipation⁴	2	W	
Тѕтс	Storage Temperature Range	-55 to 150	°C	
TJ	Operating Junction Temperature Range	-55 to 150	°C	
R <sub>θ</sub> JA	Thermal Resistance Junction-Ambient <sup>1</sup>	62	°C/W	
Rejc	Thermal Resistance Junction-Case <sup>1</sup>	3.6 °C/W		



## Electrical Characteristics (T<sub>A</sub>=25°Cunless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BVpss	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =-250uA	-60			V
△Bvpss /△Tj	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25℃, I <sub>D</sub> =-1mA		-0.03		V/°C
		V <sub>GS</sub> =-10V , I <sub>D</sub> =-12A		70	82	
Rds(on)	Static Drain-Source On-Resistance	V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-8A		85	105	$\text{m}\Omega$
V <sub>GS(th)</sub>	Gate Threshold Voltage		-1.2	1.5	-2.5	V
$\triangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	$V_{GS}=V_{DS}$ , $I_D=-250uA$		4.56		mV/℃
		V <sub>DS</sub> =-48V , V <sub>GS</sub> =0V , T <sub>J</sub> =25℃			1	_
IDSS	Drain-Source Leakage Current	V <sub>DS</sub> =-48V , V <sub>GS</sub> =0V , T <sub>J</sub> =55°C			5	uA
Igss	Gate-Source Leakage Current	V <sub>GS</sub> =±20V , V <sub>DS</sub> =0V			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =-5V , I <sub>D</sub> =-12A		15.4		S
Rg	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		13.5		Ω
Qg	Total Gate Charge (-4.5V)			9.86		
Qgs	Gate-Source Charge	V <sub>DS</sub> =-48V , V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-10A		3.08		nC
Q <sub>gd</sub>	Gate-Drain Charge			2.95		
Td(on)	Turn-On Delay Time			28.8		
Tr	Rise Time	V <sub>DD</sub> =-15V , V <sub>GS</sub> =-10V ,		19.8		
Td(off)	Turn-Off Delay Time	- R <sub>G</sub> =3.3 □, I <sub>D</sub> =-1A		60.8		ns
Tf	Fall Time			7.2		
Ciss	Input Capacitance			1447		
Coss	Output Capacitance	V <sub>DS</sub> =-15V , V <sub>GS</sub> =0V , f=1MHz		97.3		pF
Crss	Reverse Transfer Capacitance			70		
Is	Continuous Source Current <sup>1,5</sup>				-18	Α
Ism	Pulsed Source Current <sup>2,5</sup>	· V <sub>G</sub> =V <sub>D</sub> =0V , Force Current ·			-36	Α
VsD	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =-1A , T <sub>J</sub> =25℃			-1.2	V

#### Note:

<sup>1.</sup> The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.

<sup>2.</sup>The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%

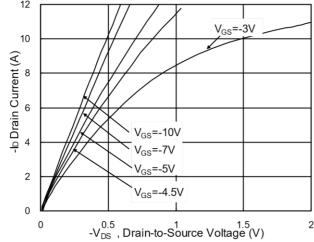
<sup>3.</sup> The EAS data shows Max. rating . The test condition is  $V_{DD}$ =-25V,  $V_{GS}$ =-10V, L=0.1mH,  $I_{AS}$ =-26.6A

<sup>4.</sup> The power dissipation is limited by 150°C junction temperature

<sup>5.</sup> The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.







**Fig.1 Typical Output Characteristics** 

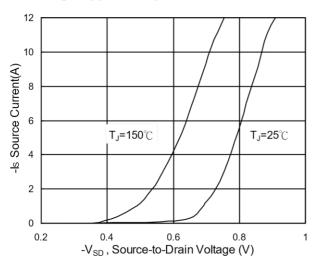


Fig.3 Forward Characteristics of Reverse

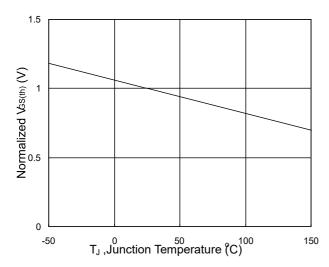


Fig.5 Normalized  $V_{GS(th)}$  v.s  $T_J$ 

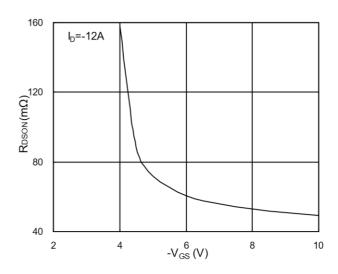


Fig.2 On-Resistance v.s Gate-Source

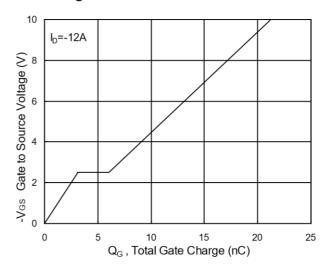


Fig.4 Gate-Charge Characteristics

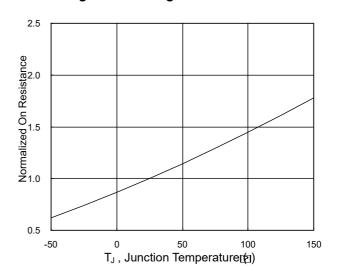
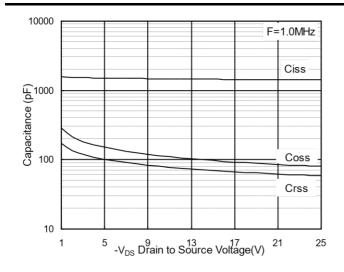


Fig.6 Normalized R<sub>DSON</sub> v.s T<sub>J</sub>





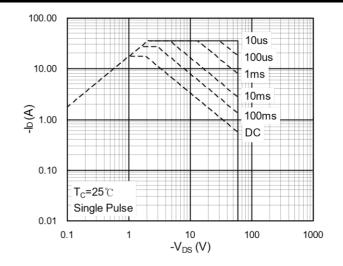


Fig.7 Capacitance

Fig.8 Safe Operating Area

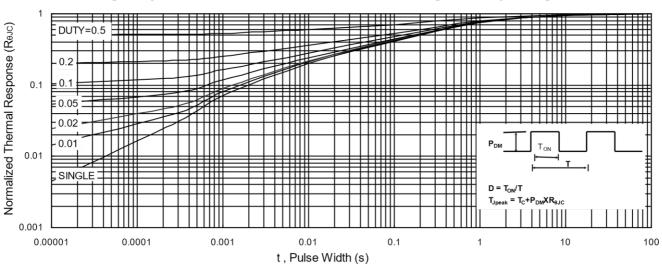


Fig.9 Normalized Maximum Transient Thermal Impedance

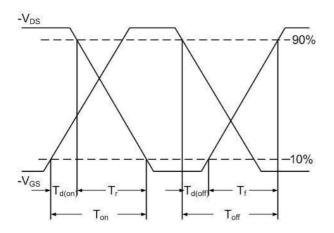


Fig.10 Switching Time Waveform

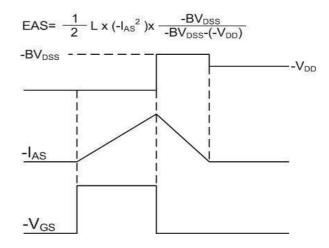
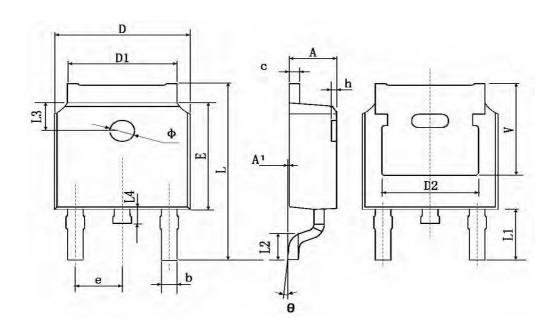


Fig.11 Unclamped Inductive Waveform



# **TO-252-2L Package Information**



Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min.	Max.	Min.	Max.	
Α	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.830 TYP.		0.190 TYP.		
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.900 TYP.		0.114 TYP.		
L2	1.400	1.700	0.055	0.067	
L3		1.600 TYP.		0.063 TYP.	
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.350	0.211 TYP.		TYP.	

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