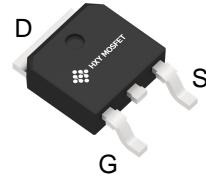




Description

The HXY60P02D uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

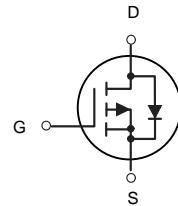


TO-252-2L

General Features

$V_{DS} = -20V$ $I_D = -60A$

$R_{DS(ON)} < 8.5 \text{ m}\Omega$ @ $V_{GS} = 4.5V$



Application

Battery protection

P-Channel MOSFET

Load switch

Uninterruptible power supply

Package Marking and Ordering Information

| Product ID | Pack | Marking | Qty(PCS) |
|------------|-----------|------------|----------|
| HXY60P02D | TO-252-2L | 60P02 XXXX | 2500 |

Absolute Maximum Ratings ($T_c = 25^\circ\text{C}$ unless otherwise noted)

| Symbol | Parameter | Rating | Units |
|---------------------------------|--|------------|---------------------------|
| V_{DS} | Drain-Source Voltage | -20 | V |
| V_{GS} | Gate-Source Voltage | ± 12 | V |
| $I_D @ T_c = 25^\circ\text{C}$ | Continuous Drain Current, $V_{GS} @ 10V^1$ | -60 | A |
| $I_D @ T_c = 100^\circ\text{C}$ | Continuous Drain Current, $V_{GS} @ 10V^1$ | -39 | A |
| Idm | Pulsed Drain Current ² | -240 | A |
| $P_D @ T_c = 25^\circ\text{C}$ | Total Power Dissipation ⁴ | 70 | W |
| T_{STG} | Storage Temperature Range | -55 to 150 | $^\circ\text{C}$ |
| T_J | Operating Junction Temperature Range | -55 to 150 | $^\circ\text{C}$ |
| $R_{\theta JA}$ | Thermal Resistance Junction-ambient ¹ | 75 | $^\circ\text{C}/\text{W}$ |
| $R_{\theta JC}$ | Thermal Resistance Junction-Case ¹ | 4.2 | $^\circ\text{C}/\text{W}$ |



Electrical Characteristics (T_J=25°C, unless otherwise noted)

| Symbol | Parameter | Test Condition | Min. | Typ. | Max. | Units |
|---|--|--|-------|-------|------|-------|
| Off Characteristic | | | | | | |
| V _{(BR)DSS} | Drain-Source Breakdown Voltage | V _{GS} =0V, I _D = -250μA | -20 | - | - | V |
| I _{DSS} | Zero Gate Voltage Drain Current | V _{DS} = -20V, V _{GS} = 0V, | - | - | -1 | μA |
| I _{GSS} | Gate to Body Leakage Current | V _{DS} =0V, V _{GS} = ±12V | - | - | ±100 | nA |
| On Characteristics | | | | | | |
| V _{GS(th)} | Gate Threshold Voltage | V _{DS} = V _{GS} , I _D = -250μA | -0.35 | -0.65 | -1.0 | V |
| R _{DS(on)} note3 | Static Drain-Source on-Resistance | V _{GS} =-4.5V, I _D =-15A | - | 6.6 | 8.5 | mΩ |
| | | V _{GS} =-2.5V, I _D =-12A | - | 8 | 12 | |
| Dynamic Characteristics | | | | | | |
| C _{iss} | Input Capacitance | V _{DS} =-10V, V _{GS} =0V, f = 1.0MHz | - | 4590 | - | pF |
| C _{oss} | Output Capacitance | | - | 505 | - | pF |
| C _{rss} | Reverse Transfer Capacitance | | - | 440 | - | pF |
| Q _g | Total Gate Charge | V _{DS} =-10V, I _D =-15A, V _{GS} =-4.5V | - | 46 | - | nC |
| Q _{gs} | Gate-Source Charge | | - | 7.3 | - | nC |
| Q _{gd} | Gate-Drain("Miller") Charge | | - | 10 | - | nC |
| Switching Characteristics | | | | | | |
| t _{d(on)} | Turn-on Delay Time | V _{DD} =-10V, I _D =-14A, R _{GEN} =2.7Ω, V _{GS} =-10V | - | 8 | - | ns |
| t _r | Turn-on Rise Time | | - | 59 | - | ns |
| t _{d(off)} | Turn-off Delay Time | | - | 111 | - | ns |
| t _f | Turn-off Fall Time | | - | 43 | - | ns |
| Drain-Source Diode Characteristics and Maximum Ratings | | | | | | |
| I _S | Maximum Continuous Drain to Source Diode Forward Current | - | - | -60 | - | A |
| I _{SM} | Maximum Pulsed Drain to Source Diode Forward Current | - | - | -240 | - | A |
| V _{SD} | Drain to Source Diode Forward Voltage | V _{GS} = 0V, I _S =-20A | - | - | -1.2 | V |
| t _{rr} | Reverse Recovery Time | T _J =25°C, I _{SD} =-15A, V _{GS} =0V di/dt=-100A/μs | - | 18 | - | ns |
| Q _{rr} | Reverse Recovery Charge | | - | 7.7 | - | nC |

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition: T_J=25°C, V_{DD}=-10V, V_G=-10V, R_G=5.9Ω, L=0.5mh, I_{AS}=-13.2A

3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%



Typical Characteristics

Figure 1: Output Characteristics

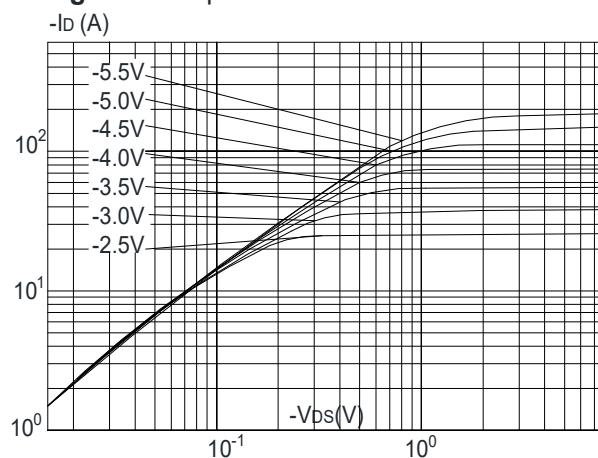


Figure 2: Typical Transfer Characteristics

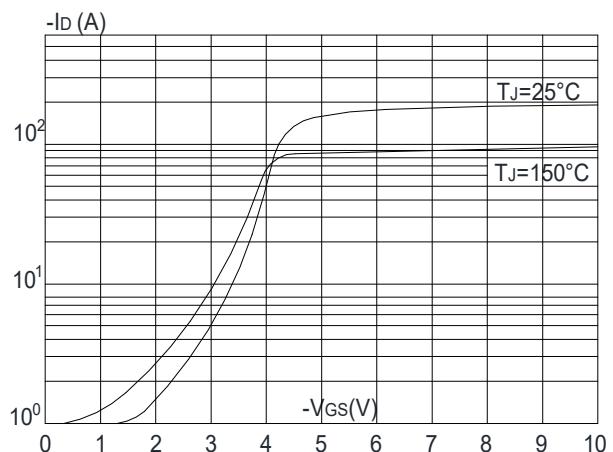


Figure 3: On-resistance vs. Drain Current

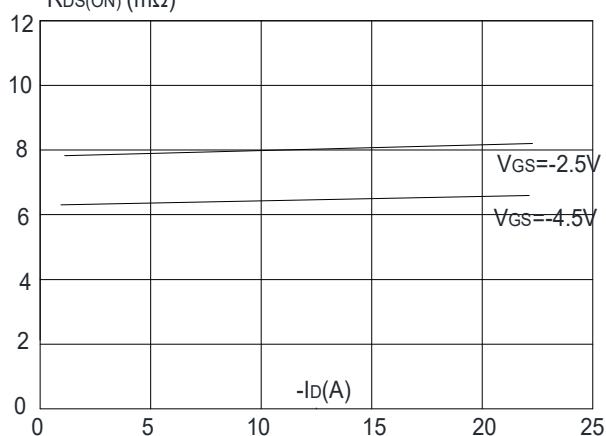


Figure 4: Body Diode Characteristics

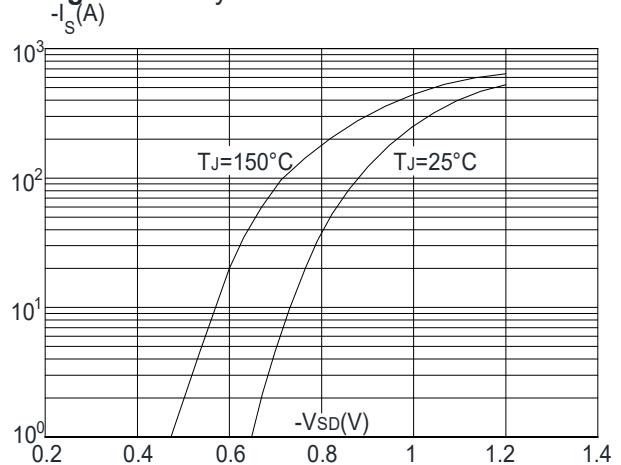


Figure 5: Gate Charge Characteristics

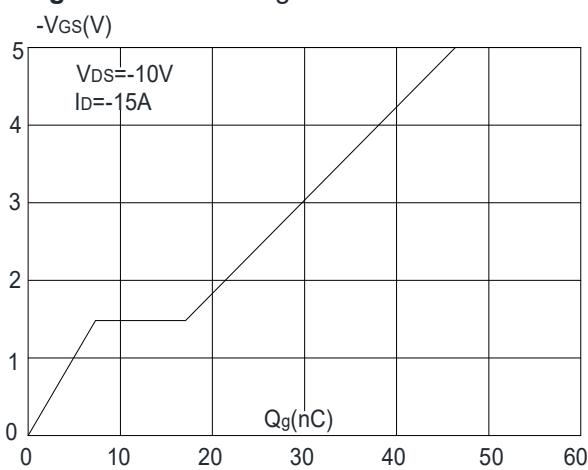


Figure 6: Capacitance Characteristics

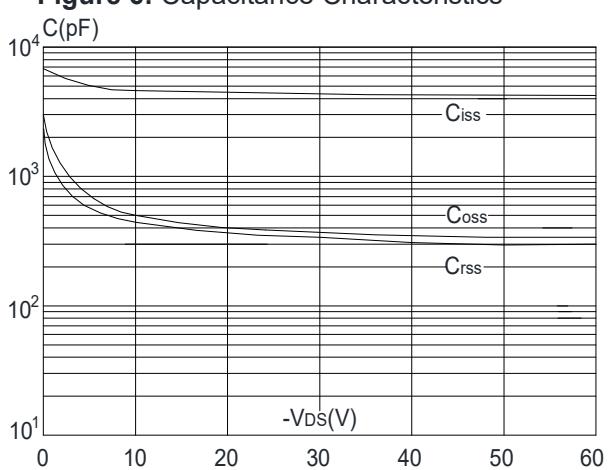




Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

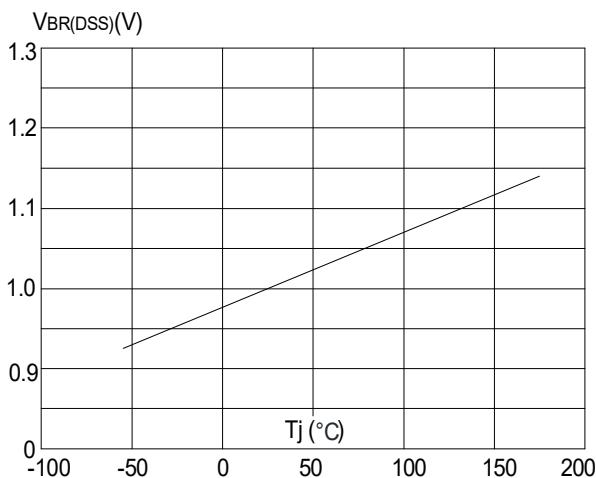


Figure 8: Normalized on Resistance vs. Junction Temperature

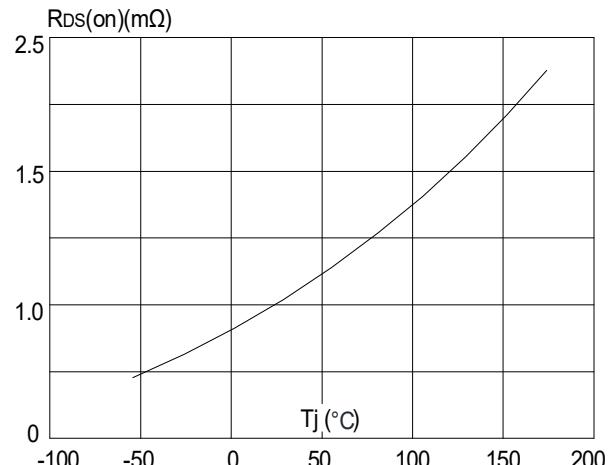


Figure 9: Maximum Safe Operating Area

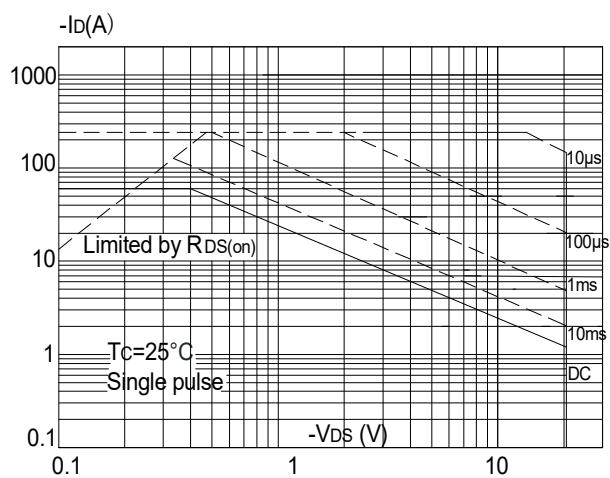


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

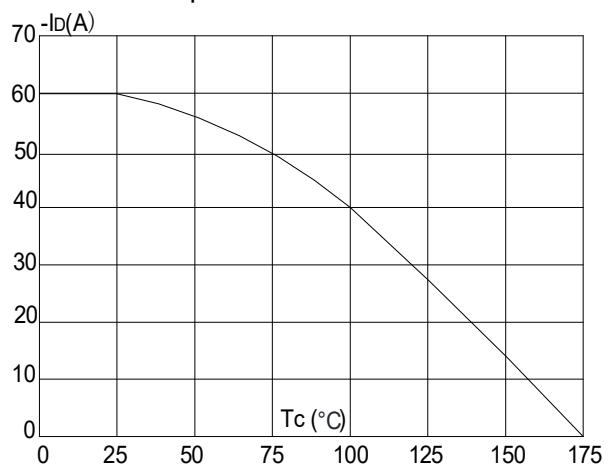
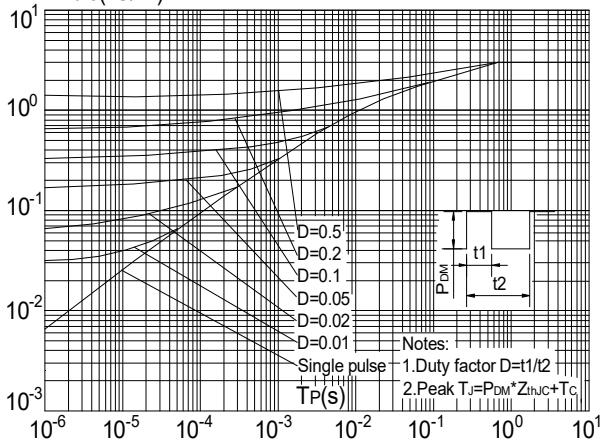
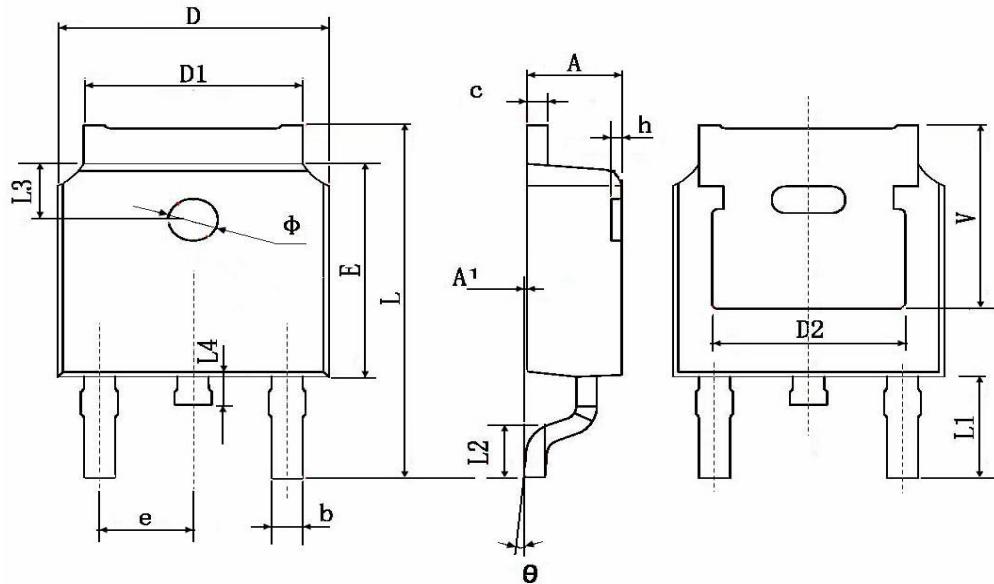


Figure 11: Maximum Effective Transient Thermal Impedance, Junction-to-Case





TO-252-2L Package Information



| Symbol | Dimensions In Millimeters | | Dimensions In Inches | |
|--------|---------------------------|--------|----------------------|-------|
| | Min. | Max. | Min. | Max. |
| A | 2.200 | 2.400 | 0.087 | 0.094 |
| A1 | 0.000 | 0.127 | 0.000 | 0.005 |
| b | 0.660 | 0.860 | 0.026 | 0.034 |
| c | 0.460 | 0.580 | 0.018 | 0.023 |
| D | 6.500 | 6.700 | 0.256 | 0.264 |
| D1 | 5.100 | 5.460 | 0.201 | 0.215 |
| D2 | 4.830 TYP. | | 0.190 TYP. | |
| E | 6.000 | 6.200 | 0.236 | 0.244 |
| e | 2.186 | 2.386 | 0.086 | 0.094 |
| L | 9.800 | 10.400 | 0.386 | 0.409 |
| L1 | 2.900 TYP. | | 0.114 TYP. | |
| L2 | 1.400 | 1.700 | 0.055 | 0.067 |
| L3 | 1.600 TYP. | | 0.063 TYP. | |
| L4 | 0.600 | 1.000 | 0.024 | 0.039 |
| Φ | 1.100 | 1.300 | 0.043 | 0.051 |
| θ | 0° | 8° | 0° | 8° |
| h | 0.000 | 0.300 | 0.000 | 0.012 |
| V | 5.350 TYP. | | 0.211 TYP. | |



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