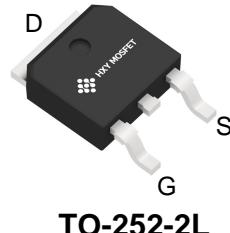




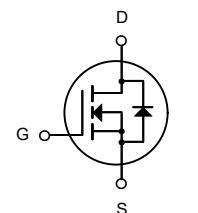
Description

The HXY4N50D can be used in various power switching circuit for system miniaturization and higher efficiency. The package form is TO-252-2L, which accords with the RoHS standard.



General Features

$V_{DS} = 500V$, $I_D = 4A$
 $R_{DS(ON)} < 3\Omega$ @ $V_{GS}=10V$



Application

- Power switch circuit of adaptor and charger.

N-Channel MOSFET

Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
HXY4N50D	TO-252-2L	5N50 XXX YYYY	2500

Absolute Maximum Ratings@ $T_j=25^\circ C$ (unless otherwise specified)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	500	V
V_{GS}	Gate-Source Voltage	+30	V
I_D @ $T_c=25^\circ C$	Drain Current, V_{GS} @ 10V	4	A
I_D @ $T_c=100^\circ C$	Drain Current, V_{GS} @ 10V	2	A
IDM	Pulsed Drain Current	15	A
P_D @ $T_c=25^\circ C$	Total Power Dissipation	32.9	W
E_{AS}	Single Pulse Avalanche Energy	67	mJ
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_j	Operating Junction Temperature Range	-55 to 150	°C



Electrical Characteristics (T_J=25°C, unless otherwise noted)

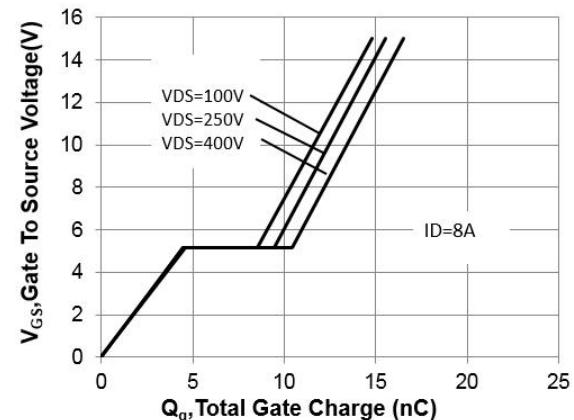
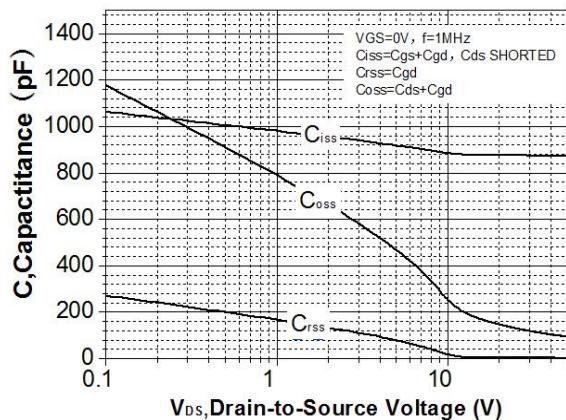
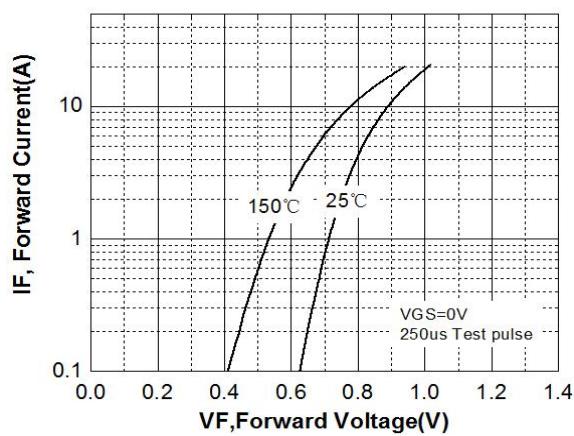
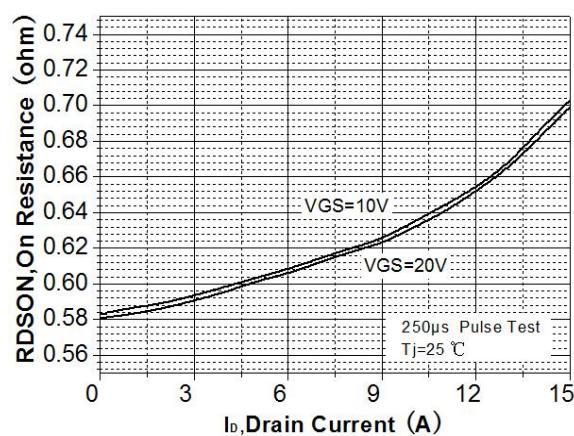
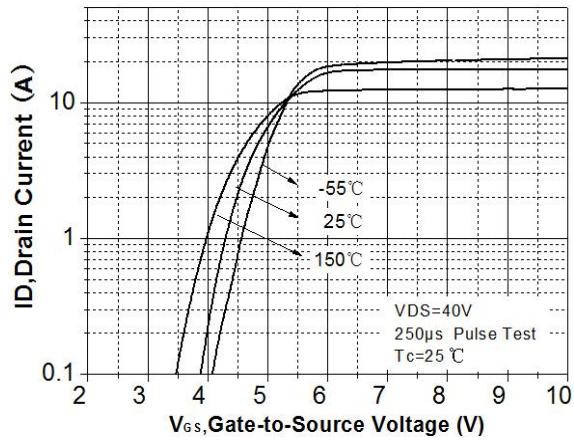
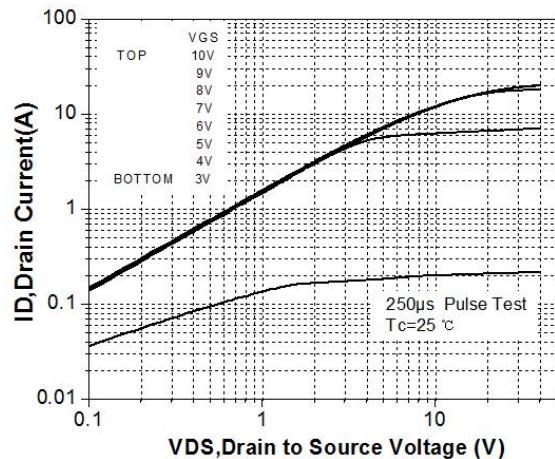
Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
V(BR)DSS	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA	500	550	--	V
IDSS	Zero Gate Voltage Drain Current	V _{DS} = 650V, V _{GS} = 0V, T _J = 25°C	--	--	1	μA
IGSS	Gate-Source Leakage	V _{GS} = ±30V	--	--	±100	nA
V _{GS(th)}	Gate-Source Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	2.0	3.0	4.0	V
R _{DS(on)}	Drain-Source On-Resistance (Note3)	V _{GS} = 10V, I _D = 3.5A	--	2.4	3.0	Ω
C _{iss}	Input Capacitance	V _{GS} = 0V, V _{DS} = 25V, f = 1.0MHz	--	310	--	pF
C _{oss}	Output Capacitance		--	39	--	
C _{rss}	Reverse Transfer Capacitance		--	6	--	
Q _g	Total Gate Charge	V _{DD} = 400V, I _D = 3A, V _{GS} = 10V	--	8	--	nC
Q _{gs}	Gate-Source Charge		--	1.2	--	
Q _{gd}	Gate-Drain Charge		--	5	--	
t _{d(on)}	Turn-on Delay Time	V _{DD} = 250V, I _D = 3A, R _G = 25Ω	--	7.8	--	ns
t _r	Turn-on Rise Time		--	33	--	
t _{d(off)}	Turn-off Delay Time		--	23	--	
t _f	Turn-off Fall Time		--	59	--	
I _S	Continuous Body Diode Current	T _C = 25 °C	--	--	4	A
I _{SM}	Pulsed Diode Forward Current		--	--	12	A
V _{SD}	Body Diode Voltage	T _J = 25°C, I _{SD} = 3A, V _{GS} = 0V	--	--	1.4	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0V, I _S = 3A, dI/dt = 100A / μs	--	80	--	ns
Q _{rr}	Reverse Recovery Charge		--	1.8	--	μC

Note :

- 1、The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2、The EAS data shows Max. rating . IAS = 2.4A, VDD = 50V, RG = 25 Ω, Starting TJ = 25 °C
- 3、The test condition is Pulse Test: Pulse width ≤ 300μs, Duty Cycle ≤ 1%
- 4、The power dissipation is limited by 150°C junction temperature
- 5、The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.



Typical Characteristics



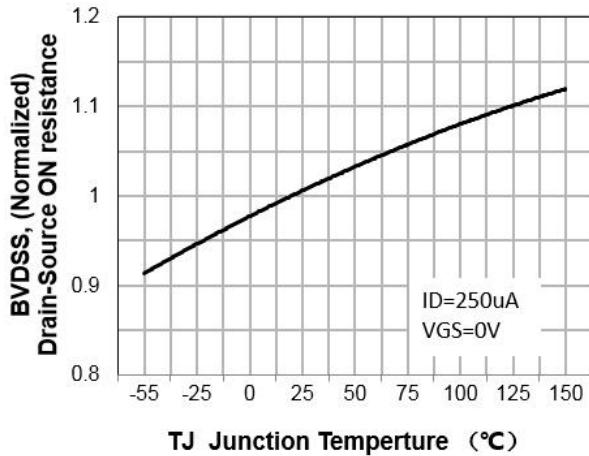


Figure 7. Breakdown Voltage Variation vs Temperature

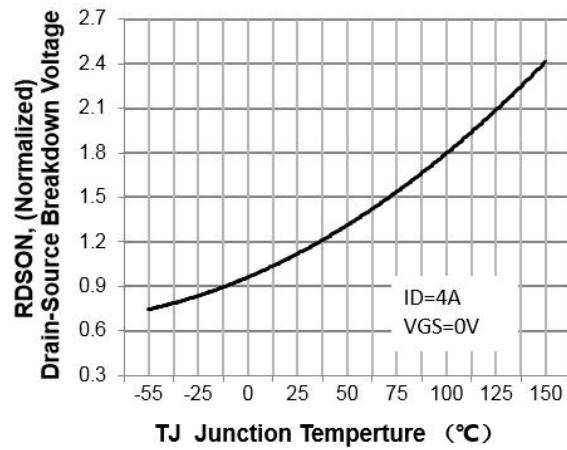


Figure 8. On-Resistance Variation vs Temperature

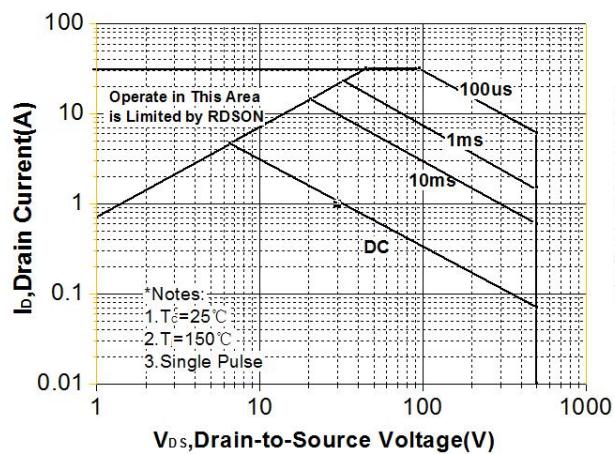


Figure 9. Maximum Safe Operating Area

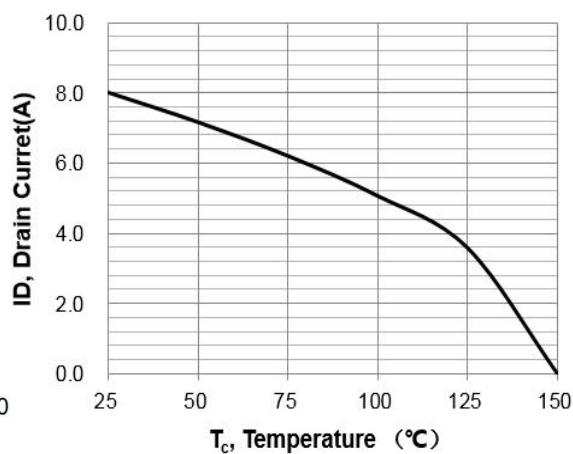


Figure 10. Maximum Drain Current vs Case Temperature

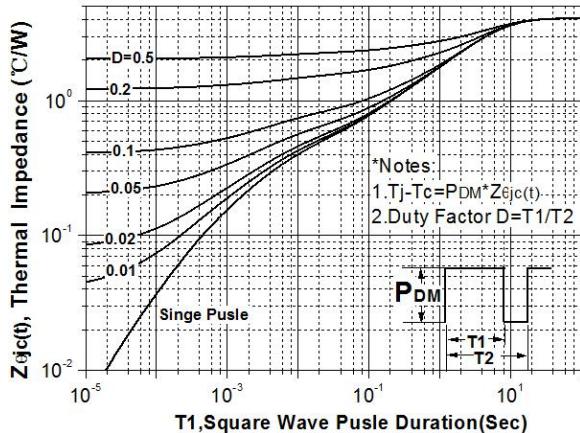
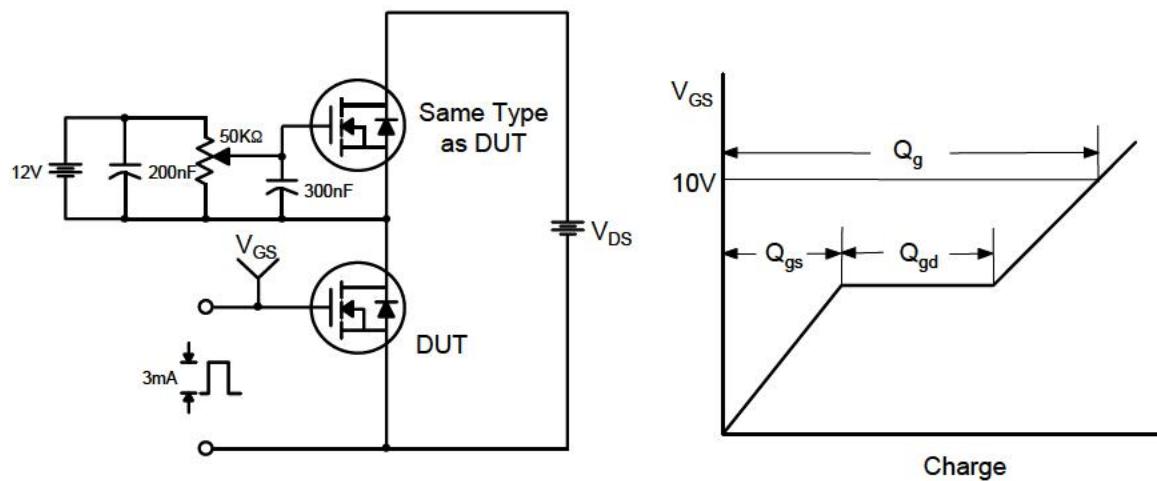


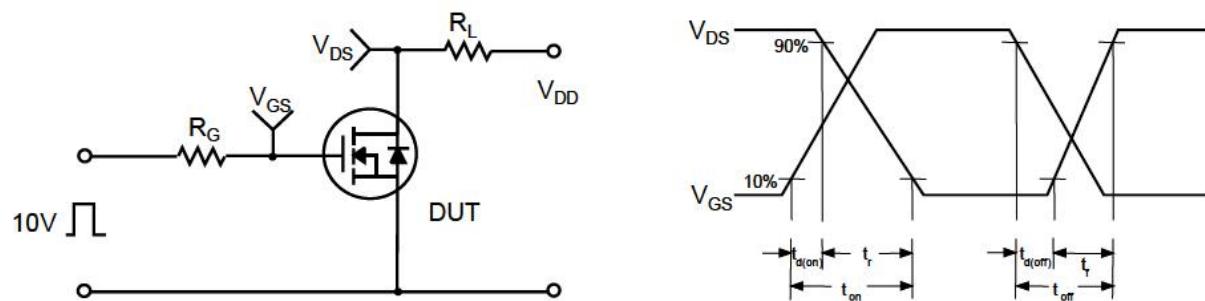
Figure 11. Transient Thermal Response Curve



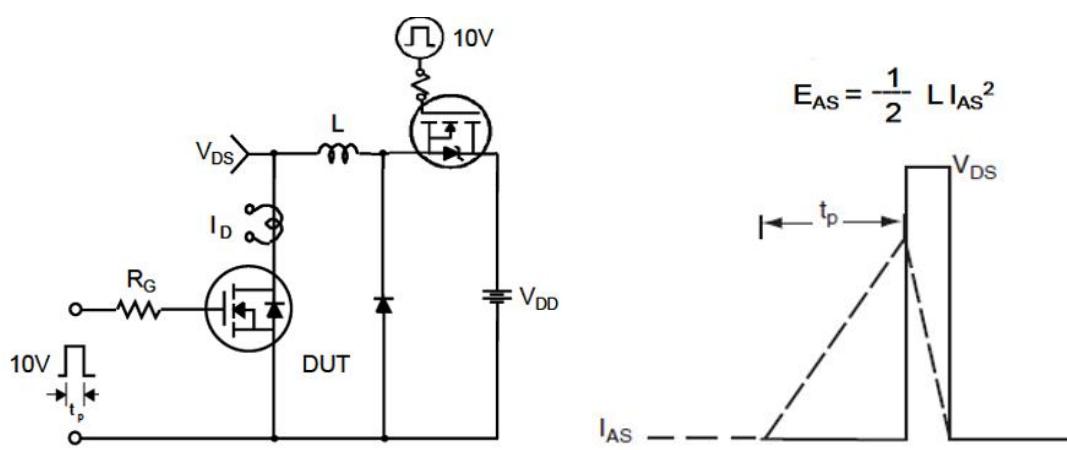
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms

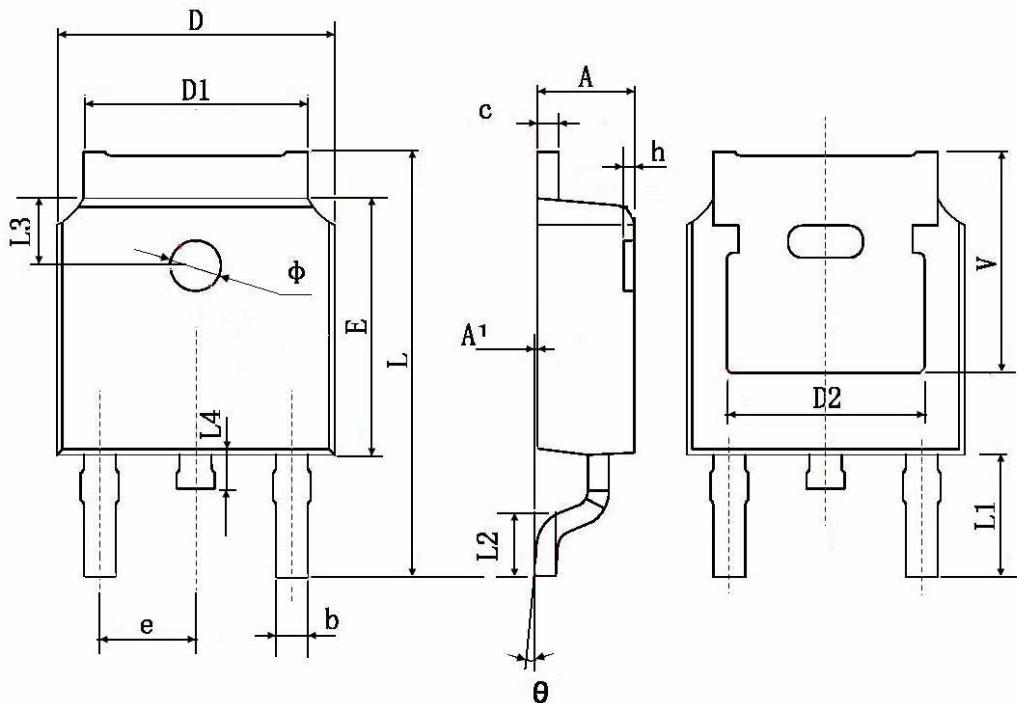


Unclamped Inductive Switching Test Circuit & Waveforms





TO-252-2L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.660	0.860	0.026	0.034
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	0.483 TYP.		0.190 TYP.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.800	10.400	0.386	0.409
L1	2.900 TYP.		0.114 TYP.	
L2	1.400	1.700	0.055	0.067
L3	1.600 TYP.		0.063 TYP.	
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.350 TYP.		0.211 TYP.	



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