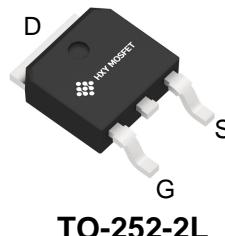




## Description

The HXY40P02D uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.



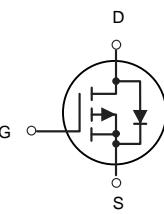
## General Features

$V_{DS} = -20V$   $I_D = -40A$

$R_{DS(ON)} < 18m\Omega$  @  $V_{GS} = -4.5V$

## Application

Battery protection



P-Channel MOSFET

Load switch

Uninterruptible power supply

## Package Marking and Ordering Information

Product ID	Pack	Brand	Qty(PCS)
HXY40P02D	TO-252-2L	HXY MOSFET	2500

## Absolute Maximum Ratings ( $T_A = 25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-20	V
$V_{GS}$	Gate-Source Voltage	$\pm 12$	V
$I_D @ T_c = 25^\circ C$	Continuous Drain Current, $V_{GS} @ -10V$	-40	A
$I_D @ T_c = 100^\circ C$	Continuous Drain Current, $V_{GS} @ -10V$	-15.7	A
$I_{DM}$	Pulsed Drain Current	-66	A
EAS	Single Pulse Avalanche Energy	28.8	mJ
$I_{AS}$	Avalanche Current	-26.6	A
$P_D @ T_c = 25^\circ C$	Total Power Dissipation	30	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	41.6	°C/W



**Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)**

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250μA	-20	-	-	V
Gate-body Leakage current	I <sub>GSS</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±12V	-	-	±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = -20V, V <sub>GS</sub> = 0V	-	-	-1	μA
			-	-	-100	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250μA	-0.4	-0.65	-1	V
Drain-Source On-Resistance <sup>4</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -8A	-	12.0	18	mΩ
		V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -6A	-	17	23	
Forward Transconductance <sup>4</sup>	g <sub>fs</sub>	V <sub>DS</sub> = -4.5V, I <sub>D</sub> = -8A	-	36	-	S
<b>Dynamic Characteristics<sup>5</sup></b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = -10V, V <sub>GS</sub> = 0V, f = 1MHz	-	1630	-	pF
Output Capacitance	C <sub>oss</sub>		-	211	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	187	-	
Gate Resistance	R <sub>g</sub>	f = 1MHz	-	10	-	Ω
<b>Switching Characteristics<sup>5</sup></b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>GS</sub> = -4.5V, V <sub>DS</sub> = -10V, I <sub>D</sub> = -8A	-	12	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	1.8	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	3.2	-	
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>GS</sub> = -4.5V, V <sub>DD</sub> = -10V, R <sub>G</sub> = 3Ω, I <sub>D</sub> = -8A	-	17	-	ns
Rise Time	t <sub>r</sub>		-	25.5	-	
Turn-Off Delay Time	t <sub>d(off)</sub>		-	32	-	
Fall Time	t <sub>f</sub>		-	15	-	
<b>Drain-Source Body Diode Characteristics</b>						
Diode Forward Voltage <sup>4</sup>	V <sub>SD</sub>	I <sub>S</sub> = -8A, V <sub>GS</sub> = 0V	-	-	-1.2	V
Continuous Source Current	T <sub>A</sub> = 25°C	I <sub>S</sub>	-	-	-40	A

Notes:

1. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C.
2. The EAS data shows Max. rating . The test condition is V<sub>DD</sub>= -25V, V<sub>GS</sub>= -10V, L= 0.1mH, I<sub>AS</sub>= -24A
3. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
4. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%.
5. This value is guaranteed by design hence it is not included in the production test..



## Typical Characteristics

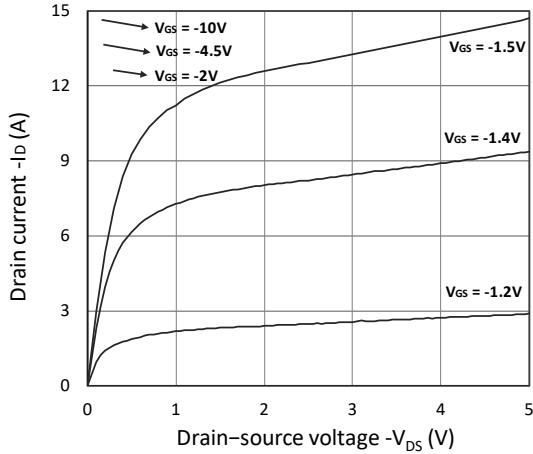


Figure 1. Output Characteristics

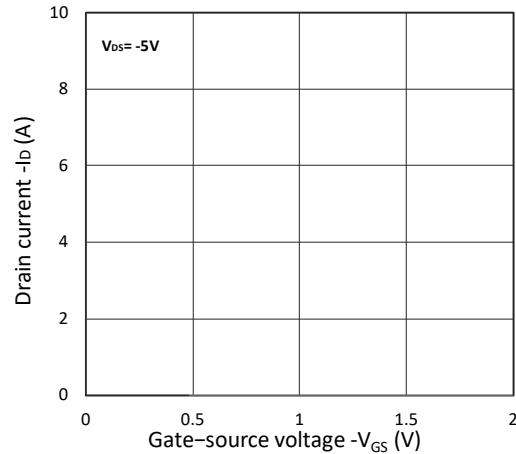


Figure 2. Transfer Characteristics

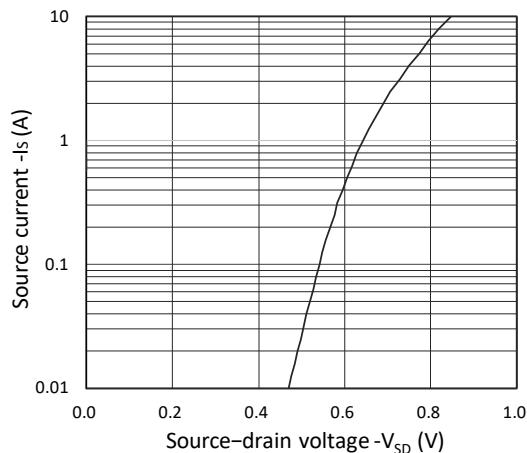


Figure 3. Forward Characteristics of Reverse

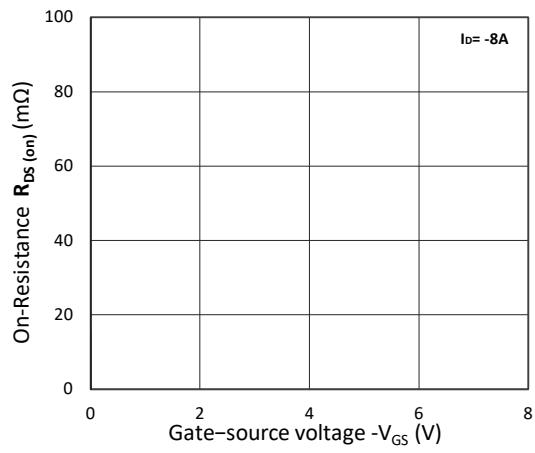


Figure 4.  $R_{DS(on)}$  vs.  $V_{GS}$

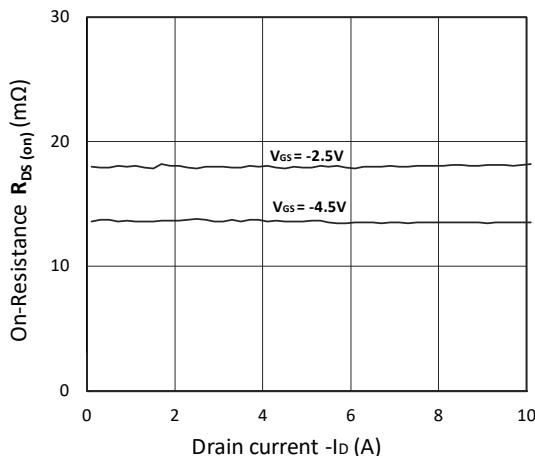


Figure 5.  $R_{DS(on)}$  vs.  $I_D$

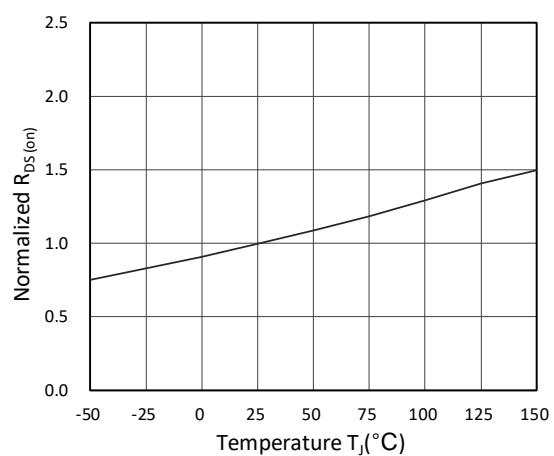


Figure 6. Normalized  $R_{DS(on)}$  vs. Temperature

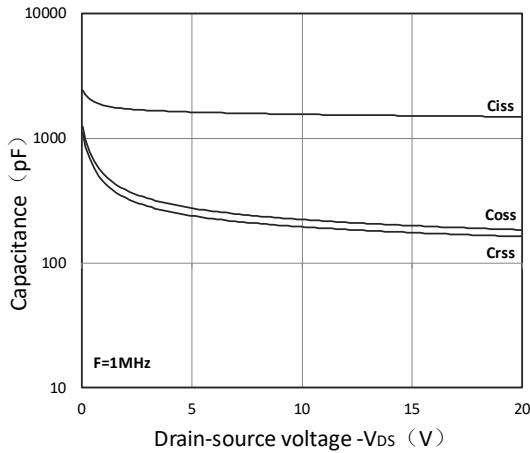


Figure 7. Capacitance Characteristics

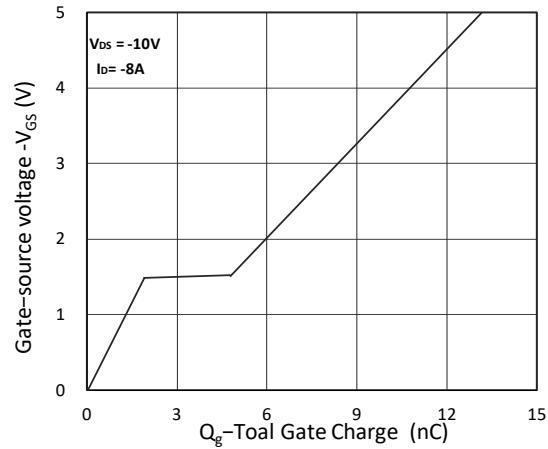


Figure 8. Gate Charge Characteristics

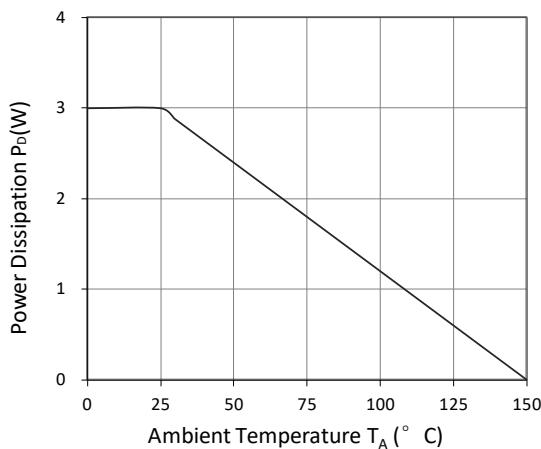


Figure 9. Power Dissipation

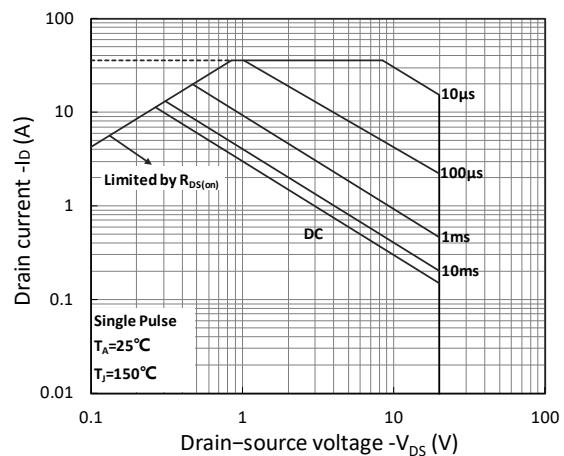


Figure 10. Safe Operating Area

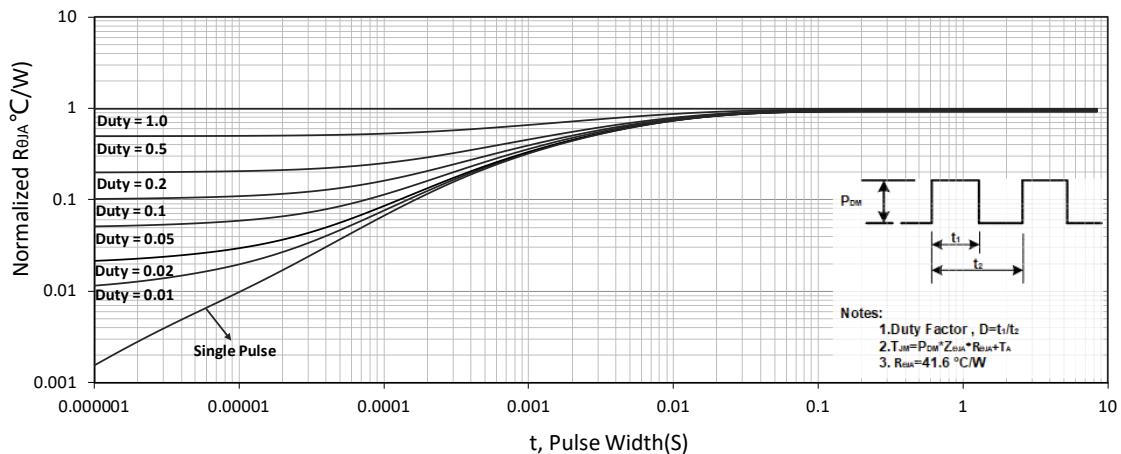
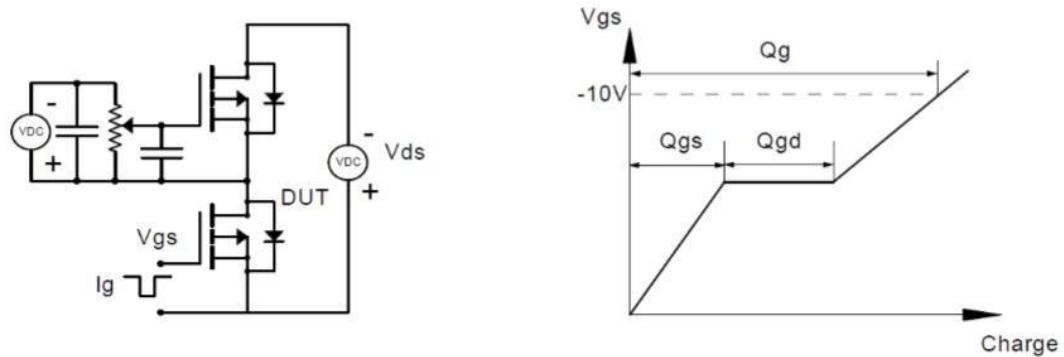


Figure 11. Normalized Maximum Transient Thermal Impedance

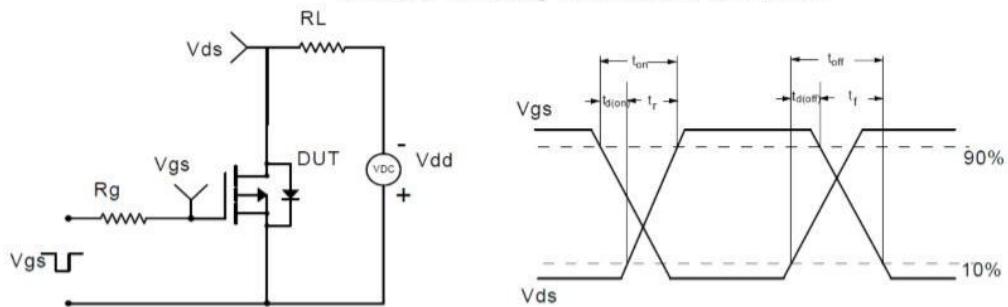


## Test Circuit

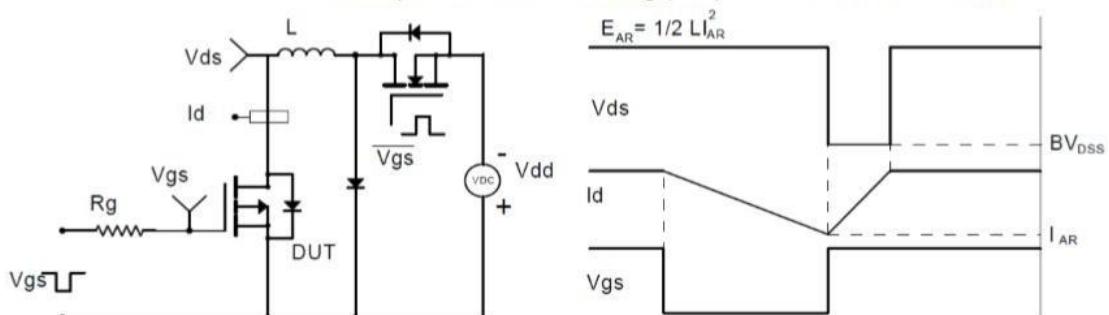
Gate Charge Test Circuit & Waveform



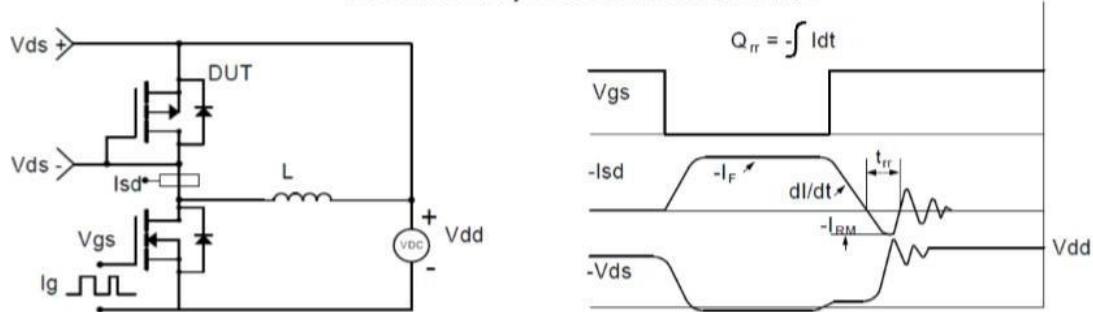
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms

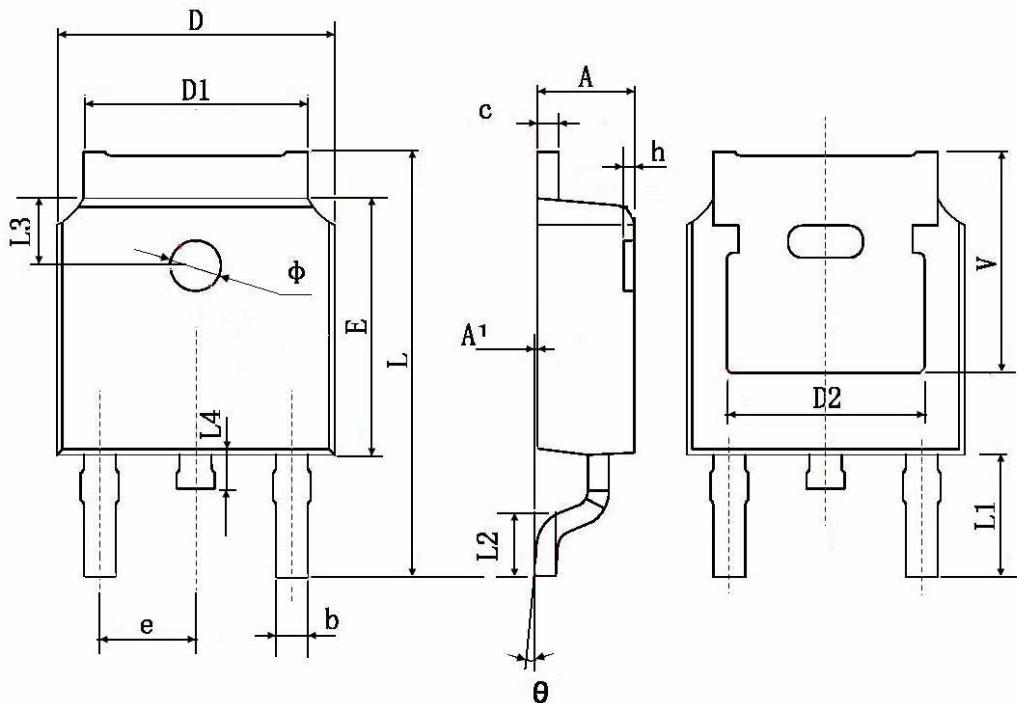


Diode Recovery Test Circuit & Waveforms





## TO-252-2L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.660	0.860	0.026	0.034
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	0.483 TYP.		0.190 TYP.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.800	10.400	0.386	0.409
L1	2.900 TYP.		0.114 TYP.	
L2	1.400	1.700	0.055	0.067
L3	1.600 TYP.		0.063 TYP.	
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.350 TYP.		0.211 TYP.	



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