

Description

The AP18P10AGH uses advanced trench technology

to provide excellent R_{DS(ON)}, low gate charge and

operation with gate voltages as low as 4.5V. This

device is suitable for use as a

Battery protection or in other Switching application.

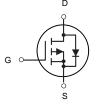


TO-252-2L

General Features

 $V_{DS} = -100V I_{D} = -20A$

 $R_{DS(ON)}$ < 100 m Ω @ V_{GS} =10V



P-Channel MOSFET

Application

Battery protection

Load switch

Uninterruptible power supply

Package Marking and Ordering Information

Product ID	Pack	Brand	Qty(PCS)
AP18P10AGH	TO-252-2L	HXY MOSFET	2500

Absolute Maximum Ratings (T_c=25°Cunless otherwise noted)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	-100	V
VGS	Gate-Source Voltage	±20	V
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	-20	А
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ¹	-11	А
IDM	Pulsed Drain Current ²	Pulsed Drain Current ² -72	
P _D @T _C =25°C	Total Power Dissipation ⁴	Total Power Dissipation ⁴ 70	
TSTG	Storage Temperature Range	-55 to 150	
TJ	Operating Junction Temperature Range	rature Range -55 to 150	
ReJA	Thermal Resistance Junction-ambient ¹	75	°C/W
R _θ JC	Thermal Resistance Junction-Case ¹	nermal Resistance Junction-Case ¹ 1.78	



Electrical Characteristics (T_J = 25°C, unless otherwise noted)

Parameter		Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static Characteristics				•				
Drain-Source Breakdown \	/oltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = -250μA	-100	-	-	V	
Gate-body Leakage curren	t	lgss	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA	
Zero Gate Voltage Drain	T _J = 25°C		100)/)/ 0)/	-	-	-1	μА	
Current	T _J = 100°C	I _{DSS}	V _{DS} = -100V, V _{GS} = 0V	-	-	-100		
Gate-Threshold Voltage		V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	-1	-1.8	-2.5	V	
Drain Course On Besistan	004	R _{DS(on)}	V _{GS} = -10V, I _D = -10A	-	80	100	mΩ	
Drain-Source On-Resistan	ce		V _{GS} = -4.5V, I _D = -6A		88	120		
Forward Transconductance ⁴		G fs	V _{DS} = -10V, I _D = -10A	-	30	-	S	
Dynamic Characteristi	CS ⁵							
Input Capacitance		C _{iss}		-	3985	-		
Output Capacitance		Coss	$V_{DS} = -50V, V_{GS} = 0V,$ f = 1MHz	-	85	-	pF	
Reverse Transfer Capacitance		C _{rss}		-	71	-		
Gate Resistance		R_g	f = 1MHz	-	4	-	Ω	
Switching Characterist	tics ⁵							
Total Gate Charge		\mathbf{Q}_{g}		-	65	-	nC	
Gate-Source Charge		\mathbf{Q}_{gs}	$V_{GS} = -10V, V_{DS} = -50V,$ $I_{D} = -10A$	-	10.2	-		
Gate-Drain Charge	Gate-Drain Charge			-	13	-		
Turn-On Delay Time		t _{d(on)}		-	12.8	-		
Rise Time	Rise Time		$V_{GS} = -10V, V_{DD} = -50V,$	-	30	-	ns	
Turn-Off Delay Time		t _{d(off)}	$R_G = 3\Omega$, $I_D = -10A$	-	82	-		
Fall Time	Fall Time		-	-	61	-		
Body Diode Reverse Recovery Time		t _{rr}	100 11/11 1000/	-	62	-	ns	
Body Diode Reverse Recovery Charge		Qrr	- I _F = -10A,dI/dt= 100A/μs	-	56	-	nC	
Drain-Source Body Did	ode Characte	eristics		•	•			
Diode Forward Voltage ⁴		V _{SD}	I _S = -10A, V _{GS} = 0V	-	-	-1.2	V	
Continuous Source Curren	t T _C = 25°C	ls	-	-	-	-20	Α	

Notes:

- 1. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}$ =150°C.
- 2. The EAS data shows Max. rating . The test condition is V_{DD} = -35V, V_{GS} = -10V, L= 0.5mH, I_{AS} = -23A
- 3. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
- 4. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- 5. This value is guaranteed by design hence it is not included in the production test..



Typical Characteristics

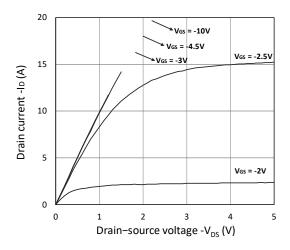


Figure 1. Output Characteristics

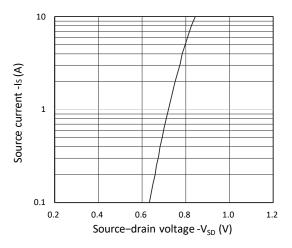


Figure 3. Forward Characteristics of Reverse

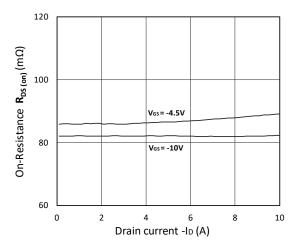


Figure 5. $R_{DS(ON)}$ vs. I_D

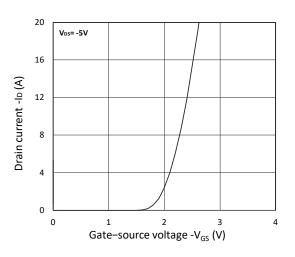


Figure 2. Transfer Characteristics

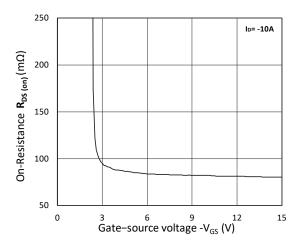


Figure 4. $R_{DS(ON)}$ vs. V_{GS}

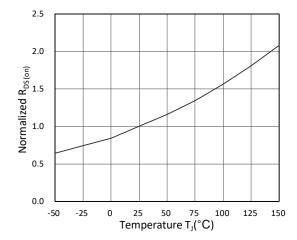


Figure 6. Normalized $R_{DS(on)}$ vs. Temperature



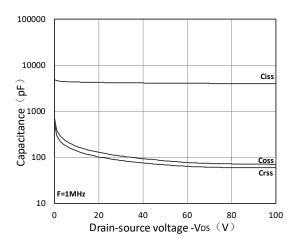
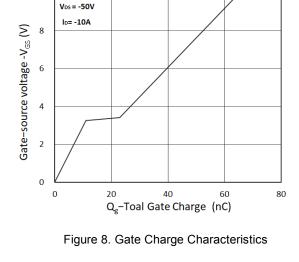


Figure 7. Capacitance Characteristics



10

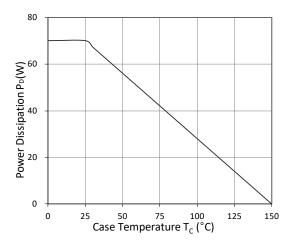


Figure 9. Power Dissipation

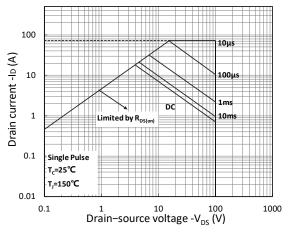


Figure 10. Safe Operating Area

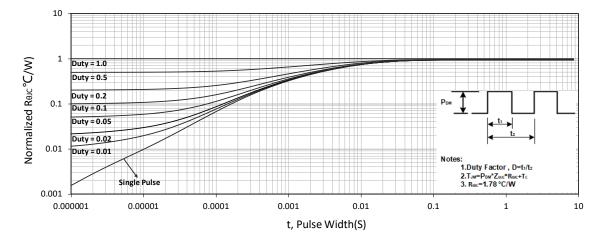
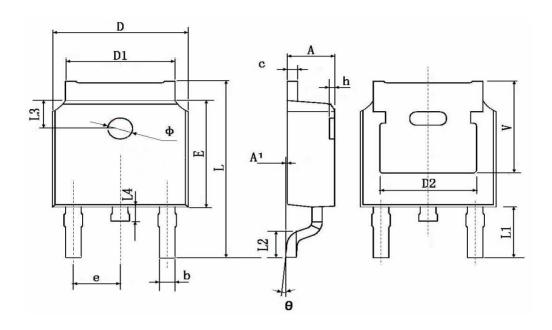


Figure 11. Normalized Maximum Transient Thermal Impedance



TO-252-2L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches			
	Min.	Max.	Min.	Max.		
A	2.200	2.400	0.087	0.094		
A1	0.000	0.127	0.000	0.005		
b	0.660	0.860	0.026	0.034		
С	0.460	0.580	0.018	0.023		
D	6.500	6.700	0.256	0.264		
D1	5.100	5.460	0.201	0.215		
D2	4.830	4.830 TYP.		0.190 TYP.		
E	6.000	6.200	0.236	0.244		
е	2.186	2.386	0.086	0.094		
L	9.800	10.400	0.386	0.409		
L1	2.900 TYP.		0.114 TYP.			
L2	1.400	1.700	0.055	0.067		
L3	1.600 TYP.		0.063 TYP.			
L4	0.600	1.000	0.024	0.039		
Ф	1.100	1.300	0.043	0.051		
θ	0°	8°	0°	8°		
h	0.000	0.300	0.000	0.012		
V	5.350 TYP.		0.211 TYP.			



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