

Description

The AOD454A uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.



TO-252-2L

General Features

 $V_{DS} = 40V I_{D} = 25 A$

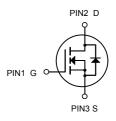
 $R_{DS(ON)}$ < 25m Ω @ V_{GS} =10V

Application

Battery protection

Load switch

Uninterruptible power supply



N-Channel MOSFET

Package Marking and Ordering Information

Product ID	Pack	Brand	Qty(PCS)
AOD454A	TO252-2L	HXY MOSFET	2500

Absolute Maximum Ratings (T_C=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	40	V
Vgs	Gate-Source Voltage	±20	V
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	25	А
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ¹	17	А
Ірм	Pulsed Drain Current ²	100	А
EAS	Single Pulse Avalanche Energy ³	23	mJ
P _D @T _C =25°C	Total Power Dissipation ⁴	26	W
Тѕтс	Storage Temperature Range	-50 to 150	°C
TJ	Operating Junction Temperature Range	-50 to 150	°C
Reja	Thermal Resistance Junction-ambient	62	°C/W
Rejc	Thermal Resistance Junction-Case	4.8	°C/W



Electrical Characteristics (T_J = 25°C, unless otherwise noted)

Symbol	Parameter	Conditions	Min	Тур	Max	Units
BV _{DSS}	Drain-Sourtce Breakdown Voltage	V _{GS} =0V,I _D =250μA	40			V
	Zero Gate Voltage Drain Current	V _{GS} =0V, V _{DS} =40V			1	μA
I _{DSS}		V _{GS} =0V, V _{DS} =32V			10	μA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0A			±100	nA
V _{GS(th)}	Gate-Source Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	1.2	1.6	2.5	V
_		V _{GS} =10V,I _D =12A		18	25	mΩ
R _{DS(ON)}	Drain-Source On Resistance	V _{GS} =4.5V,I _D =10A		25	35	mΩ
C _{iss}	Input Capacitance	V_{DS} =25V, V_{GS} =0V, f=1MHz		825		
C _{oss}	Output Capacitance			70		pF
C _{rss}	Reverse Transfer Capacitance			39		
t _{d(on)}	Turn-On Delay Time			2.2		ns
t _r	Rise Time	V_{DD} =20V, I_D =1A,		7.6		ns
$t_{ ext{d(off)}}$	Turn-Off Delay Time	R_{ENG} =25 Ω , V_{GS} =4.5 V		17		ns
t _f	Fall Time			5		ns
Q _{gs}	Total Gate Charge	V _{GS} =10V, V _{DS} =20V, I _D =5A		1.3		nC
Q_gd	Gate-Source Charge			1.7		nC
Q_g	Gate-Drain "Miller" Charge			13		nC
V _{SD}	Diode Forward Voltage	V _{GS} =0V, I _S =1A			1	V
I _S	Continuous Drain Curren	VD=VG=0V			25	А
Ism	Pulsed Drain Current				95	А

Notes:

- 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
- 2. EAS condition : TJ=25 $^{\circ}$ C,VDD=30V,VG=10V,L=0.5mH
- 3. Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%



Typical Characteristics

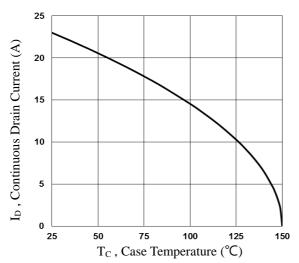


Fig.1 Continuous Drain Current vs. TC

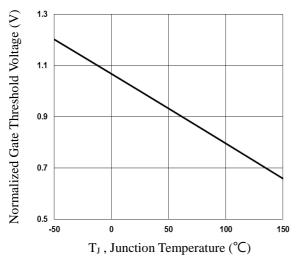


Fig.3 Normalized V_{th} vs. T_J

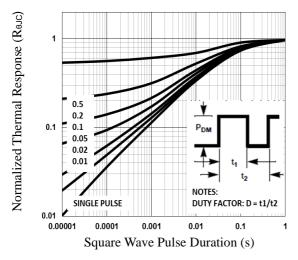


Fig.5 Normalized Transient Impedance

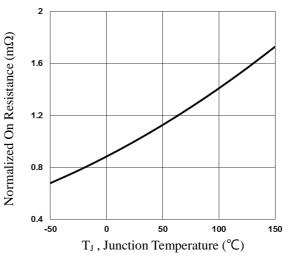


Fig.2 Normalized RDSON vs. TJ

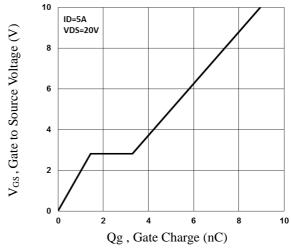


Fig.4 Gate Charge Characteristics

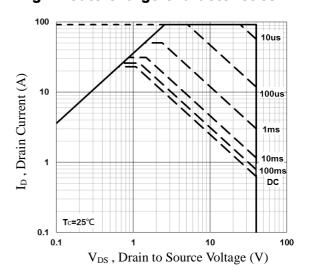
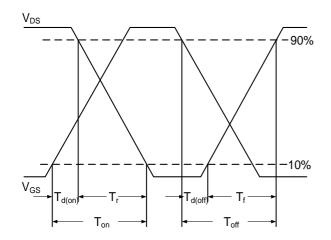


Fig.6 Maximum Safe Operation Area



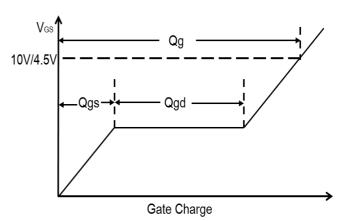


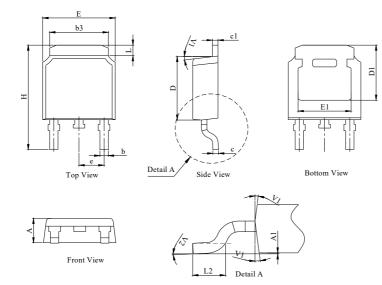
Fig.7 Switching Time Waveform

Fig.8 Gate Charge Waveform



TO252-2L Package Information

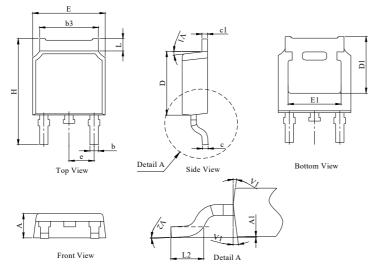
Package Outline Type-A



UNIT: mm

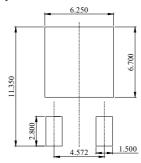
	MILLIMETER			
DIM.	MIN.	NOM.	MAX.	
A	2.18	2.30	2.39	
A1	0		0.13	
b	0.64	0.76	0.89	
c	0.40	0.50	0.61	
c1	0.46	0.50	0.58	
D	5.97	6.10	6.23	
D1	5.05			
E	6.35	6.60	6.73	
E1	4.32			
b3	5.21	5.38	5.55	
e	2.29 BSC			
Н	9.40	10.00	10.40	
L	0.89		1.27	
L2	1.40		1.78	
V1	7° REF			
V2	0°		6°	

Package Outline Type-B



DIM.	MILLIMETER			
	MIN.	NOM.	MAX.	
A	2.10	2.30	2.40	
A1	0		0.13	
b	0.66	0.76	0.86	
b3	5.21	5.38	5.55	
с	0.40	0.50	0.60	
c1	0.44	0.50	0.58	
D	5.90	6.10	6.30	
D1	5.30REF			
E	6.40	6.60	6.80	
E1	4.63	-	-	
e	2.29 BSC			
Н	9.50	10.00	10.70	
L	1.09		1.21	
L2	1.35		1.65	
V1	7° REF			
V2	0°		6°	

Recommended Soldering Footprint





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