

#### **Description**

The 2SJ598-ZK-E1-AZ uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

# D S S

TO-252-2L

#### **General Features**

 $V_{DS} = -60V I_{D} = -10 A$ 

 $R_{DS(ON)}$  < 140m $\Omega$  @  $V_{GS}$ =10V

# G S

### **Application**

Brushless motor

Load switch

Uninterruptible power supply

P-Channel MOSFET

#### **Package Marking and Ordering Information**

Product ID	Pack	Brand	Qty(PCS)
2SJ598-ZK-E1-AZ	TO252-2L	HXY MOSFET	2500

# Absolute Maximum Ratings (T<sub>C</sub>=25 ℃ unless otherwise noted)

Symbol	Parameter Ratin		Units	
V <sub>D</sub> s	Drain-Source Voltage	-60	V	
Vgs	Gate-Source Voltage	±20	V	
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ -10V <sup>1</sup>	-10	А	
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ -10V <sup>1</sup>	Continuous Drain Current, V <sub>GS</sub> @ -10V <sup>1</sup> -8.3		
I <sub>D</sub> @T <sub>A</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ -10V <sup>1</sup>	-3.3	А	
I <sub>D</sub> @T <sub>A</sub> =70°C	Continuous Drain Current, V <sub>GS</sub> @ -10V <sup>1</sup>	Continuous Drain Current, V <sub>GS</sub> @ -10V <sup>1</sup> -2.7		
Ідм	Pulsed Drain Current <sup>2</sup>	Pulsed Drain Current <sup>2</sup> -26		
EAS	Single Pulse Avalanche Energy <sup>3</sup>	Single Pulse Avalanche Energy <sup>3</sup> 29.8		
las	Avalanche Current	Avalanche Current -24.4		
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>4</sup>	Power Dissipation <sup>4</sup> 31.3		
P <sub>D</sub> @T <sub>A</sub> =25°C	Total Power Dissipation <sup>4</sup>	Power Dissipation <sup>4</sup> 2		
Тѕтс	Storage Temperature Range	-55 to 150	°C	
TJ	Operating Junction Temperature Range	-55 to 150	°C	
$R_{\theta}$ JA	Thermal Resistance Junction-Ambient <sup>1</sup>	62	°C/W	
Rejc	Thermal Resistance Junction-Case <sup>1</sup>	4.0	°C/W	



# P-Channel Electrical Characteristics (TJ =25 ℃, unless otherwise noted)

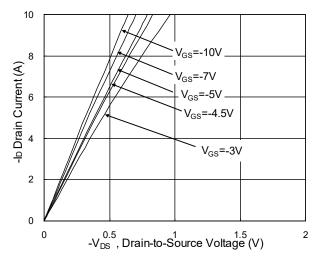
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BVDSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =-250uA	-60			V	
∆BVDSS/∆TJ	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C , I <sub>D</sub> =-1mA		-0.03		V/°C	
RDS(ON)	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =-10V , I <sub>D</sub> =-3A		125	140	mΩ	
1100(011)	Statio Brain-Source On-Resistance	V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-2A			200	11122	
VGS(th)	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D$ =-250uA	-1.2	1.6	-2.5	<b>V</b>	
IDSS	Drain-Source Leakage Current	V <sub>DS</sub> =-48V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			1	uA	
1033		V <sub>DS</sub> =-48V , V <sub>GS</sub> =0V , T <sub>J</sub> =55°C			5		
IGSS	Gate-Source Leakage Current	$V_{GS}$ =±20 $V$ , $V_{DS}$ =0 $V$			±100	nA	
gfs	Forward Transconductance	V <sub>DS</sub> =-5V , I <sub>D</sub> =-3A		8.5		S	
Qg	Total Gate Charge (-4.5V)			12.1			
Qgs	Gate-Source Charge	V <sub>DS</sub> =-48V , V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-3A		2.2		nC	
Qgd	Gate-Drain Charge			6.3			
Td(on)	Turn-On Delay Time			9.2		- ns	
Tr	Rise Time	V <sub>DD</sub> =-15V , V <sub>GS</sub> =-10V , R <sub>G</sub> =3.3⊑,		20.1			
Td(off)	Turn-Off Delay Time	I <sub>D</sub> =-1A		46.7			
Tf	Fall Time			9.4			
Ciss	Input Capacitance			1137			
Coss	Output Capacitance	V <sub>DS</sub> =-15V , V <sub>GS</sub> =0V , f=1MHz		76		pF	
Crss	Reverse Transfer Capacitance			50			
IS	Continuous Source Current <sup>1,5</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			-13	Α	
VSD	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =-1A , T <sub>J</sub> =25°C			-1.2	V	

#### Note:

- 1. The data tested by surface mounted on a 1 inch 2 FR-4 board with 2OZ copper.
- $2\sqrt{100}$  The data tested by pulsed , pulse width  $\leq 300$ us , duty cycle  $\leq 2\%$
- 3. The EAS data shows Max. rating . The test condition is V DD =-25V,V GS =-10V,L=0.1mH,IAS =-24A
- 5. The data is theoretically the same as I D and I DM, in real applications, should be limited by total power dissipation.



# **P-Channel Typical Characteristics**



**Fig.1 Typical Output Characteristics** 

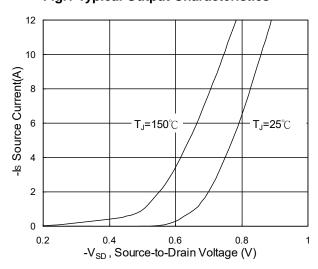


Fig.3 Forward Characteristics of Reverse

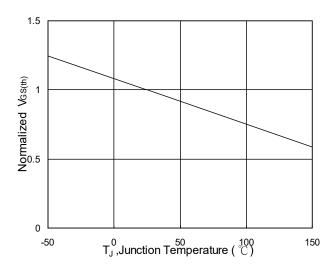


Fig.5 Normalized  $V_{GS(th)}$  v.s  $T_J$ 

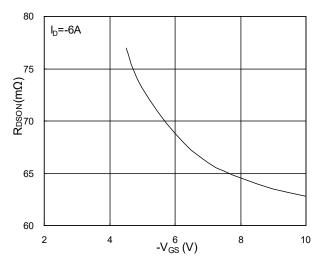


Fig.2 On-Resistance v.s Gate-Source

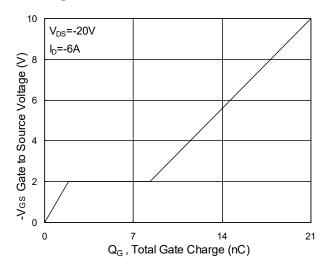


Fig.4 Gate-Charge Characteristics

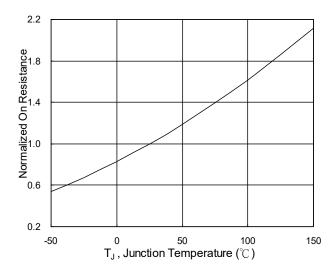
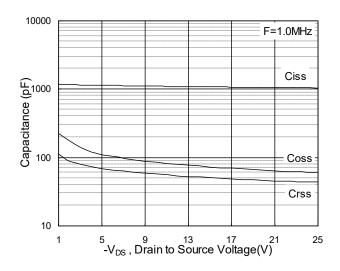


Fig.6 Normalized  $R_{DSON}$  v.s  $T_J$ 





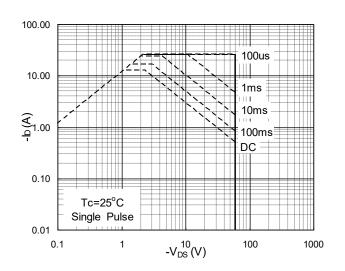


Fig.7 Capacitance

Fig.8 Safe Operating Area

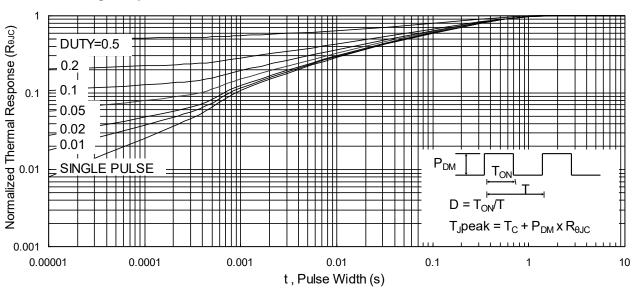


Fig.9 Normalized Maximum Transient Thermal Impedance

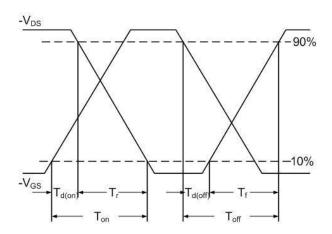


Fig.10 Switching Time Waveform

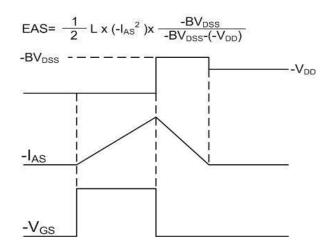
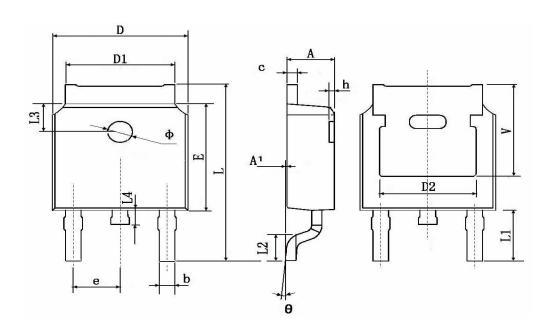


Fig.11 Unclamped Inductive Switching Waveform



# **TO252-2L Package Information**



Symbol	Dimensions In Millimeters		Dimensions In Inches			
	Min.	Max.	Min.	Max.		
A	2.200	2.400	0.087	0.094		
A1	0.000	0.127	0.000	0.005		
b	0.660	0.860	0.026	0.034		
С	0.460	0.580	0.018	0.023		
D	6.500	6.700	0.256	0.264		
D1	5.100	5.460	0.201	0.215		
D2	4.830	4.830 TYP.		0.190 TYP.		
E	6.000	6.200	0.236	0.244		
е	2.186	2.386	0.086	0.094		
L	9.800	10.400	0.386	0.409		
L1	2.900	2.900 TYP.		0.114 TYP.		
L2	1.400	1.700	0.055	0.067		
L3		1.600 TYP.		0.063 TYP.		
L4	0.600	1.000	0.024	0.039		
Ф	1.100	1.300	0.043	0.051		
θ	0°	8°	0°	8°		
h	0.000	0.300	0.000	0.012		
V	5.350	5.350 TYP. 0.211 TYP.		TYP.		

#### P-Channel Enhancement Mode MOSFET

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