



General Description

The HXYG80P10MP use advanced SGT MOSFET technology to provide low $R_{DS(ON)}$, low gate charge, fast switching and excellent avalanche characteristics. This device is specially designed to get better ruggedness and suitable.

General Features

$V_{DS} = -100V$ $I_D = -80A$

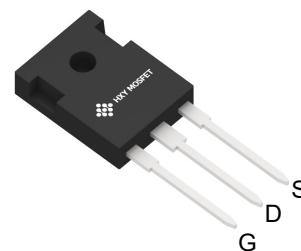
$R_{DS(ON)} < 25m\Omega$ @ $V_{GS} = -10V$

Applications

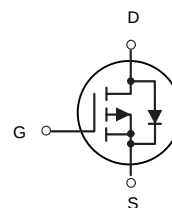
Consumer electronic power supply Motor control

Synchronous-rectification Isolated DC

Synchronous-rectification applications



TO-247



P-Channel MOSFET

Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
HXYG80N10MP	TO-247		30

Absolute Maximum Ratings at $T_J = 25^\circ C$ unless otherwise noted

Parameter	Symbol	Value	Unit
Drain source voltage	V_{DS}	-100	V
Gate source voltage	V_{GS}	± 20	V
Continuous drain current ¹⁾	I_D	-80	A
Pulsed drain current ²⁾	$I_{D, pulse}$	-260	A
Power dissipation ³⁾	P_D	250	W
Operation and storage temperature	T_{stg}, T_J	-55 to 150	$^\circ C$
Thermal resistance, junction-case	$R_{\theta JC}$	0.5	$^\circ C/W$



Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise specified):

Static Characteristics						
Symbol	Parameter	Test Conditions	Value			Units
			Min.	Typ.	Max.	
V_{DSS}	Drain to Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-100	--	--	V
I_{DSS}	Drain to Source Leakage Current	$V_{DS} = -100V, V_{GS}=0V$	--	--	1	μA
$I_{GSS(F)}$	Gate to Source Forward Leakage	$V_{GS} = -20V$	--	--	100	nA
$I_{GSS(R)}$	Gate to Source Reverse Leakage	$V_{GS} = +20V$	--	--	-100	nA
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu A$	-2	-3	-4	V
$R_{DS(ON)}$	Drain-to-Source On-Resistance	$V_{GS}=-10V, I_D=-15A$	--	22	25	m Ω

Dynamic Characteristics						
Symbol	Parameter	Test Conditions	Value			Units
			Min.	Typ.	Max.	
C_{iss}	Input Capacitance	$V_{GS}=0V$ $V_{DS}=-50V$ $f=1.0MHz$	--	4200	--	pF
C_{oss}	Output Capacitance		--	536	--	
C_{rss}	Reverse Transfer Capacitance		--	52	--	

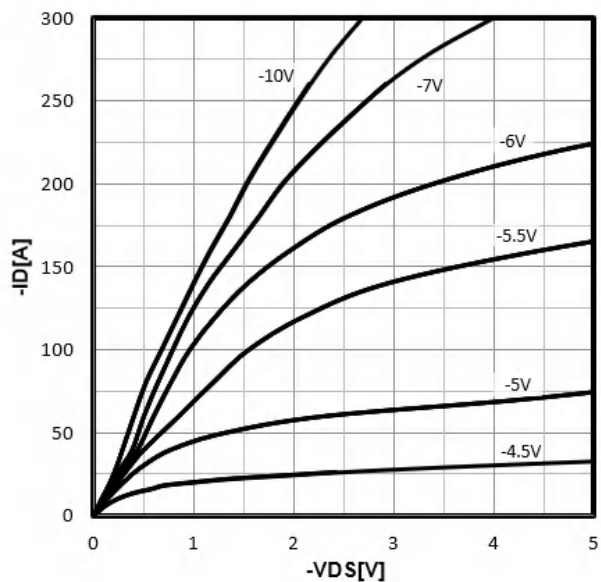
Resistive Switching Characteristics						
Symbol	Parameter	Test Conditions	Value			Units
			Min.	Typ.	Max.	
$t_{d(ON)}$	Turn-on Delay Time	$I_D=-15A, R_L=0.75\Omega$ $V_{DS}= -50V$ $V_{GS}= -10V$ $R_G= 3\Omega$	--	13	--	ns
t_r	Rise Time		--	51	--	
$t_{d(OFF)}$	Turn-Off Delay Time		--	177	--	
t_f	Fall Time		--	82	--	
Q_g	Total Gate Charge	$V_{GS}=-10V$ $V_{DS}=-50V$ $I_D=-15A$	--	76	--	nC
Q_{gs}	Gate Source Charge		--	13	--	
Q_{gd}	Gate Drain Charge		--	12.4	--	

Source-Drain Diode Characteristics						
Symbol	Parameter	Test Conditions	Value			Units
			Min.	Typ.	Max.	
I_S	Diode Forward Current	$T_C=25^\circ\text{C}$	--	--	-80	A
V_{SD}	Diode Forward Voltage	$I_S=-15A, V_{GS}=0V$	--	--	-1.2	V
t_{rr}	Reverse Recovery time	$I_S=-15A, V_{DD}=-50V$ $dI/dt=100A/\mu s$	--	110	--	ns
Q_{rr}	Reverse Recovery Charge		--	590	--	nC

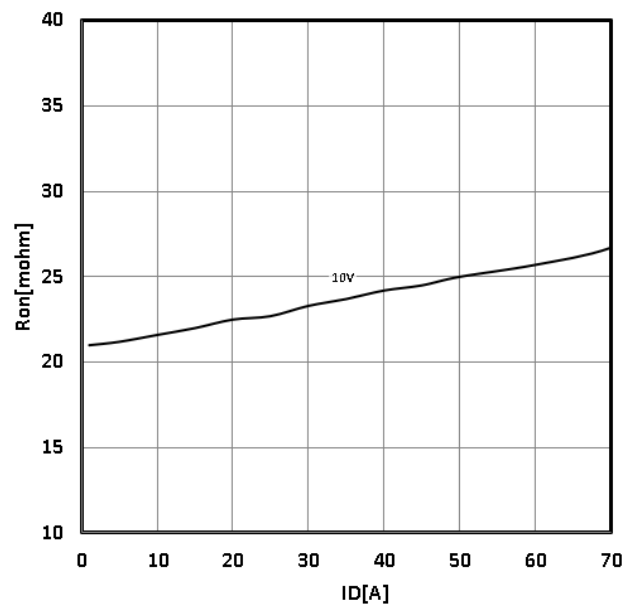
^{a1}: Repetitive rating; pulse width limited by maximum junction temperature



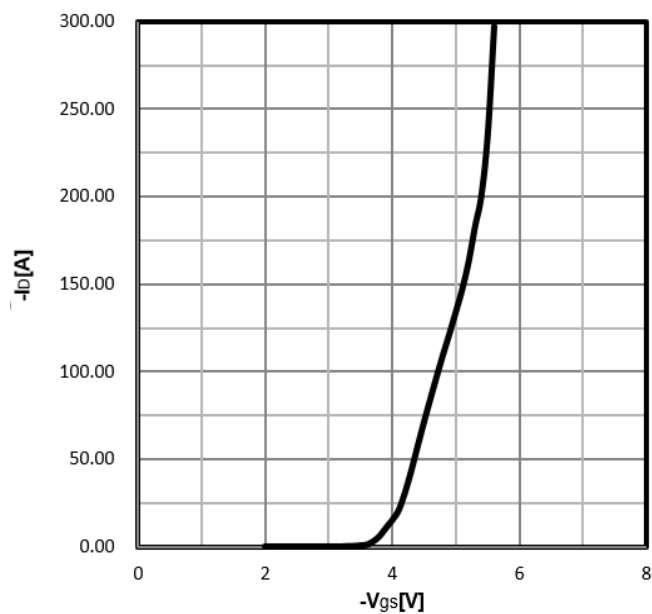
Typ. output characteristics
 $I_D = f(V_{DS})$



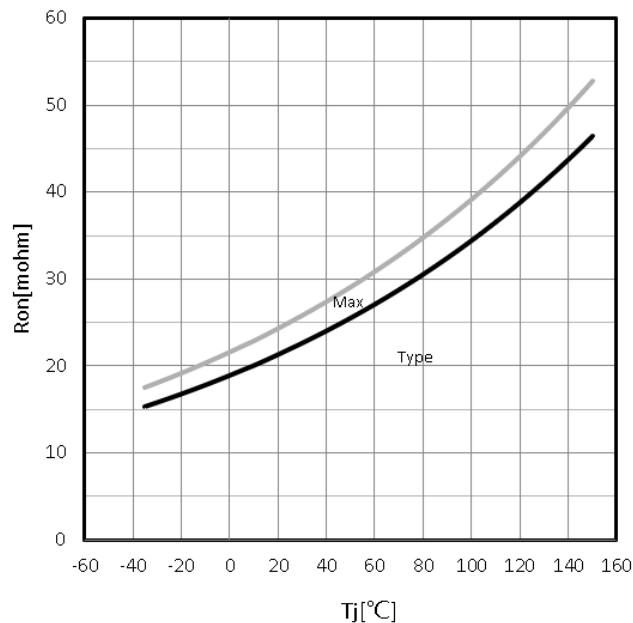
Typ. drain-source on resistance
 $R_{DS(on)} = f(I_D)$



Typ. transfer characteristics
 $I_D = f(V_{GS})$

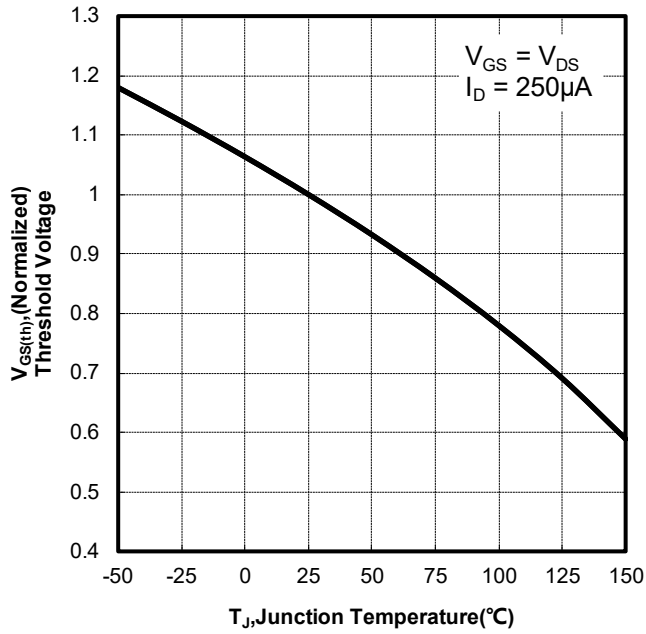


Drain-source on-state resistance
 $R_{DS(on)} = f(T_j); I_D = -15A; V_{GS} = -10V$

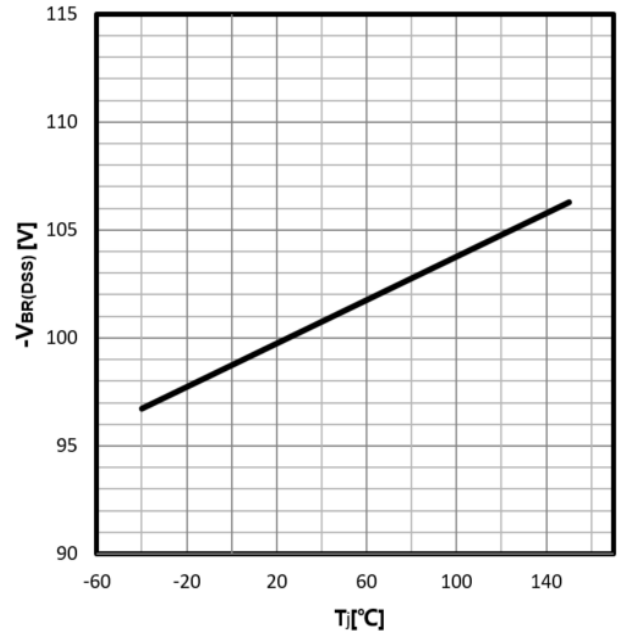




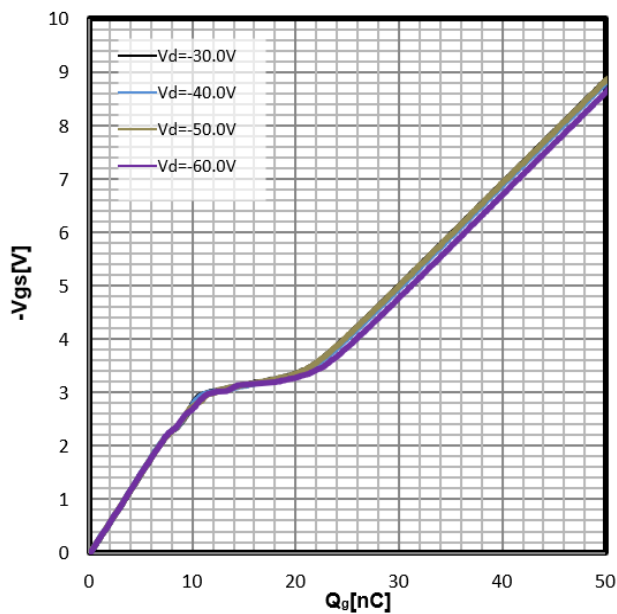
Gate Threshold Voltage
 $-V_{TH}=f(T_j); I_D=-250\mu A$



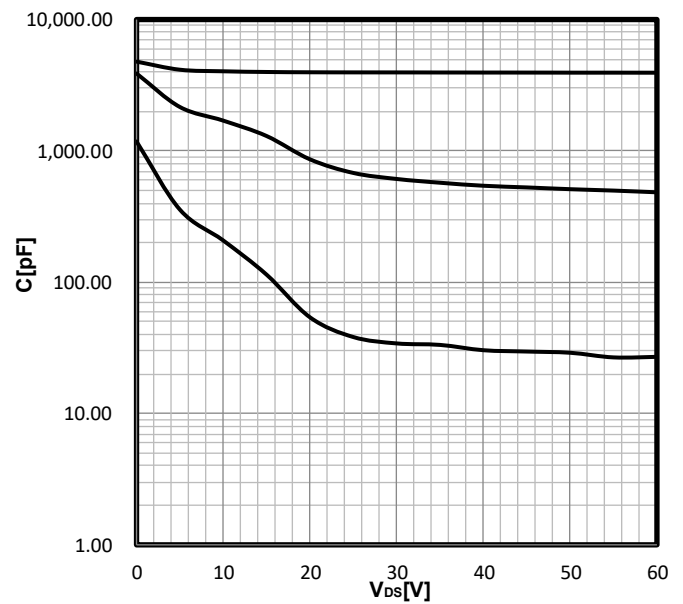
Drain-source breakdown voltage
 $V_{BR(DSS)}=f(T_j); I_D=-250\mu A$



Typ. gate charge
 $V_{GS}=f(Q_{gate}); I_D=-15A$



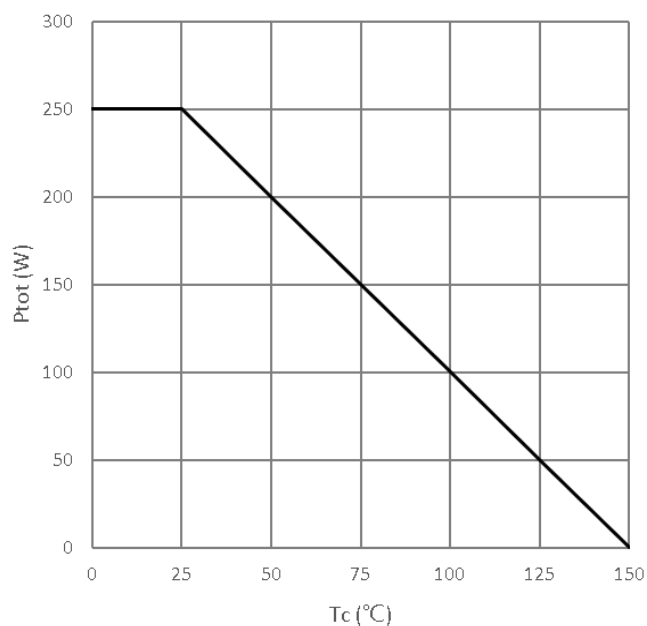
Typ. capacitances





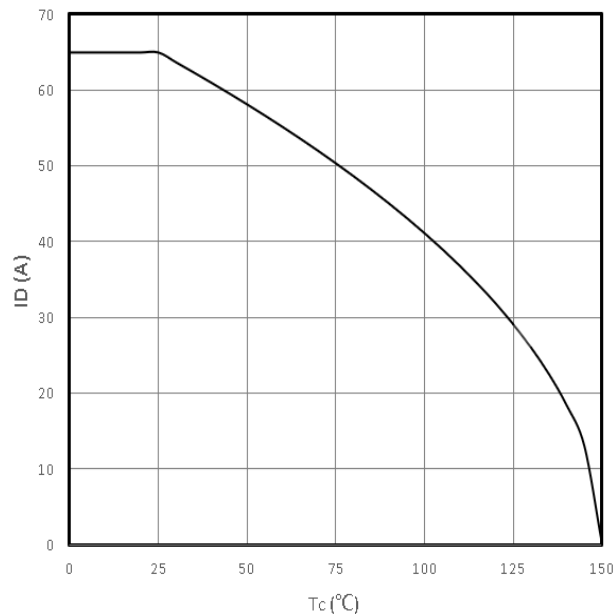
Power Dissipation

$$P_{tot}=f(T_C)$$



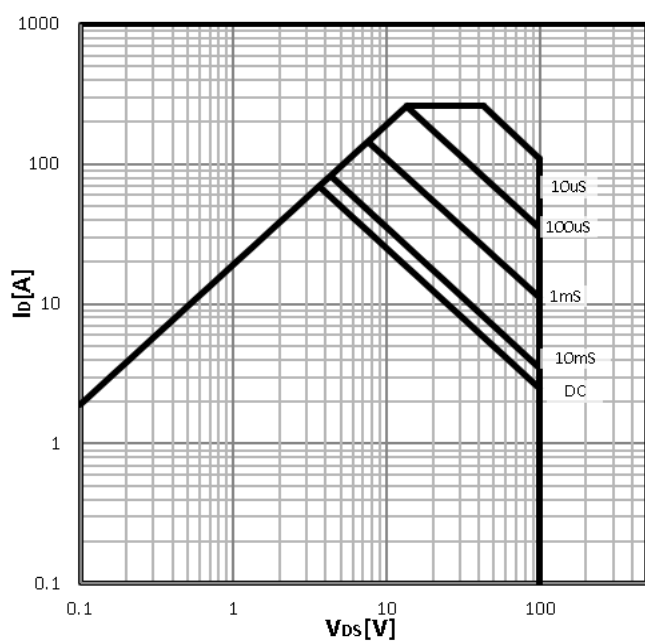
Maximum Drain Current

$$-I_D=f(T_C)$$



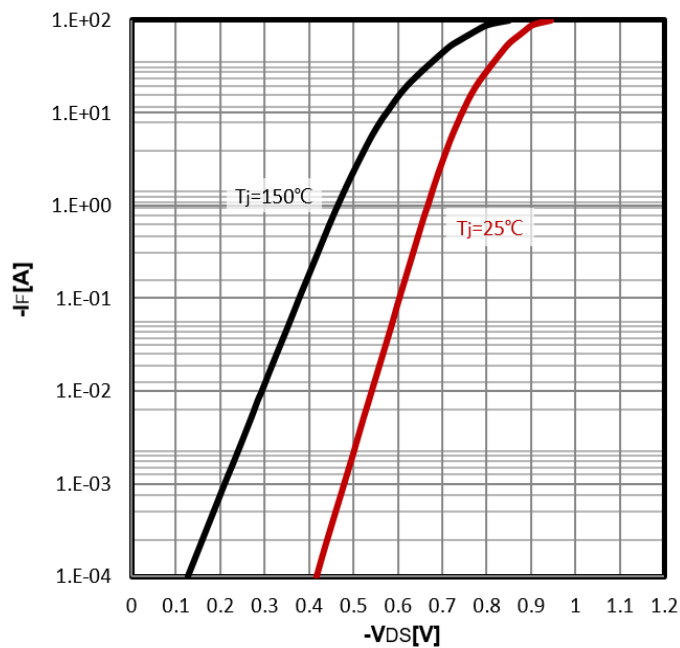
Safe operating area

$$-I_D=f(-V_{DS})$$



Body Diode Forward Voltage Variation

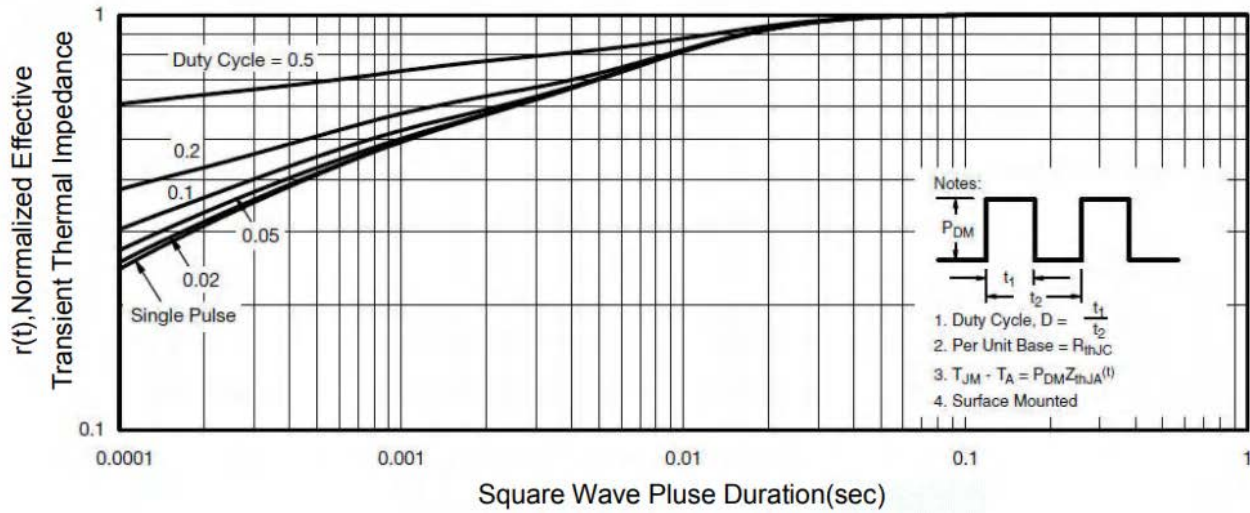
$$-I_F=f(-V_{DS})$$





Max. transient thermal impedance

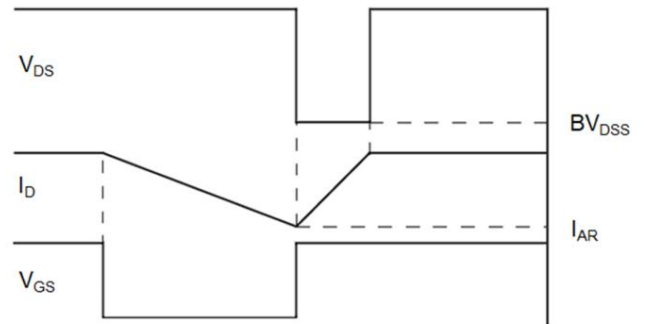
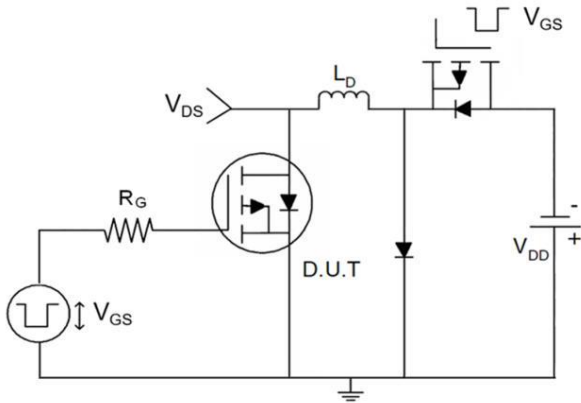
$$Z_{thJC}=f(t_p)$$



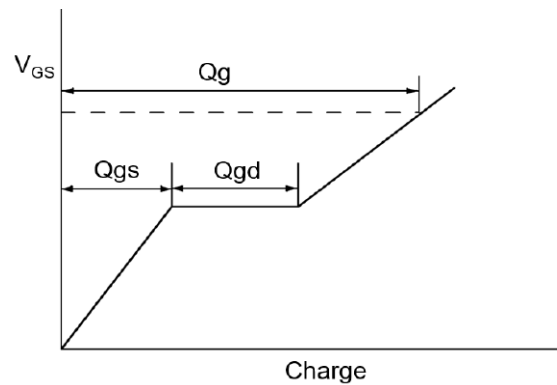
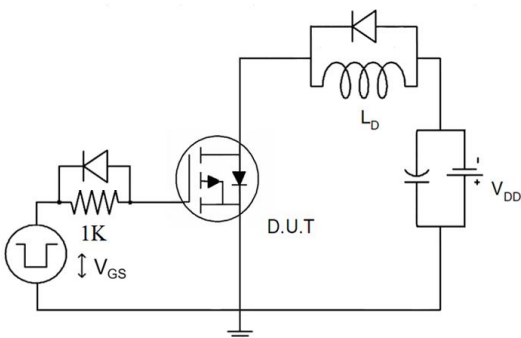


Test Circuit

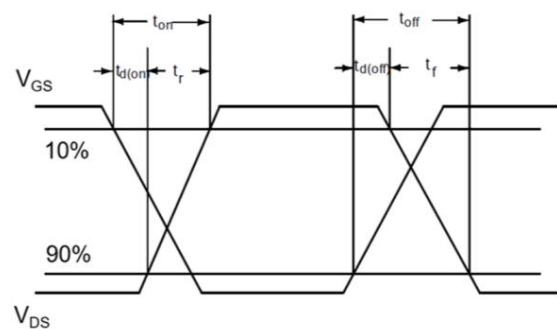
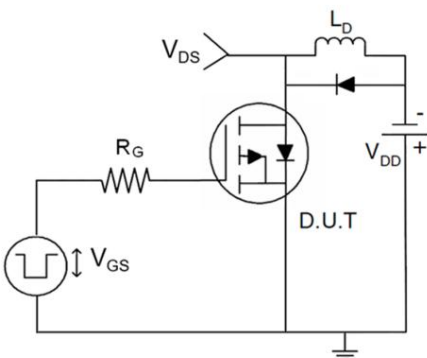
1) E_{AS} Test Circuits



2) Gate Charge Test Circuit



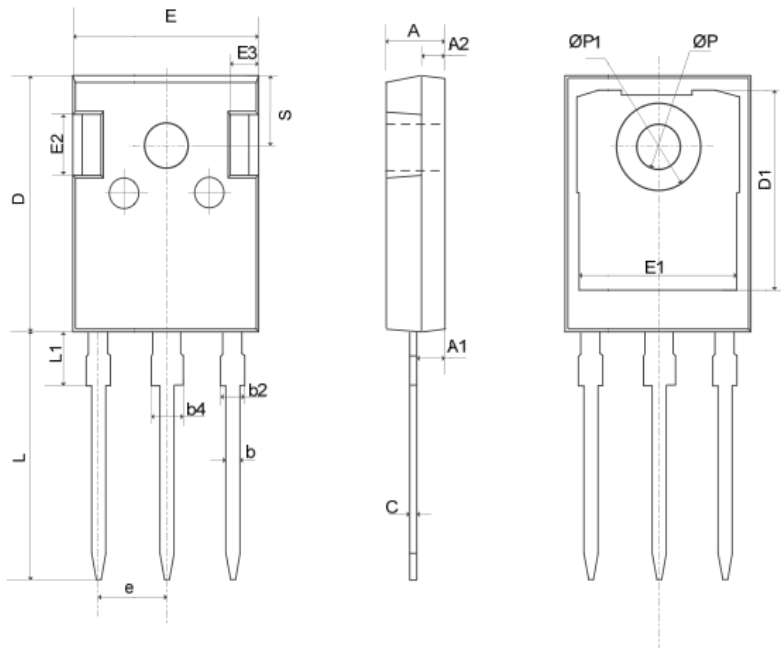
3) Switch Time Test Circuit





TO-247 Package Information

COMMON DIMENSIONS



SYMBOL	MM	
	MIN	MAX
A	4.80	5.20
A1	2.21	2.61
A2	1.85	2.15
b	1.11	1.36
b2	1.91	2.21
b4	2.91	3.21
c	0.51	0.75
D	20.70	21.30
D1	16.25	16.85
E	15.50	16.10
E1	13.00	13.60
E2	4.80	5.20
E3	2.30	2.70
e	5.44BSC	
L	19.62	20.22
L1	—	4.30
ØP	3.40	3.80
ØP1	—	7.30
S	6.15BSC	



Attention

- Any and all HUA XUAN YANG ELECTRONICS products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your HUA XUAN YANG ELECTRONICS representative nearest you before using any HUA XUAN YANG ELECTRONICS products described or contained herein in such applications.
- HUA XUAN YANG ELECTRONICS assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein.
- Specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- HUA XUAN YANG ELECTRONICS CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all HUA XUAN YANG ELECTRONICS products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of HUA XUAN YANG ELECTRONICS CO.,LTD.
- Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. HUA XUAN YANG ELECTRONICS believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the HUA XUAN YANG ELECTRONICS product that you intend to use.