



Description

The HXY70N07P uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.



TO-220

$V_{DS} = 70V, I_D = 70A$

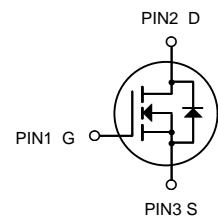
$R_{DS(ON)} < 8.5m\Omega$ @ $V_{GS}=10V$

Application

High efficiency switch mode power supplies

Power factor correction

Electronic lamp ballast



N-Channel MOSFET

Ordering Information

Product ID	Pack	Brand	Units Tube
HXY70N07P	TO-220	HXY MOSFET	50

Absolute Maximum Ratings@ $T_j=25^{\circ}C$ (unless otherwise specified)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	70	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	70	A
Drain Current-Continuous($T_C=100^{\circ}C$)	$I_D (100^{\circ}C)$	52	A
Pulsed Drain Current	I_{DM}	320	A
Maximum Power Dissipation	P_D	103	W
Single pulse avalanche energy	E_{AS}	110	mJ
Thermal Resistance,Junction-to-Case	$R_{\theta JC}$	1.46	°C/W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150	°C



Electrical Characteristics $T_J = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_{\text{D}}=250\mu\text{A}$	70		---	V
I_{DSS}	Zero Gate Voltage Drain Current	$\text{V}_{\text{GS}}=0\text{V}, \text{V}_{\text{DS}}=70\text{V}$	---	---	1	μA
I_{GSS}	Gate-Source Leakage Current	$\text{V}_{\text{GS}}=\pm 20\text{V}, \text{V}_{\text{DS}}=0\text{A}$	---	---	± 100	nA
$\text{V}_{\text{GS(th)}}$	GATE-Source Threshold Voltage	$\text{V}_{\text{GS}}=\text{V}_{\text{DS}}, \text{I}_{\text{D}}=250\mu\text{A}$	2	3	4	V
$\text{R}_{\text{DS(ON)}}$	Drain-Source On Resistance ^{note3}	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_{\text{D}}=30\text{A}$	---	7.5	8.5	$\text{m}\Omega$
C_{iss}	Input Capacitance	$\text{V}_{\text{DS}}=30\text{V}, \text{V}_{\text{GS}}=0\text{V}, \text{f}=1\text{MHz}$	---	3900	---	pF
C_{oss}	Output Capacitance		---	260	--	
C_{rss}	Reverse Transfer Capacitance		---	240	---	
$\text{t}_{\text{d(on)}}$	Turn-On Delay Time	$\text{V}_{\text{DS}}=30\text{V}, \text{I}_{\text{D}}=30\text{A}, \text{R}_{\text{GEN}}=3\Omega, \text{V}_{\text{GS}}=10\text{V}$	---	15	---	ns
t_r	Rise Time		---	90	---	ns
$\text{t}_{\text{d(off)}}$	Turn-Off Delay Time		---	45	---	ns
t_f	Fall Time		---	30	---	ns
Q_{g}	Total Gate Charge	$\text{V}_{\text{GS}}=10\text{V}, \text{V}_{\text{DS}}=30\text{V}, \text{I}_{\text{D}}=30\text{A}$	---	35	---	nC
Q_{gs}	Gate-Source Charge		---	10	---	nC
Q_{gd}	Gate-Drain "Miller" Charge		---	9	---	nC
V_{SD}	Source-Drain Diode Forward Voltage	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_{\text{S}}=30\text{A}$	---	---	1.2	V
I_{S}	Diode Forward Current	$\text{V}_{\text{D}}=\text{V}_{\text{G}}=0\text{V}$	---	---	70	A
I_{SM}	Plused Diode Forward Current	$\text{V}_{\text{D}}=\text{V}_{\text{G}}=0\text{V}$	---	---	320	A
T_{rr}	Reverse Recovery Time	$\text{I}_{\text{F}}=20\text{A}, \text{dI}/\text{dt}=100\text{A}/\text{us}$	---	78	---	NS
Q_{rr}	Reverse Recovery Charge	$\text{I}_{\text{F}}=20\text{A}, \text{dI}/\text{dt}=100\text{A}/\text{us}$	---	51	---	NC

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
2. EAS condition: $T_J=25^\circ\text{C}$, $\text{V}_{\text{DD}}=30\text{V}$, $\text{V}_{\text{G}}=10\text{V}$, $\text{R}_{\text{G}}=25\Omega$, $\text{L}=0.5\text{mH}$, $\text{I}_{\text{AS}}=21\text{A}$
3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$



Typical Characteristics

Figure 1: Output Characteristics

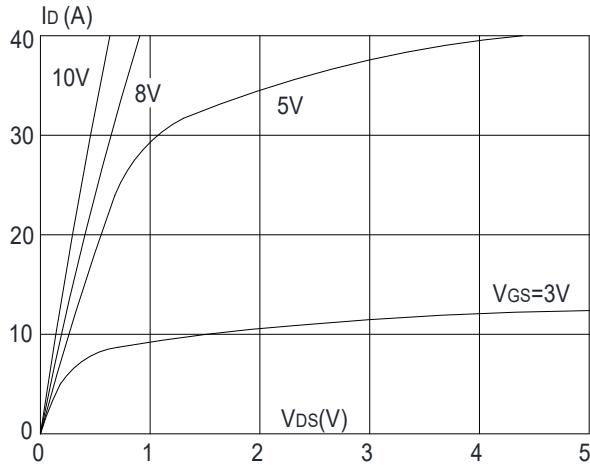


Figure 2: Typical Transfer Characteristics

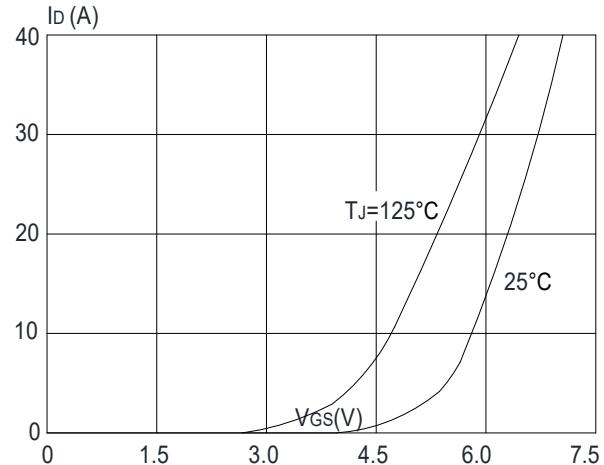


Figure 3: On-resistance vs. Drain Current

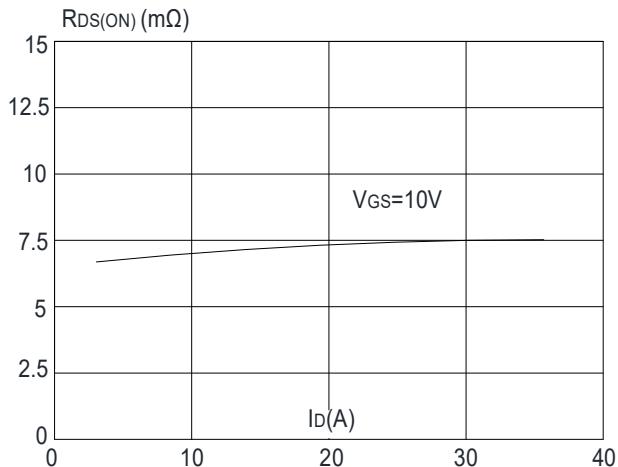


Figure 4: Body Diode Characteristics

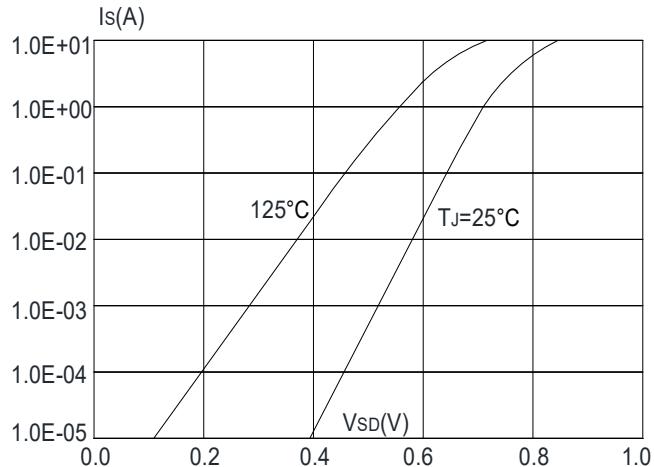


Figure 5: Gate Charge Characteristics

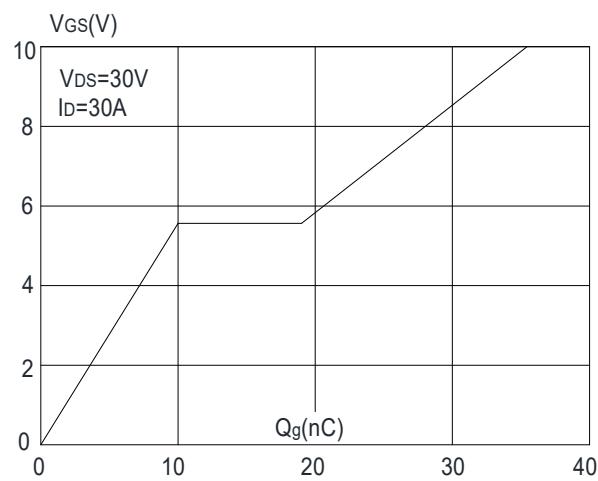


Figure 6: Capacitance Characteristics

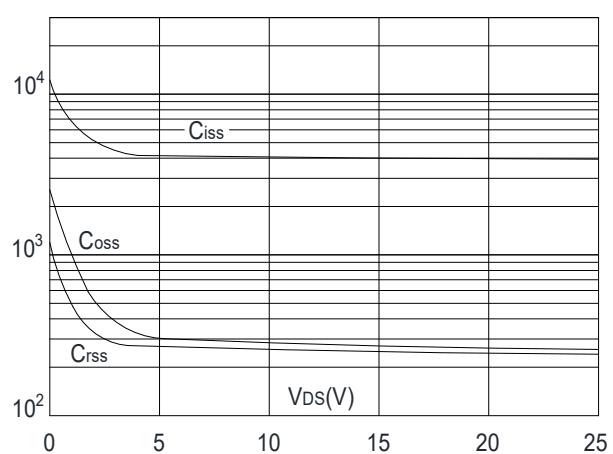




Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

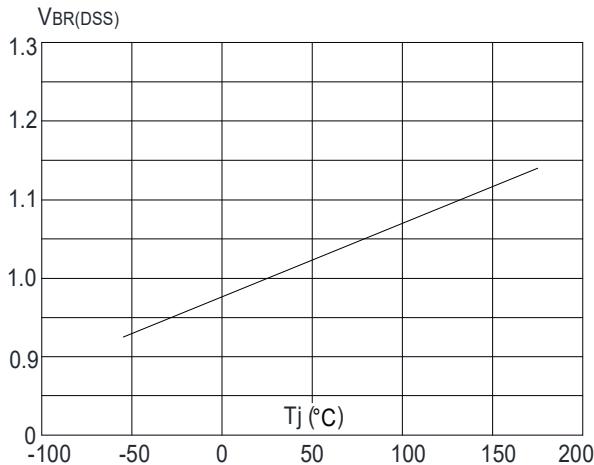


Figure 8: Normalized on Resistance vs. Junction Temperature

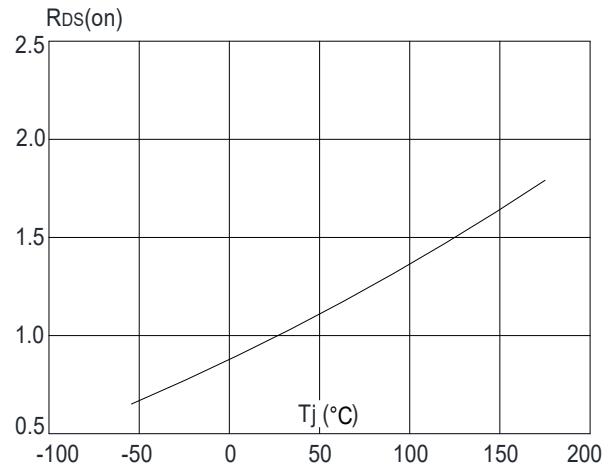


Figure 9: Maximum Safe Operating Area

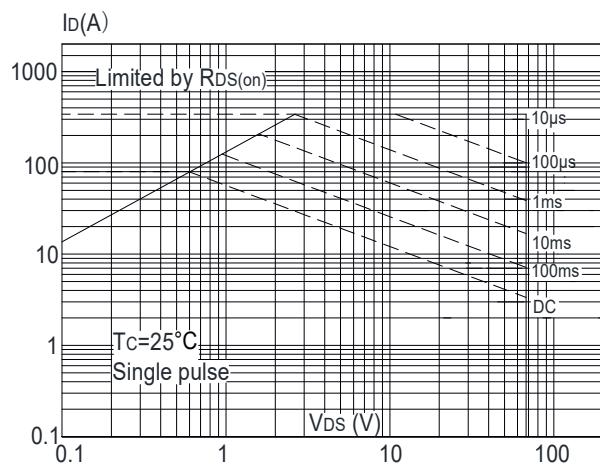


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

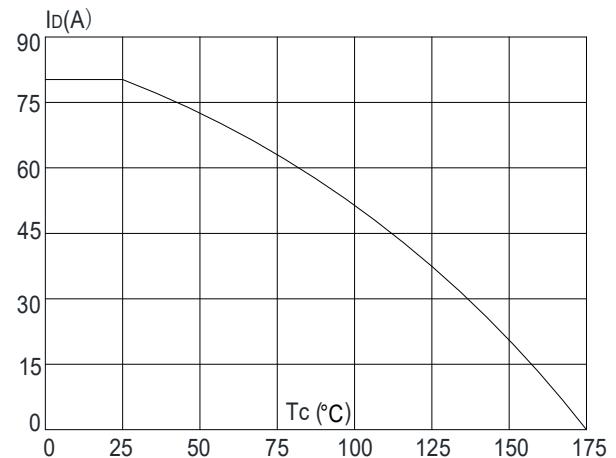
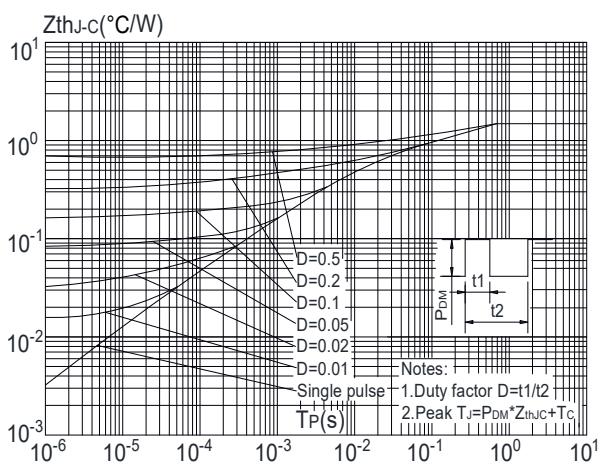
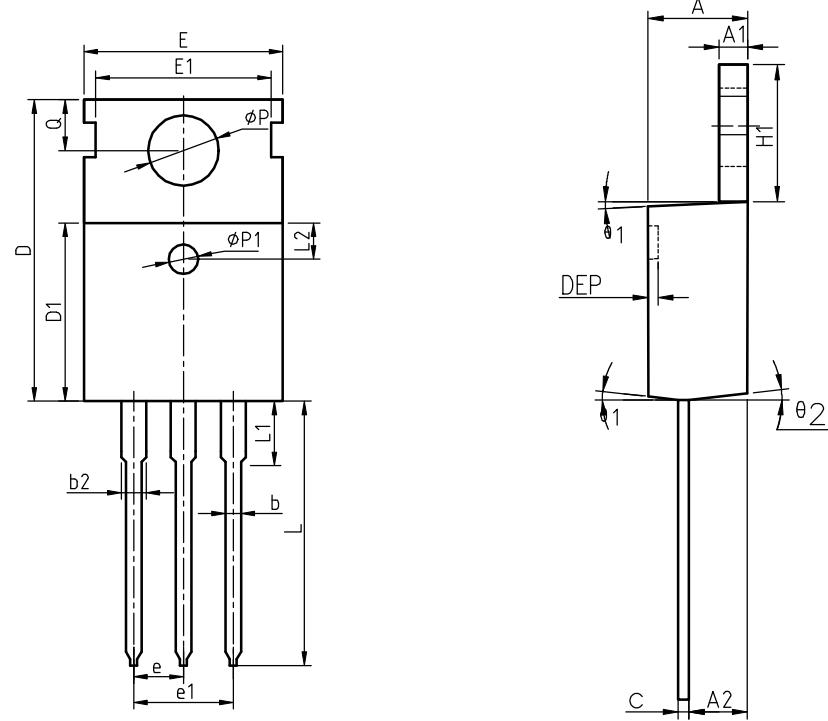


Figure 11: Maximum Effective Transient Thermal Impedance, Junction-to-Case

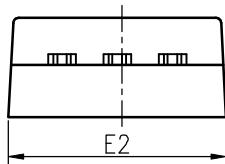




Package Information
TO-220



COMMON DIMENSIONS



SYMBOL	MIN	NOM	MAX	MIN	NOM	MAX
A	4.40	4.57	4.70	0.173	0.180	0.185
A1	1.27	1.30	1.33	0.050	0.051	0.052
A2	2.35	2.40	2.50	0.093	0.094	0.098
b	0.77	0.80	0.90	0.030	0.031	0.035
b2	1.17	1.27	1.36	0.046	0.050	0.054
c	0.48	0.50	0.56	0.019	0.020	0.022
D	15.40	15.60	15.80	0.606	0.614	0.622
D1	9.00	9.10	9.20	0.354	0.358	0.362
DEP	0.05	0.10	0.20	0.002	0.004	0.008
E	9.80	10.00	10.20	0.386	0.394	0.402
E1	-	8.70	-	-	0.343	-
E2	9.80	10.00	10.20	0.386	0.394	0.402
e		2.54	BSC		0.100	BSC
e1		5.08	BSC		0.200	BSC
H1	6.40	6.50	6.60	0.252	0.256	0.260
L	12.75	13.50	13.65	0.502	0.531	0.537
L1	-	3.10	3.30	-	0.122	0.130
L2		2.50	REF		0.098	REF
P	3.50	3.60	3.63	0.138	0.142	0.143
P1	3.50	3.60	3.63	0.138	0.142	0.143
Q	2.73	2.80	2.87	0.107	0.110	0.113
θ1	5°	7°	9°	5°	7°	9°
θ2	1°	3°	5°	1°	3°	5°
θ3	1°	3°	5°	1°	3°	5°



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