



## Description

The HXY11N40P uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.



**TO-220C  
(TO-220AB)**

## General Features

$V_{DS} = 420V$ ,  $I_D = 11A$

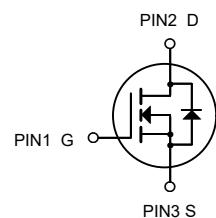
$R_{DS(ON)} < 0.5\Omega$  @  $V_{GS}=10V$

## Application

High efficiency switch mode power supplies

Power factor correction

Electronic lamp ballast



N-Channel MOSFET

## Ordering Information

Product ID	Pack	Brand	Units Tube
HXY11N40P	TO-220C(TO-220AB)	HXY MOSFET	50

## Absolute Maximum Ratings@ $T_j=25^\circ C$ (unless otherwise specified)

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	420	V
$V_{GS}$	Gate-Source Voltage	$\pm 30$	V
$I_D@T_c=25^\circ C$	Drain Current, $V_{GS} @ 4.5V$	11	A
$IDM$	Pulsed Drain Current <sup>1</sup>	44	A
$P_D@T_c=25^\circ C$	Total Power Dissipation	87	W
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_j$	Operating Junction Temperature Range	-55 to 150	°C



**Electrical Characteristics** ( $T_C = 25^\circ\text{C}$ , unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	420			V
Drain-Source Leakage Current	$\text{I}_{\text{DSS}}$	$\text{V}_{\text{DS}}=650\text{V}, \text{V}_{\text{GS}}=0\text{V}$			1	$\mu\text{A}$
Gate- Source Leakage Current	Forward	$\text{V}_{\text{G}}=30\text{V}, \text{V}_{\text{DS}}=0\text{V}$			100	nA
	Reverse	$\text{V}_{\text{GS}}=-30\text{V}, \text{V}_{\text{DS}}=0\text{V}$			-100	nA
<b>On Characteristics</b>						
Gate Threshold Voltage	$\text{V}_{\text{GS(TH)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$	2.0		4.0	V
Static Drain-Source On-State Resistance	$\text{R}_{\text{DS(ON)}}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=1\text{A}$		0.36	0.5	$\Omega$
<b>Dynamic Characteristics</b>						
Input Capacitance	$\text{C}_{\text{ISS}}$	$\text{V}_{\text{DS}}=25\text{V}, \text{V}_{\text{GS}}=0\text{V},$ $f=1.0 \text{ MHz}$		1368		pF
Output Capacitance	$\text{C}_{\text{OSS}}$			90.3		pF
Reverse Transfer Capacitance	$\text{C}_{\text{RSS}}$			3		pF
<b>Switching Characteristics</b>						
Turn-On Delay Time	$t_{\text{D(ON)}}$	$\text{V}_{\text{DD}}=250\text{V}, \text{I}_D=10\text{A},$ $\text{R}_G=25\Omega$ (Note 1, 2)		16		ns
Turn-On Rise Time	$t_{\text{R}}$			25		ns
Turn-Off Delay Time	$t_{\text{D(OFF)}}$			40		ns
Turn-Off Fall Time	$t_{\text{F}}$			29		ns
Total Gate Charge	$\text{Q}_{\text{G}}$	$\text{V}_{\text{DS}}=480\text{V}, \text{I}_D=12\text{A},$ $\text{V}_{\text{GS}}=10\text{V}$ (Note 1, 2)		8.1		nC
Gate-Source Charge	$\text{Q}_{\text{GS}}$			7.4		nC
Gate-Drain Charge	$\text{Q}_{\text{GD}}$			5		nC
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
Drain-Source Diode Forward Voltage	$\text{V}_{\text{SD}}$	$\text{V}_{\text{GS}} = 0 \text{ V}, \text{I}_S = 11\text{A}$			1.2	V
Maximum Continuous Drain-Source Diode Forward Current	$\text{I}_S$				11	A
Maximum Pulsed Drain-Source Diode Forward Current	$\text{I}_{\text{SM}}$				44	A
Reverse Recovery Time	$t_{\text{rr}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_S=12\text{A},$ $d\text{I}_F/dt=100 \text{ A}/\mu\text{s}$ (Note 1)		435		ns
Reverse Recovery Charge	$\text{Q}_{\text{RR}}$			4		$\mu\text{C}$

Notes: 1. Pulse Test: Pulse width  $\leq 300\mu\text{s}$ , Duty cycle  $\leq 2\%$ .

2. Essentially independent of operating temperature.



## Typical Characteristics

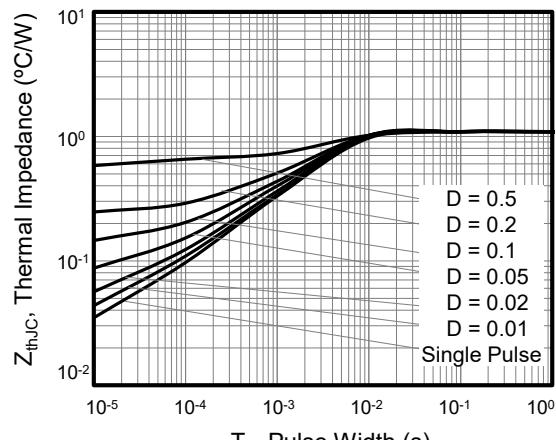


Figure 1. Transient Thermal Impedance

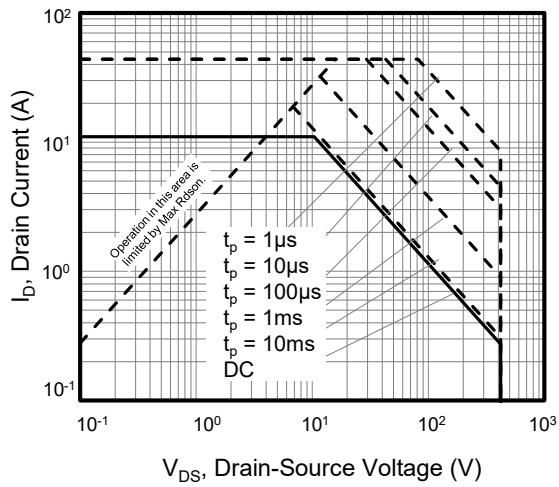


Figure 2. Safe Operation Area

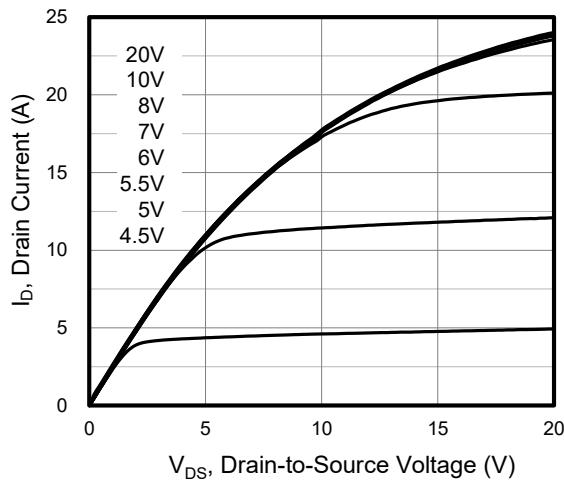


Figure 3. Output Characteristics

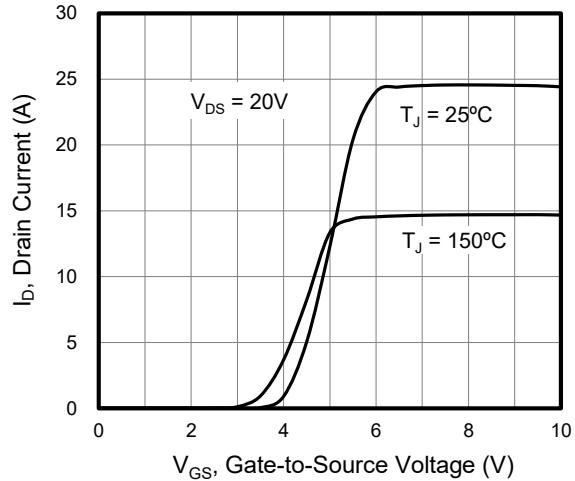


Figure 4. Transfer Characteristics

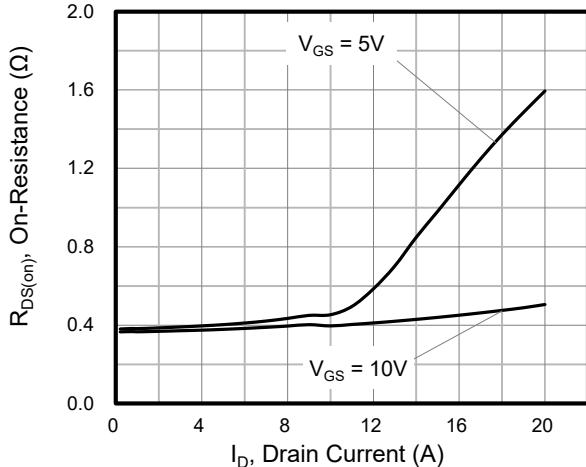


Figure 5. On-Resistance vs Drain Current

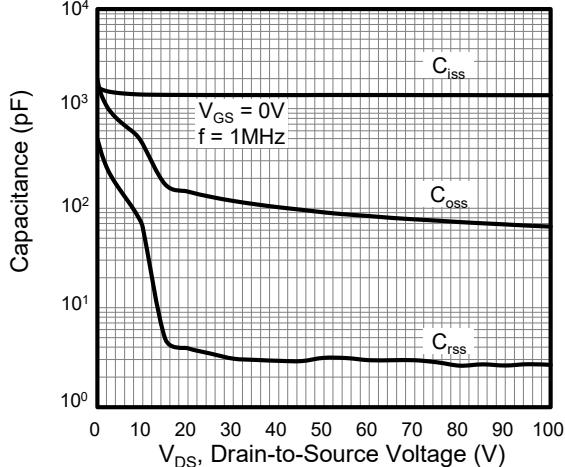
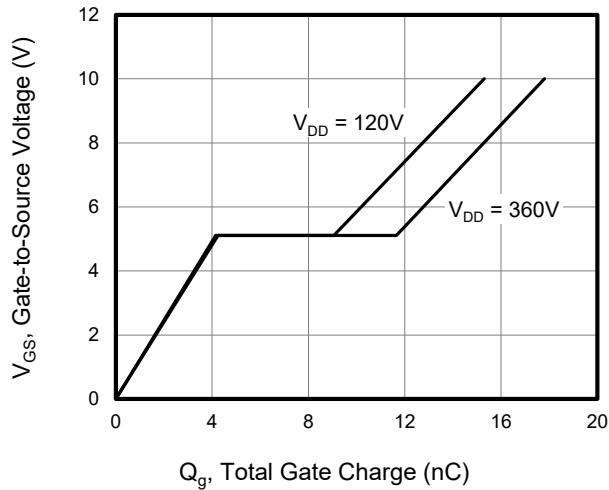
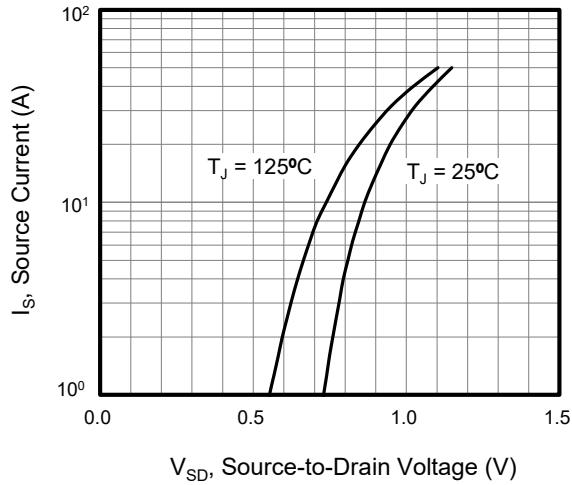


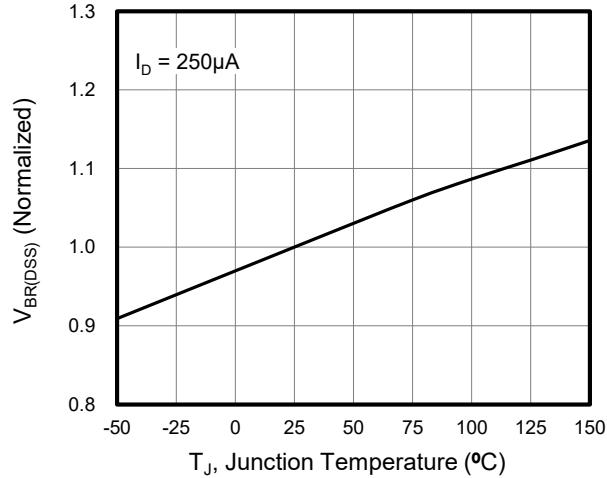
Figure 6. Capacitance



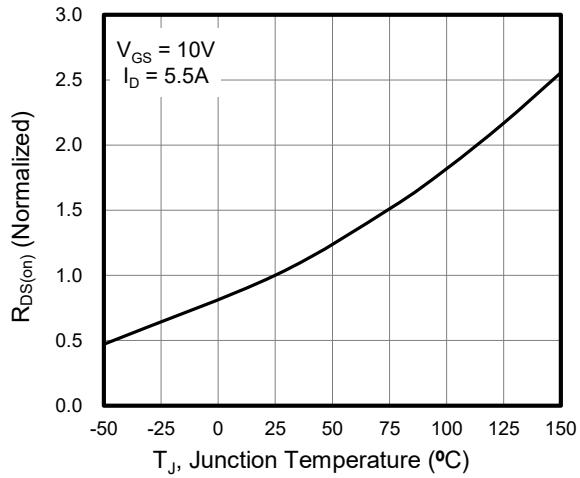
**Figure 7. Gate Charge**



**Figure 8. Body Diode Forward Voltage**



**Figure 9. Breakdown Voltage vs Junction Temperature**

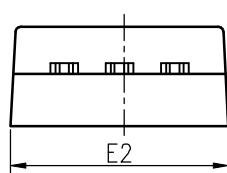
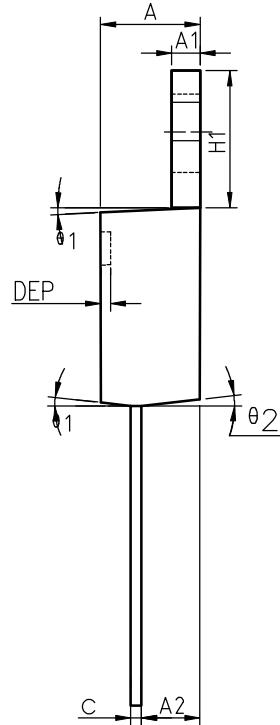
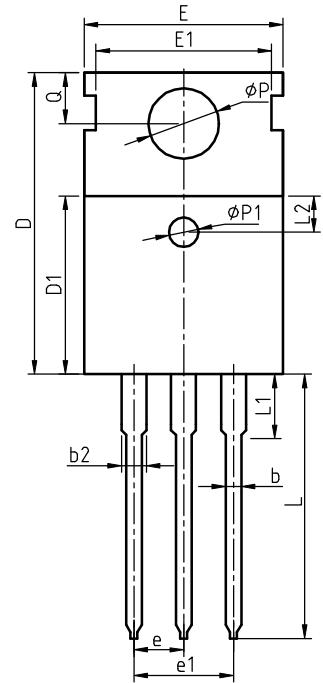


**Figure 10. On-Resistance vs Temperature**



## Package Information

### TO-220C(TO-220AB)



COMMON DIMENSIONS

SYMBOL	MIN	NOM	MAX	MIN	NOM	MAX
A	4.40	4.57	4.70	0.173	0.180	0.185
A1	1.27	1.30	1.33	0.050	0.051	0.052
A2	2.35	2.40	2.50	0.093	0.094	0.098
b	0.77	0.80	0.90	0.030	0.031	0.035
b2	1.17	1.27	1.36	0.046	0.050	0.054
c	0.48	0.50	0.56	0.019	0.020	0.022
D	15.40	15.60	15.80	0.606	0.614	0.622
D1	9.00	9.10	9.20	0.354	0.358	0.362
DEP	0.05	0.10	0.20	0.002	0.004	0.008
E	9.80	10.00	10.20	0.386	0.394	0.402
E1	-	8.70	-	-	0.343	-
E2	9.80	10.00	10.20	0.386	0.394	0.402
e		2.54	BSC		0.100	BSC
e1		5.08	BSC		0.200	BSC
H1	6.40	6.50	6.60	0.252	0.256	0.260
L	12.75	13.50	13.65	0.502	0.531	0.537
L1	-	3.10	3.30	-	0.122	0.130
L2		2.50	REF		0.098	REF
P	3.50	3.60	3.63	0.138	0.142	0.143
P1	3.50	3.60	3.63	0.138	0.142	0.143
Q	2.73	2.80	2.87	0.107	0.110	0.113
θ 1	5°	7°	9°	5°	7°	9°
θ 2	1°	3°	5°	1°	3°	5°
θ 3	1°	3°	5°	1°	3°	5°



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