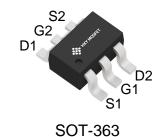


Description

The SI3439KDWA-TP uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.



General Features

 $V_{DS} = 20V I_{D} = 0.75A$

 $R_{DS(ON)}$ < 380m Ω @ V_{GS} =4.5V

 $V_{DS} = -20V I_{D} = -0.66A$

 $R_{DS(ON)}$ < 570 m Ω @ V_{GS} =-4.5V

Application

Wireless charging

Boost driver

Brushless motor

G1 G2 G2 S2

N-Channel MOSFET

P-Channel MOSFE

Package Marking and Ordering Information

0	•			
Product ID	Pack	Brand	Qty(PCS)	
SI3439KDWA-TP	SOT-363	HXY MOSFET	3000	

Absolute Maximum Ratings (T_c=25 ℃ unless otherwise noted)

O. mah al	B	Rati	I I a i i a	
Symbol	Parameter	N-Channel	P-Channel	Units
VDS	Drain-Source Voltage		-20	V
VGS	Gate-Source Voltage	±12	±12	V
I _D @T _A =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	0.75	-0.66	Α
IDM	Pulsed Drain Current ²	1.8	-1.2	Α
TSTG	Storage Temperature Range	-55 to 150	-55 to 150	°C
TJ	T _J Operating Junction Temperature Range		-55 to 150	°C
R _θ JA	Thermal Resistance Junction-Ambient ¹	83	33	°C/W



N-ch MOSFET Electrical Characteristics (T_a=25°C unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
Static Characterisitcs						
Drain-source breakdown voltage	V(BR)DSS	Vgs = 0V, Ip =250µA	20			V
Zero gate voltage drain current	Idss	Vps =20V,Vgs = 0V			1	μΑ
Gate-body leakage current	Igss	$Vgs = \pm 10V$, $Vds = 0V$			±20	uA
Gate threshold voltage (note 2)	VGS(th)	V _{DS} =V _{GS} , I _D =250μA	0.35		1.1	V
Drain aguras en registance/nete 2)	D-ac	Vgs =4.5V, ID =0.65A		210	380	mΩ
Orain-source on-resistance(note 2)	RDS(on)	Vgs =2.5V, Ip =0.55A		320	450	mΩ
		Vgs =1.8V, Ip =0.45A		390	800	mΩ
Forward tranconductance(note 2)	grs	Vps =10V, Ip =0.8A		1.6		S
Diode forward voltage	V_{SD}	I _S =0.15A, V _G S = 0V			1.2	V
Dynamic Characteristics (note 4)						
Input Capacitance	C _{iss}			79	120	рF
Output Capacitance	Coss	Vps =16V,Vgs =0V,f =1MHz		13	20	pF
Reverse Transfer Capacitance	C _{rss}]		9	15	pF
Switching Characteristics (note 3,4)						
Turn-on delay time	td(on)			6.7		ns
Turn-on rise time	tr	V _{GS} =4.5V,V _{DS} =10V,		4.8		ns
Turn-off delay time	td(off)	$I_D=500$ mA, $R_{GEN}=10\Omega$		17.3		ns
Turn-off fall time	tf			7.4		ns

P-ch MOSFET Electrical Characteristics (T_a=25°C unless otherwise noted)

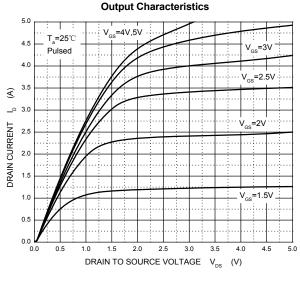
Parameter	Symbol	Test Condition	Min	Тур	Max	Unit	
Static Characteristics							
Drain-source breakdown voltage	V(BR)DSS	Vgs = 0V, Ip =-250µA	-20			V	
Zero gate voltage drain current	Idss	Vps =-20V,Vgs = 0V			-1	μΑ	
Gate-body leakage current	Igss	Vgs =±10V, Vps = 0V			±20	uA	
Gate threshold voltage (note 2)	VGS(th)	$V_{DS} = V_{GS}$, $I_{D} = -250 \mu A$	-0.35		-1.1	V	
Drain aguras en registance/nete 2)	D-a()	Vgs =-4.5V, ID =-1A		430	570	mΩ	
Drain-source on-resistance(note 2)	RDS(on)	Vgs =-2.5V, ID =-0.8A		624	700	mΩ	
		Vgs =-1.8V, ID =-0.5A		950		mΩ	
Forward tranconductance(note 2)	g FS	Vps =-10V, Ip =-0.54A		1.2		S	
Diode forward voltage	V _{SD}	I _S =-0.5A, V _G S = 0V			-1.2	V	
Dynamic Characteristics (note 4)							
Input Capacitance	C _{iss}			113	170	рF	
Output Capacitance	Coss	Vps =-16V,Vgs =0V,f =1MHz		15	25	pF	
Reverse Transfer Capacitance	C_{rss}			9	15	pF	
Switching Characteristics (note 3, 4)							
Turn-on delay time	t d(on)			9		ns	
Turn-on rise time	tr	V _{GS} =-4.5V,V _{DS} =-10V,		5.8		ns	
Turn-off delay time	td(off)	I_D =-200mA, R_{GEN} =10 Ω		32.7		ns	
Turn-off fall time	tf			20.3		ns	

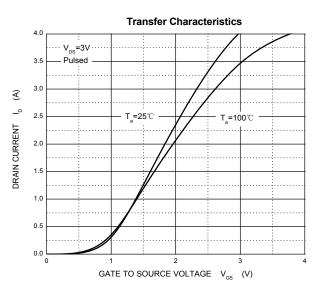
Notes:

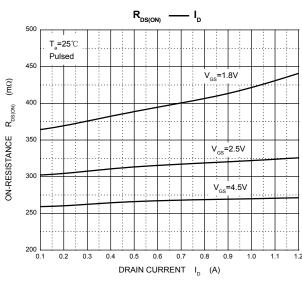
- 1. Surface mounted on FR4 board using the minimum recommended pad size.
- 2. Pulse Test : Pulse width=300µs, duty cycle≤2%.
- 3. Switching characteristics are independent of operating junction temperature.
- 4. Graranted by design, not subject to producting.

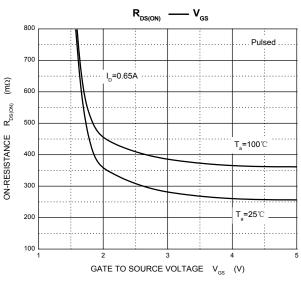


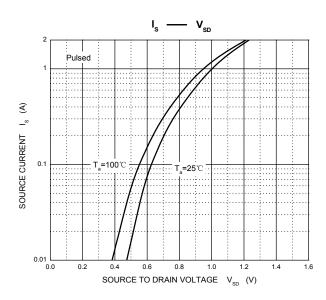
N-Channel Typical Characteristics

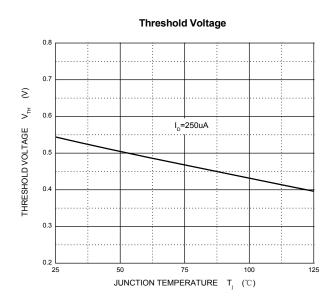






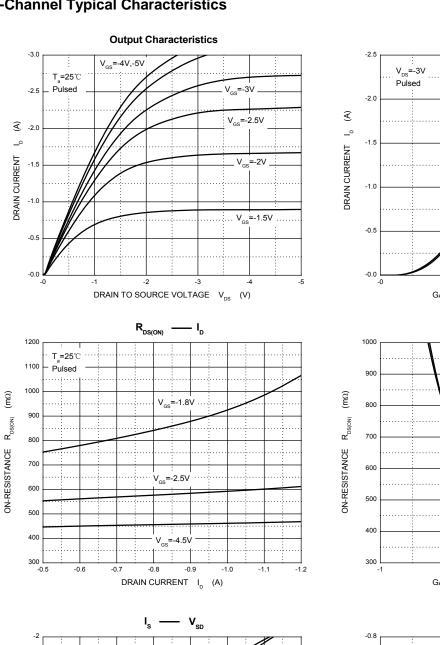


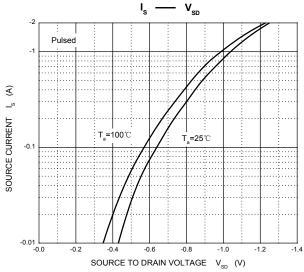


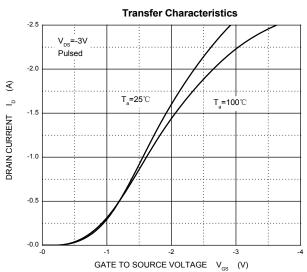


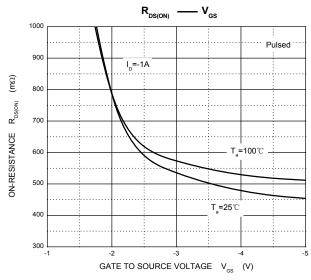


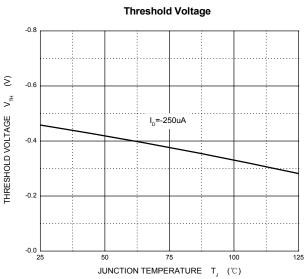
P-Channel Typical Characteristics





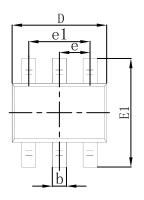


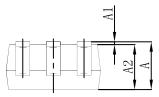


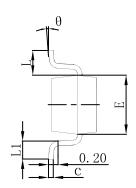




SOT-363 Package Outline Dimensions

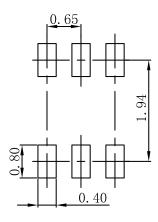






Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Syllibol	Min	Max	Min	Max	
Α	0.900	1.100	0.035	0.043	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.000	0.035	0.039	
b	0.150	0.350	0.006	0.014	
С	0.100	0.150	0.004	0.006	
D	2.000	2.200	0.079	0.087	
E	1.150	1.350	0.045	0.053	
E1	2.150	2.400	0.085	0.094	
е	0.650 TYP		0.026 TYP		
e1	1.200	1.400	0.047	0.055	
L	0.525 REF		0.021	REF	
L1	0.260	0.460	0.010	0.018	
θ	0°	8°	0°	8°	

SOT-363 Suggested Pad Layout



Note:

- 1. Controlling dimension: in millimeters.
- 2.General tolerance:± 0.05mm.
- 3. The pad layout is for reference purposes only.

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