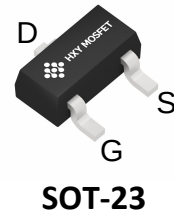




Description

The HXY5P04I uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

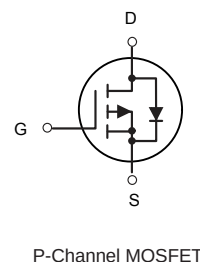


General Features

$V_{DS} = -40V$ $I_D = -5A$
 $R_{DS(ON)} < 55m\Omega @ V_{GS} = -10V$
 $R_{DS(ON)} < 88m\Omega @ V_{GS} = -4.5V$

Application

Battery protection
Load switch
Uninterruptible power supply



Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
HXY5P04I	SOT-23		3000

Absolute Maximum Ratings ($T_A = 25^\circ C$ unless otherwise noted)

Symbol	Parameter	Limit	Unit
V_{DS}	Drain-Source Voltage	-40	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current-Continuous	-5	A
I_{DM}	Drain Current-Pulsed (Note 1)	-22	A
P_D	Maximum Power Dissipation	2	W
T_J, T_{STG}	Operating Junction and Storage Temperature Range	-55 To 150	$^\circ C$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient (Note 2)	65	$^\circ C/W$



Electrical Characteristics (T_J=25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D = -250μA	-40	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = -40V, V _{GS} =0V	-	-	-1	μA
I _{GSS}	Gate to Body Leakage Current	V _{DS} =0V, V _{GS} = ±20V	-	-	±100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D = -250μA	-1.0	-1.6	-2.5	V
R _{DS(on)}	Static Drain-Source on-Resistance Note2	V _{GS} = -10V, I _D = -5A	-	47	55	mΩ
		V _{GS} = -4.5V, I _D = -4A	-	62	88	
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = -20V, V _{GS} =0V, f=1.0MHz	-	869	-	pF
C _{oss}	Output Capacitance		-	94	-	pF
C _{rss}	Reverse Transfer Capacitance		-	69	-	pF
Q _g	Total Gate Charge	V _{DS} = -20V, I _D = -4A, V _{GS} = -10V	-	17.3	-	nC
Q _{gs}	Gate-Source Charge		-	3.2	-	nC
Q _{gd}	Gate-Drain(“Miller”) Charge		-	4.3	-	nC
Switching Characteristics						
t _{d(on)}	Turn-on Delay Time	V _{DS} = -20V, I _D = -4A, V _{GS} = -10V, R _{GEN} =3Ω	-	10.3	-	ns
t _r	Turn-on Rise Time		-	4.3	-	ns
t _{d(off)}	Turn-off Delay Time		-	39	-	ns
t _f	Turn-off Fall Time		-	46.5	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	- 5	A
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-22	A
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} =0V, I _S = -5.5A	-	-0.8	-1.2	V
trr	Reverse Recovery Time	V _{GS} =0V, I _S = -5.5A,	-	17	-	ns
Qrr	Reverse Recovery Charge	di/dt=100A/μs	-	11.5	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. Pulse Test: Pulse Width≤300μs, Duty Cycle≤2%



Typical Performance Characteristics

Figure1: Output Characteristics

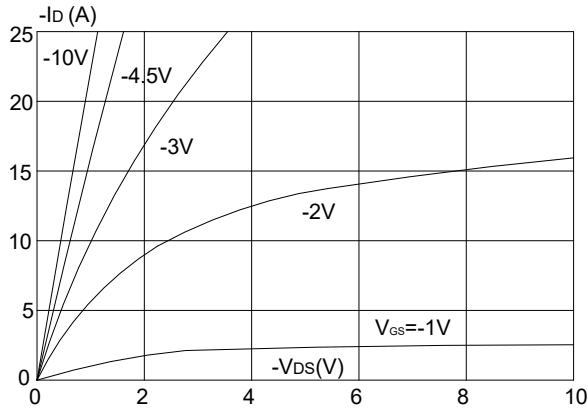


Figure 2: Typical Transfer Characteristics

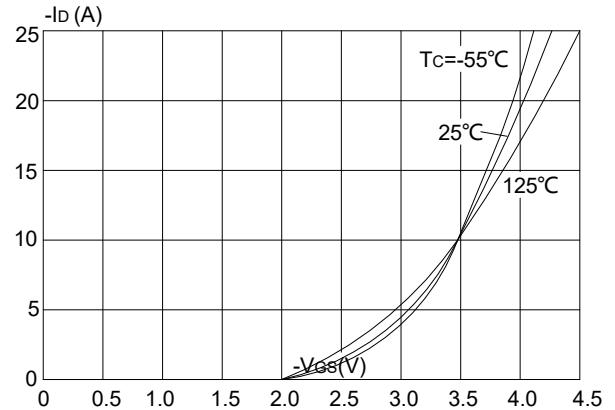


Figure 3: On-resistance vs. Drain Current

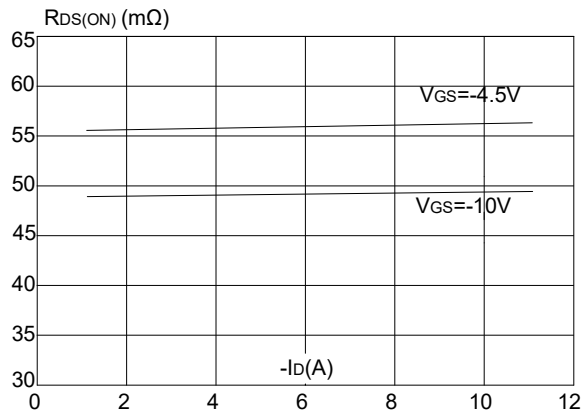


Figure 4: Body Diode Characteristics

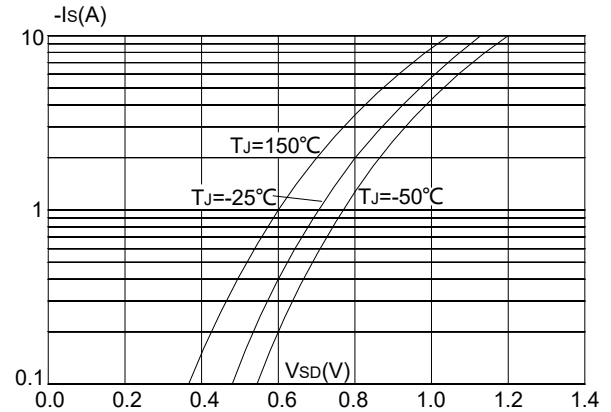


Figure 5: Gate Charge Characteristics

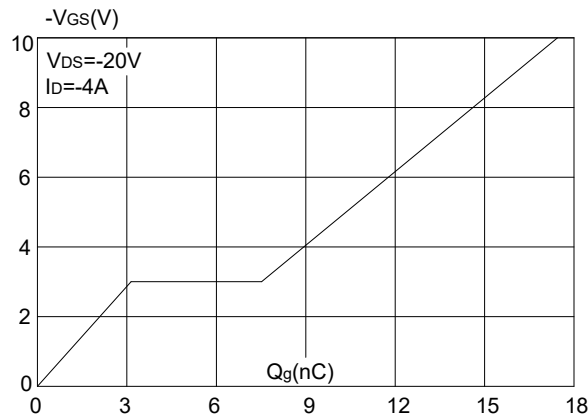


Figure 6: Capacitance Characteristics

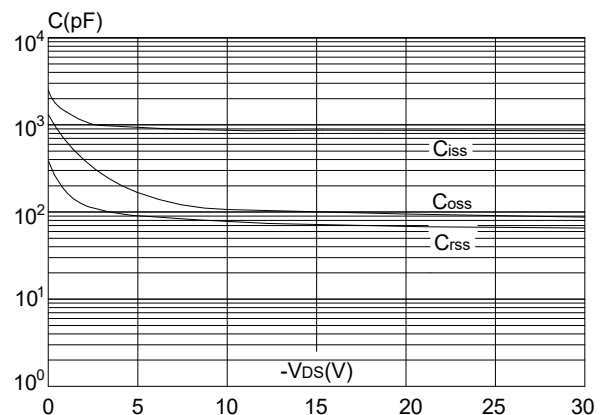




Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

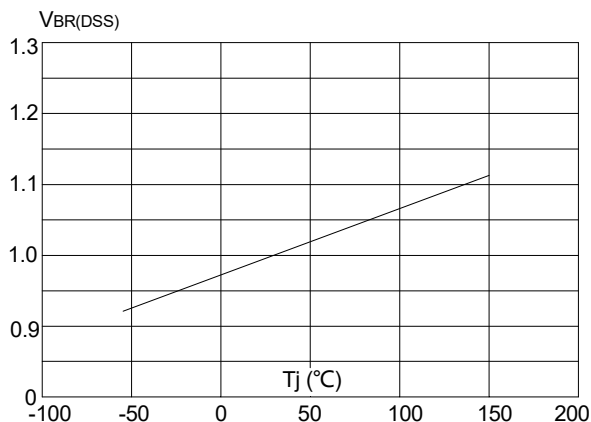


Figure 8: Normalized on Resistance vs. Junction Temperature

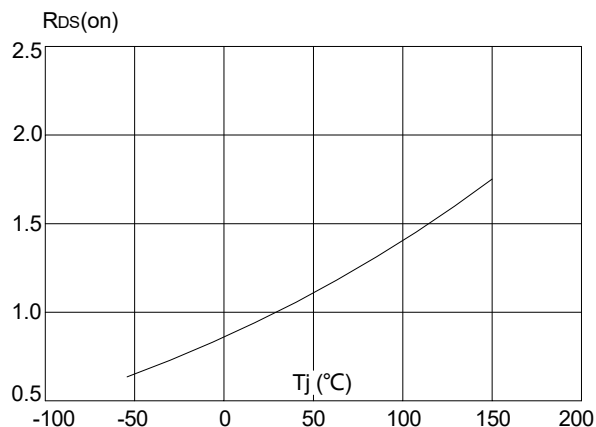


Figure 9: Maximum Safe Operating Area

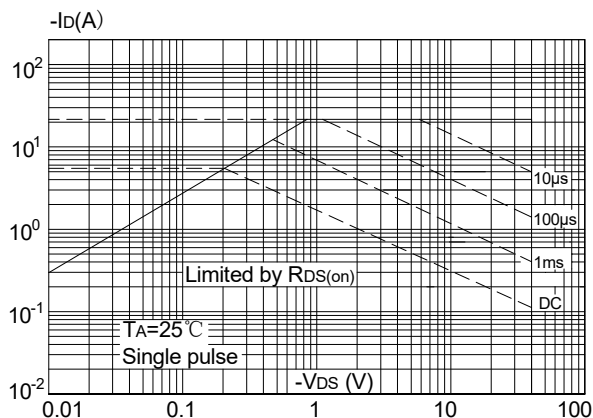


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

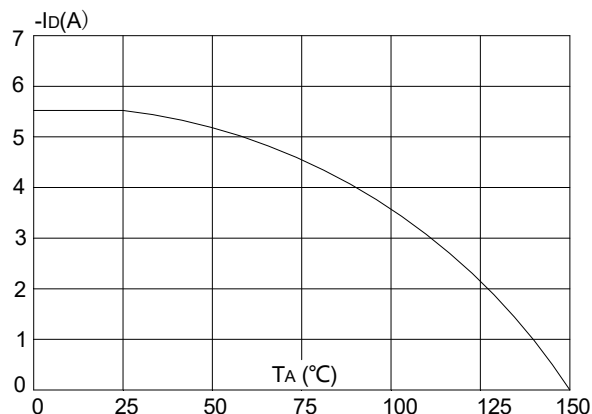
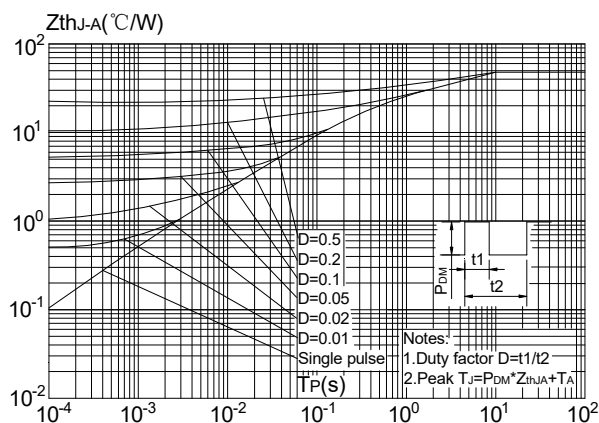
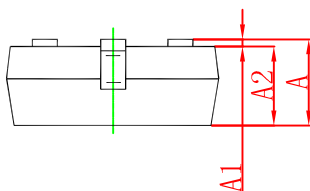
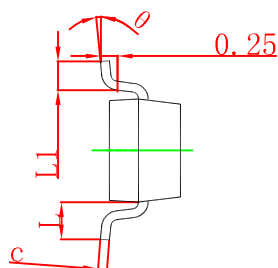
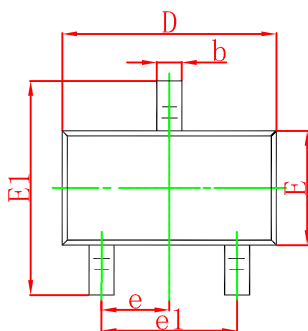


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



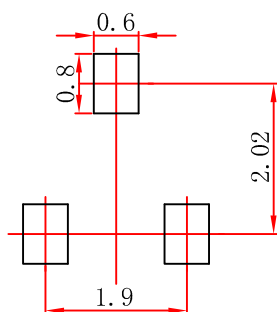


SOT-23 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP		0.037 TYP	
e1	1.800	2.000	0.071	0.079
L	0.550 REF		0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°

SOT-23 Suggested Pad Layout



Note:
1. Controlling dimension: in millimeters.
2. General tolerance: $\pm 0.05\text{mm}$.
3. The pad layout is for reference purposes only.



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