

Description

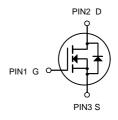
The HSI2306BDST1E3 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.



General Features

 $V_{DS} = 30V I_{D} = 5.8A$

 $R_{DS(ON)} < 30 m\Omega$ @ $V_{GS}=10V$



Application

Battery protection

Load switch

Uninterruptible power supply

N-Channel MOSFET

Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
HSI2306BDST1E3	SOT-23	A09T XXXX	3000

Absolute Maximum Ratings (T_A=25 ℃ unless otherwise noted)

Symbol	Parameter	Limit	Unit
V _{DS}	Drain-Source Voltage	30	V
Vgs	Gate-Source Voltage	±12	V
I _D	Drain Current-Continuous	5.8	Α
Ірм	Drain Current-Pulsed (Note 1)	30	Α
P _D	Maximum Power Dissipation	1.4	W
TJ,Tstg	Operating Junction and Storage Temperature Range	-55 To 150	$^{\circ}$
Rеja	Thermal Resistance, Junction-to-Ambient (Note 2)	89	°C/W

Electrical Characteristics (T_A=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	30	33	-	V
Zero Gate Voltage Drain Current	IDSS	V _{DS} =30V,V _{GS} =0V		-	1	μA
Gate-Body Leakage Current	Igss			-	±100	nA
Gate Threshold Voltage	VGS(th)	V_{DS} = V_{GS} , I_D =250 μ A	0.7	0.9	1.4	V
		V _{GS} =2.5V, I _D =4A	-	41	55	mΩ
Drain-Source On-State Resistance	RDS(ON)	V _{GS} =4.5V, I _D =5A	-	32	42	mΩ
		V _{GS} =10V, I _D =5.8A	-	28	30	mΩ
Forward Transconductance	grs	V _{DS} =5V,I _D =5A	10	-	-	S
Input Capacitance	Clss	V _{DS} =15V,V _{GS} =0V,	-	825	-	PF
Output Capacitance	Coss		-	100	-	PF
Reverse Transfer Capacitance	Crss	F=1.0MHz	-	78	-	PF
Turn-on Delay Time	td(on)	V _{DD} =15V, R _L =2.7Ω	-	3.3	-	nS
Turn-on Rise Time	tr		-	4.8	-	nS
Turn-Off Delay Time	t _{d(off)}	$V_{GS}\text{=}10V\text{,}R_{GEN}\text{=}3\Omega$	-	26	-	nS
Turn-Off Fall Time	t _f		-	4	-	nS
Total Gate Charge	Qg		-	10	-	nC
Gate-Source Charge	Q _{gs}	V _{DS} =15V,I _D =5.8A,	-	1.6	-	nC
Gate-Drain Charge	Q _{gd}	V _{GS} =4.5V	-	3.1	-	nC
Diode Forward Voltage (Note 3)	VsD	V _{GS} =0V,I _S =5.8A	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	5.8	Α

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- **3.** Pulse Test: Pulse Width $\leq 300 \mu s$, Duty Cycle $\leq 2\%$.
- 4. Guaranteed by design, not subject to production

Typical Electrical and Thermal Characteristics

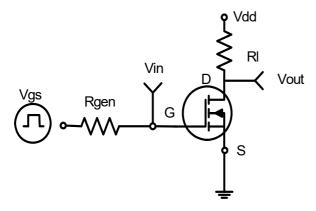


Figure 1:Switching Test Circuit

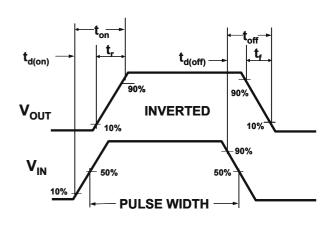


Figure 2:Switching Waveforms

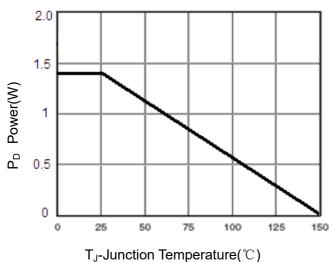


Figure 3 Power Dissipation

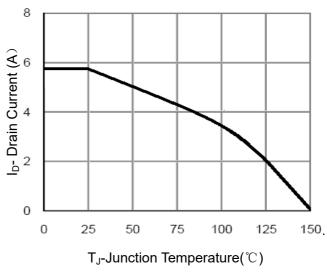


Figure 4 Drain Current

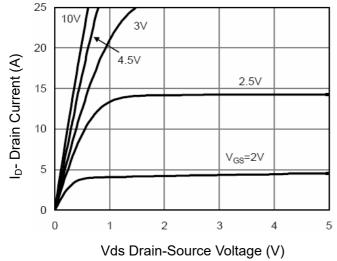


Figure 5 Output Characteristics

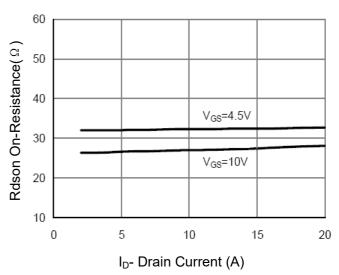
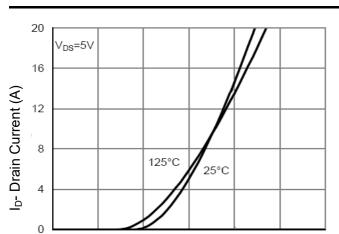


Figure 6 Drain-Source On-Resistance

0.5

0

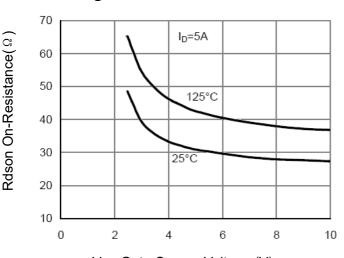


Vgs Gate-Source Voltage (V)
Figure 7 Transfer Characteristics

1.5

2.5

3



Vgs Gate-Source Voltage (V)
Figure 9 Rdson vs Vgs

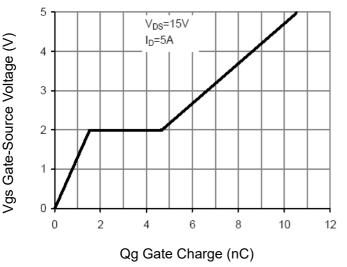


Figure 11 Gate Charge

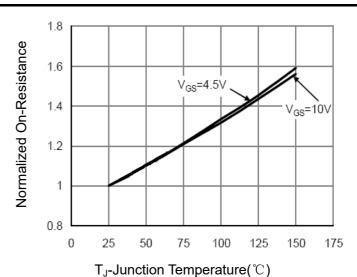


Figure 8 Drain-Source On-Resistance

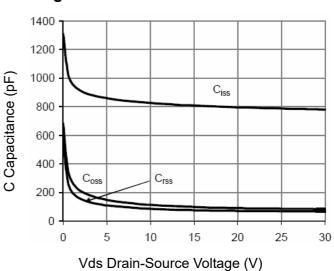


Figure 10 Capacitance vs Vds

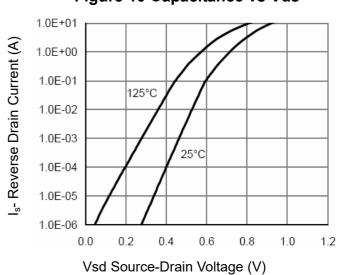
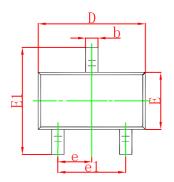
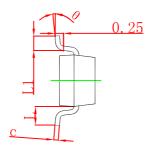


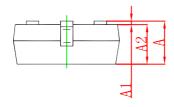
Figure 12 Source- Drain Diode Forward



SOT-23 Package Outline Dimensions

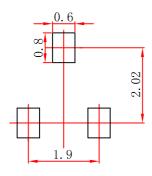






Symbol	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	0.900	1.150	0.035	0.045	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.050	0.035	0.041	
b	0.300	0.500	0.012	0.020	
С	0.080	0.150	0.003	0.006	
D	2.800	3.000	0.110	0.118	
Е	1.200	1.400	0.047	0.055	
E1	2.250	2.550	0.089	0.100	
е	0.950 TYP 0.037 TYP		7 TYP		
e1	1.800	2.000	0.071	0.079	
L	0.550 REF		0.022 REF		
L1	0.300	0.500	0.012	0.020	
θ	0°	8°	0°	8°	

SOT-23 Suggested Pad Layout



- Note:
 1.Controlling dimension:in millimeters.
- 2.General tolerance:± 0.05mm.
 3.The pad layout is for reference purposes only.



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