

Description

The SSM3J334R,LF(T uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V.This device is suitable for use as a Battery protection or in other Switching application.



General Features

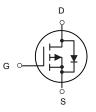
 $V_{DS} = -30V, I_{D} = -4.1A$

 $R_{DS(ON)}$ <55m Ω @ V_{GS} =10V

(SOT-23F)

Application

High power and current handing capability
Lead free product is acquired
Surface mount package
PWM applications
Load switch
Power management



P-Channel MOSFET

Package Marking and Ordering Information

Product ID	Pack	Brand	Qty(PCS)
SSM3J334R,LF(T	SOT-23-3L(SOT-23F)	HXY MOSFET	3000

Absolute Maximum Ratings (T_A=25 ℃ unless otherwise noted)

Symbol	Parameter	Limit	Unit
V _{DS}	Drain-Source Voltage	-30	V
V _G s	Gate-Source Voltage	±20	V
ID	Drain Current-Continuous	-4.1	А
Ірм	Drain Current-Pulsed (Note 1)	-13	А
Po	Maximum Power Dissipation	1.32	W
T _J ,T _{STG}	Operating Junction and Storage Temperature Range		°C
Reja	Thermal Resistance,Junction-to-Ambient (Note 2)		°C/W



Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V_{GS} =0 V , I_D =-250 u A	-30			V
△BV _{DSS} /△T _J	BVDSS Temperature Coefficient	Reference to 25°C , I _D =-1mA		-0.02		V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V_{GS} =-10V , I_D =-3A		42	55	mΩ
		V _{GS} =-4.5V , I _D =-1.5A		90	98	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =-250uA	-1.2	-1.5	-2.5	V
△V _{GS(th)}	V _{GS(th)} Temperature Coefficient			4.32		mV/°C
	Drain-Source Leakage Current	V _{DS} =-24V , V _{GS} =0V , T _J =25°C			-1	uA
I _{DSS}		V _{DS} =-24V , V _{GS} =0V , T _J =55°C			-5	
Igss	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V			±100	nA
gfs	Forward Transconductance	V _{DS} =-5V , I _D =-3A		4.8		S
Rg	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz		24	48	Ω
Qg	Total Gate Charge (-4.5V)	V _{DS} =-20V , V _{GS} =-4.5V , I _D =-3A		5.22	7.3	nC
Qgs	Gate-Source Charge V			1.25	1.8	
Q _{gd}	Gate-Drain Charge			2.3	3.2	
T _{d(on)}	Turn-On Delay Time			18.4	37	
Tr	Rise Time	V_{DD} =-15V , V_{GS} =-10V , R_{G} =3.3 Ω		11.4	21	ns
T _{d(off)}	Turn-Off Delay Time			39.4	79	
Tf	Fall Time			5.2	10.4	
C _{iss}	Input Capacitance			463	650	
Coss	Output Capacitance V _{DS} =-15V , V _{GS} =0V , f=1MHz			82	115	pF
Crss	Reverse Transfer Capacitance			68	95	
Is	Continuous Source Current ^{1,4}	Vo=Vo=OV Force Current			-3.2	Α
Isм	Pulsed Source Current ^{2,4}	Source Current ^{2,4} V _G =V _D =0V , Force Current			-13	Α
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =-1A , T _J =25°C			-1	V

Note:

^{1.} The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.

^{2.}The data tested by pulsed , pulse width $\,\leq\,300\text{us}$, duty cycle $\,\leq\,2\%$

^{3.} The power dissipation is limited by 150°C junction temperature

^{4.} The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.



Typical Characteristics

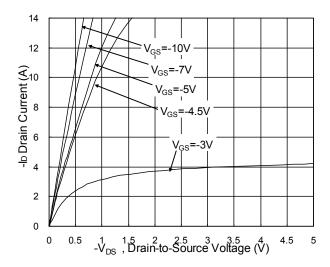


Fig.1 Typical Output Characteristics

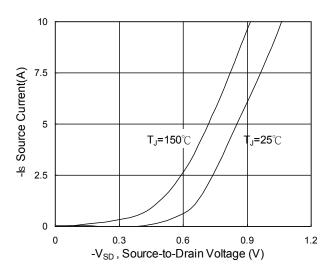


Fig.3 Source Drain Forward Characteristics

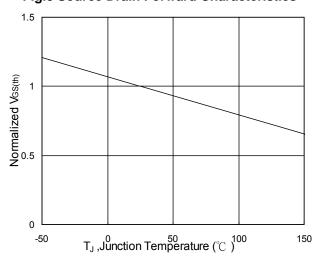


Fig.5 Normalized V_{GS(th)} vs. T_J

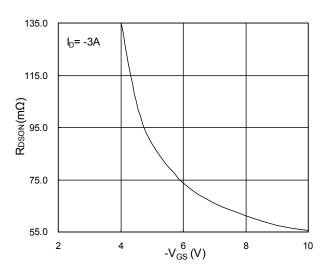


Fig.2 On-Resistance vs. G-S Voltage

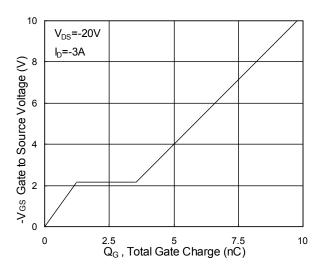


Fig.4 Gate-Charge Characteristics

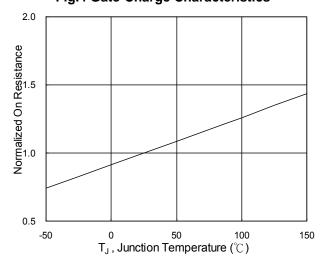
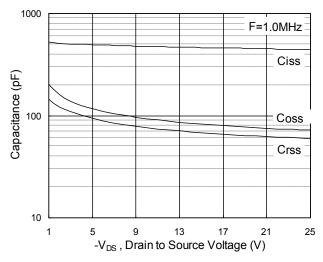


Fig.6 Normalized RDSON vs. TJ





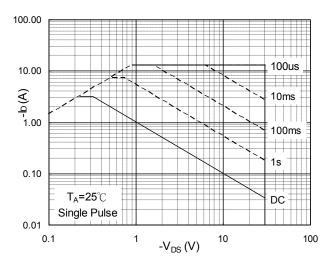


Fig.7 Capacitance

Fig.8 Safe Operating Area

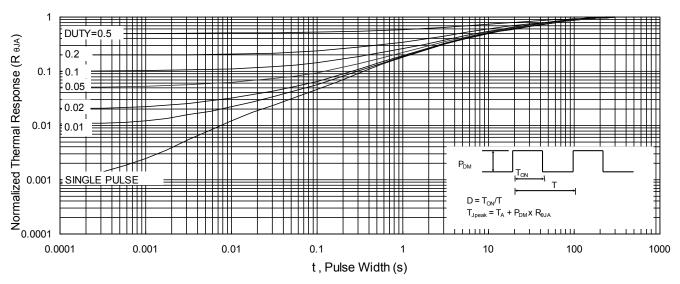
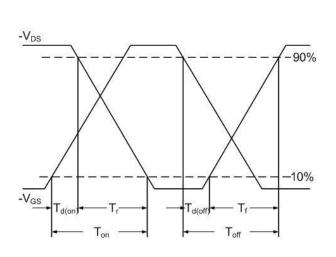


Fig.9 Normalized Maximum Transient Thermal Impedance



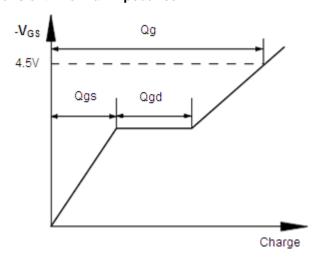
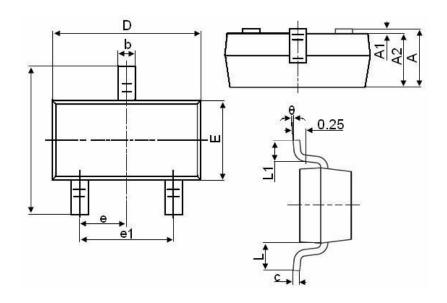


Fig.10 Switching Time Waveform

Fig.11 Gate Charge Waveform



SOT-23-3L(SOT-23F) Package Information



Symbol	Dimensions in Millimeters		
	MIN.	MAX.	
А	1.050	1.250	
A1	0.000	0.100	
A2	1.050	1.150	
b	0.300	0.500	
С	0.100	0.200	
D	2.800	3.000	
E	1.500	1.700	
E1	2.650	2.950	
е		0.950TYP	
e1	1.800	2.000	
L		0.550REF	
L1	0.300	0.600	
θ	0°	8°	



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