



## Description

The HXY3401BMI uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.

## General Features

$V_{DS} = -30V$   $I_D = -4.2A$

$R_{DS(ON)} < 57m\Omega @ V_{GS}=10V$

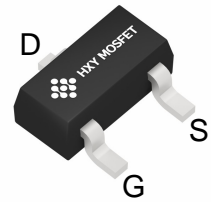
$R_{DS(ON)} < 80m\Omega @ V_{GS}=4.5V$

## Application

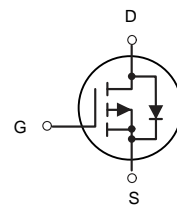
Battery protection

Load switch

Uninterruptible power supply



SOT-23-3L



P-Channel MOSFET

## Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
HXY3401BMI	SOT-23-3L	X1KX	3000

## Absolute Maximum Ratings ( $T_A=25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Limit	Unit
$V_{DS}$	Drain-Source Voltage	-30	V
$V_{GS}$	Gate-Source Voltage	$\pm 12$	V
$I_D$	Drain Current-Continuous	-4.2	A
$I_{DM}$	Drain Current-Pulsed	-30	A
$P_D$	Maximum Power Dissipation	1.2	W
$T_J, T_{STG}$	Operating Junction and Storage Temperature Range	-55 To 150	$^{\circ}C$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	104	$^{\circ}C/W$



**Electrical Characteristics ( $T_A=25^{\circ}\text{C}$  unless otherwise noted)**

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA	-30	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-24V, V <sub>GS</sub> = 0V	-	-	-1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±10V, V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-0.7	-1	-1.3	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-4.2A	-	47	57	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =4A	-	60	80	mΩ
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-1A		85	130	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =-5V, I <sub>D</sub> =-4.2A	-	10	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> =-15V, V <sub>GS</sub> =0V, F=1.0MHz	-	880	-	PF
Output Capacitance	C <sub>OSS</sub>		-	105	-	PF
Reverse Transfer Capacitance	C <sub>RSS</sub>		-	65	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =-15V, I <sub>D</sub> =-4.2A V <sub>GS</sub> =-10V, R <sub>GEN</sub> =6Ω	-	7	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	3	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	30	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	12	-	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-15V, I <sub>D</sub> =-4.2A, V <sub>GS</sub> =-4.5V	-	8.5	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	1.8	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	2.7	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> = -4.2A	-	-	-1.2	V

**Notes:**

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production



## Typical Electrical and Thermal Characteristics

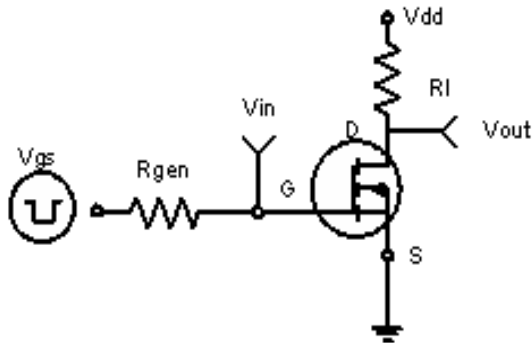


Figure 1: Switching Test Circuit

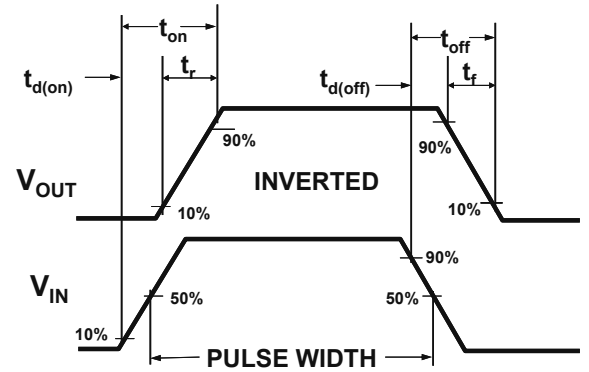


Figure 2: Switching Waveforms

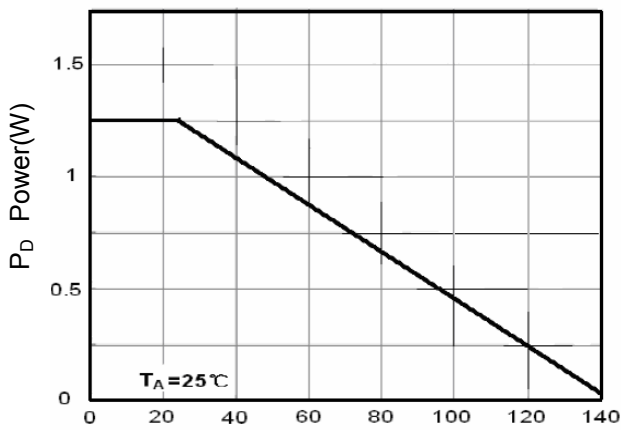


Figure 3 Power Dissipation

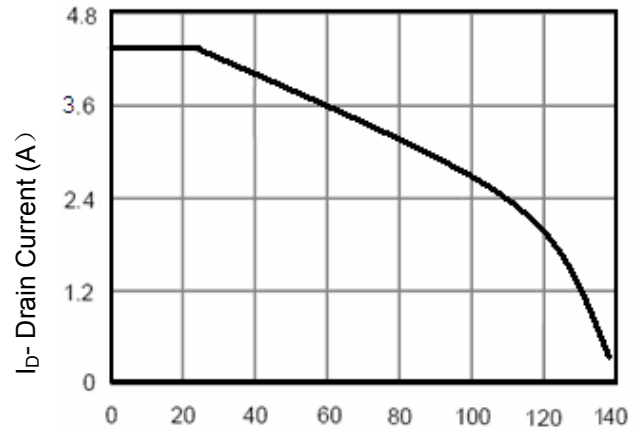


Figure 4 Drain Current

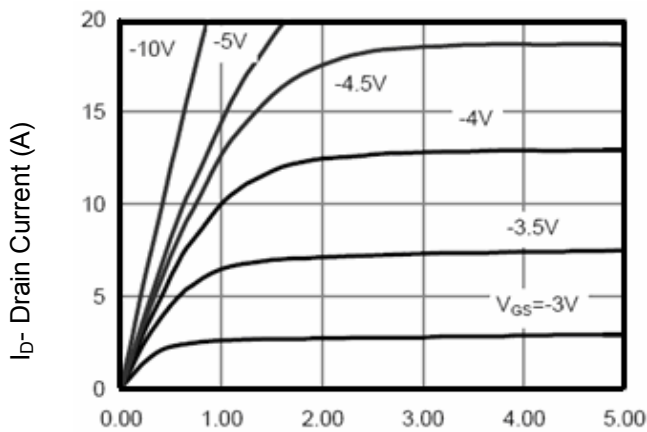


Figure 5 Output Characteristics

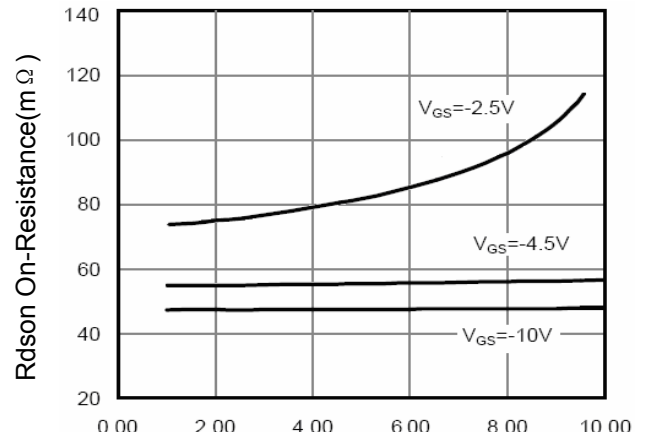
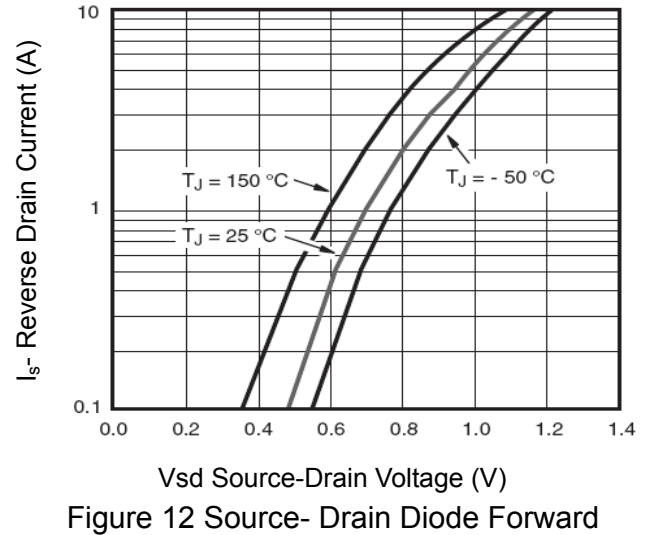
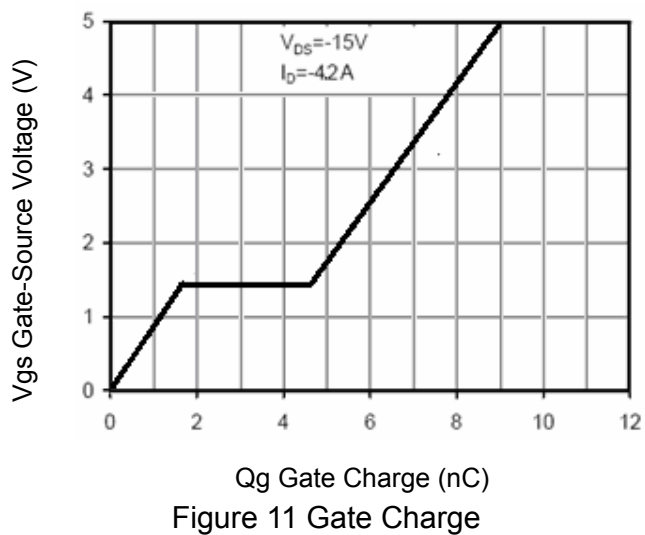
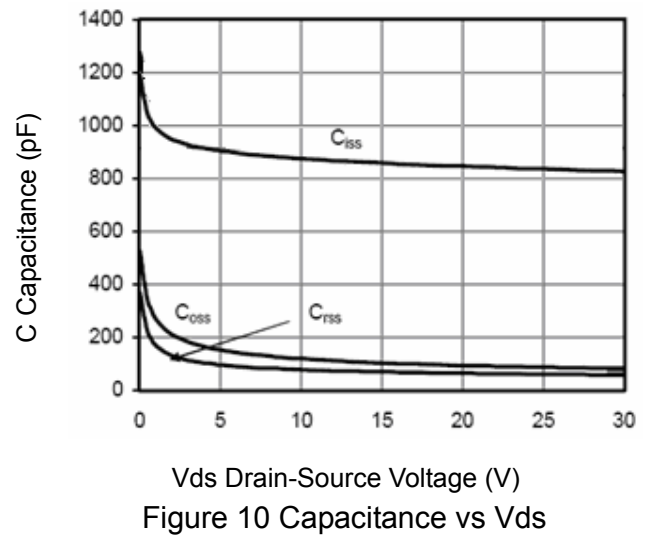
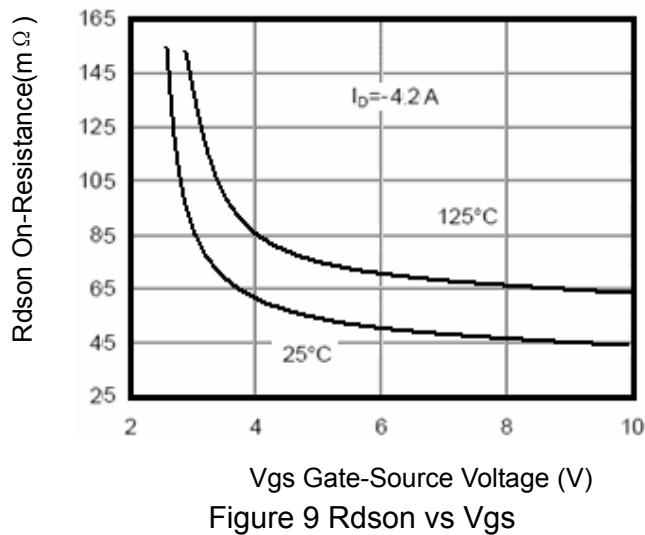
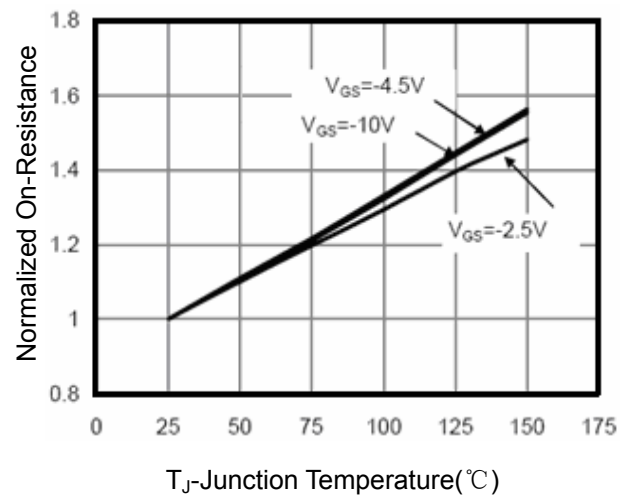
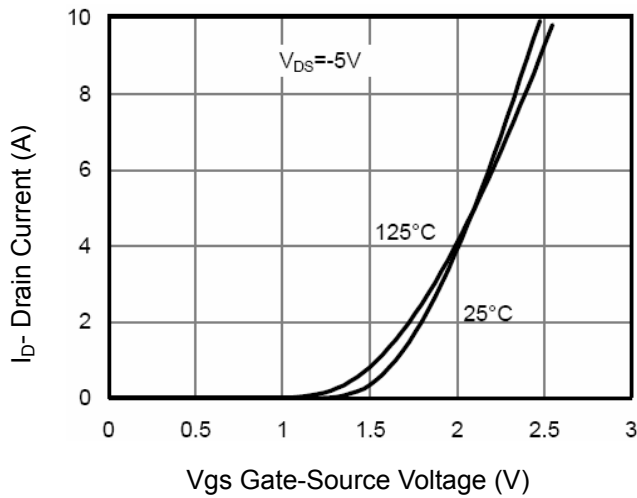


Figure 6 Drain-Source On-Resistance



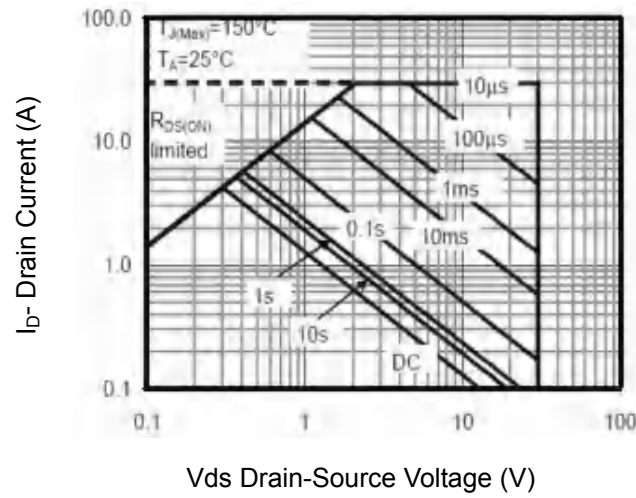


Figure 13 Safe Operation Area

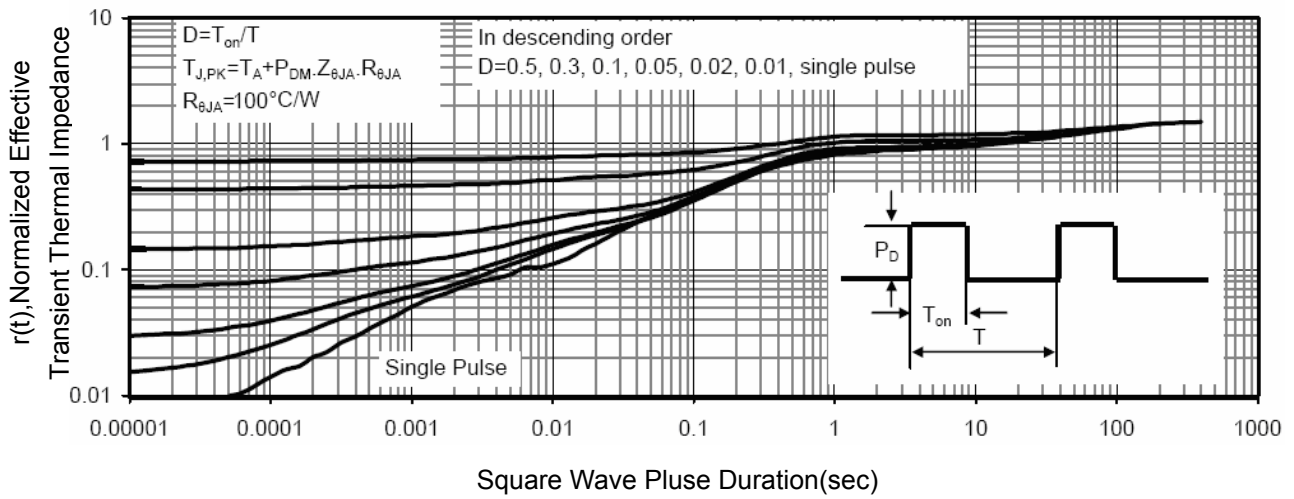
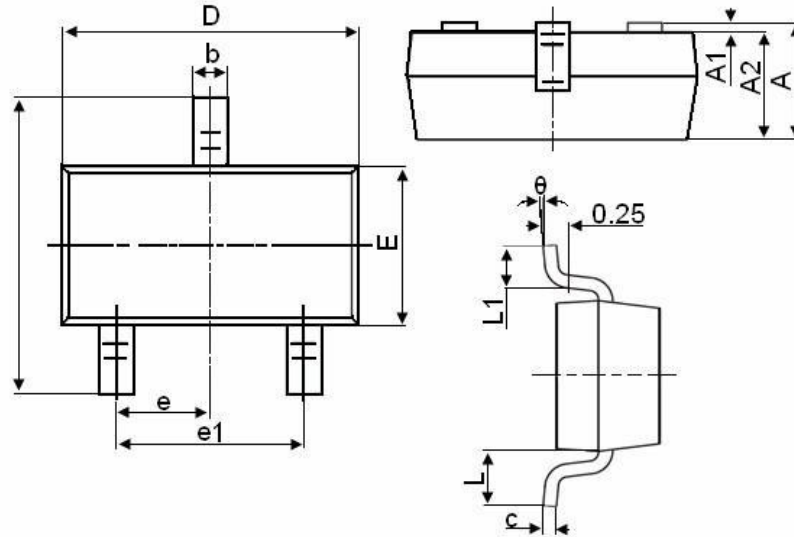


Figure 14 Normalized Maximum Transient Thermal Impedance



## SOT-23-3L Package Information



Symbol	Dimensions in Millimeters	
	MIN.	MAX.
A	1.050	1.250
A1	0.000	0.100
A2	1.050	1.150
b	0.300	0.500
c	0.100	0.200
D	2.800	3.000
E	1.500	1.700
E1	2.650	2.950
e	0.950TYP	
e1	1.800	2.000
L	0.550REF	
L1	0.300	0.600
θ	0°	8°



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