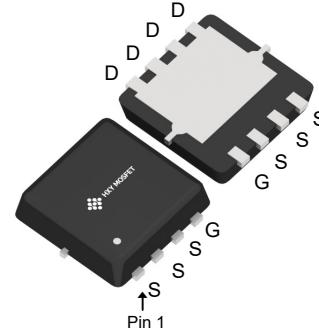




## Description

The TSM038N03PQ33 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.



DFN3X3-8L

## General Features

$V_{DS} = 30V$   $I_D = 85A$

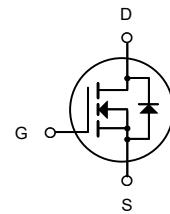
$R_{DS(ON)} < 3.1 \text{ m}\Omega$  @  $V_{GS}=10V$

## Application

Battery protection

Load switch

Uninterruptible power supply



N-Channel MOSFET

## Ordering Information

| Product ID    | Pack      | Brand      | Qty(PCS) |
|---------------|-----------|------------|----------|
| TSM038N03PQ33 | DFN3X3-8L | HXY MOSFET | 5000     |

## Absolute Maximum Ratings ( $T_c=25^\circ\text{C}$ unless otherwise noted)

| Symbol                     | Parameter                                | Rating     | Units |
|----------------------------|--|------------|-------|
| $V_{DS}$                   | Drain-Source Voltage                     | 30         | V     |
| $V_{GS}$                   | Gate-Source Voltage                      | $\pm 20$   | V     |
| $I_D@T_c=25^\circ\text{C}$ | Continuous Drain Current, $V_{GS} @ 10V$ | 85         | A     |
| $I_D@T_c=75^\circ\text{C}$ | Continuous Drain Current, $V_{GS} @ 10V$ | 78         | A     |
| $I_{DM}$                   | Pulsed Drain Current                     | 480        | A     |
| $E_{AS}$                   | Single Pulse Avalanche Energy            | 333        | mJ    |
| $I_{AS}$                   | Avalanche Current                        | 25         | A     |
| $P_D@T_c=25^\circ\text{C}$ | Total Power Dissipation                  | 88         | W     |
| $T_{STG}$                  | Storage Temperature Range                | -55 to 150 | °C    |
| $T_J$                      | Operating Junction Temperature Range     | -55 to 150 | °C    |
| $R_{\theta JC}$            | Thermal Resistance Junction-Case         | 1.4        | °C/W  |



**Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)**

| Symbol                              | Parameter                                      | Conditions  | Min. | Typ. | Max. | Unit |
|-------------------------------------|--|---|------|------|------|------|
| BV <sub>DSS</sub>                   | Drain-Source Breakdown Voltage                 | V <sub>GS</sub> =0V, I <sub>D</sub> =250uA  | ---  | ---  | ---  | V    |
| △BV <sub>DSS</sub> /△T <sub>J</sub> | BV <sub>DSS</sub> Temperature Coefficient      | Reference to 25°C, I <sub>D</sub> =1mA  | ---  | ---  | ---  | V/°C |
| R <sub>DS(ON)</sub>                 | Static Drain-Source On-Resistance <sup>2</sup> | V <sub>GS</sub> =10V, I <sub>D</sub> =20A   | ---  | 2.9  | ---  | mΩ   |
|                                     |  | V <sub>GS</sub> =4.5V, I <sub>D</sub> =1A   | ---  | ---  | ---  |      |
| V <sub>GS(th)</sub>                 | Gate Threshold Voltage                         | V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA                            | 1    | 1.1  | 2.0  | V    |
| I <sub>DSS</sub>                    | Drain-Source Leakage Current                   | V <sub>DS</sub> =30V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C                     | ---  | ---  | 1    | uA   |
| I <sub>GSS</sub>                    | Gate-Source Leakage Current                    | V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V  | ---  | ---  | ±100 | nA   |
| R <sub>g</sub>                      | Gate Resistance                                | V <sub>DS</sub> =0V, V <sub>GS</sub> =0V, f=1MHz                                    | ---  | ---  | ---  | Ω    |
| Q <sub>g</sub>                      | Total Gate Charge                              | V <sub>DS</sub> =15V, V <sub>GS</sub> =10V, I <sub>D</sub> =30A                     | ---  | 72   | ---  | nC   |
| Q <sub>gs</sub>                     | Gate-Source Charge                             |   | ---  | 46   | ---  |      |
| Q <sub>gd</sub>                     | Gate-Drain Charge                              |   | ---  | 13   | ---  |      |
| T <sub>d(on)</sub>                  | Turn-On Delay Time                             | V <sub>GS</sub> =10V, V <sub>DS</sub> =15V, I <sub>D</sub> =30A, R <sub>L</sub> =3Ω | ---  | 14   | ---  | ns   |
| T <sub>r</sub>                      | Rise Time                                      |   | ---  | 18   | ---  |      |
| T <sub>d(off)</sub>                 | Turn-Off Delay Time                            |   | ---  | 40   | ---  |      |
| T <sub>f</sub>                      | Fall Time                                      |   | ---  | 12   | ---  |      |
| C <sub>iss</sub>                    | Input Capacitance                              | V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, f=1MHz                                   | ---  | 4000 | ---  | pF   |
| C <sub>oss</sub>                    | Output Capacitance                             |   | ---  | 437  | ---  |      |
| C <sub>rss</sub>                    | Reverse Transfer Capacitance                   |   | ---  | 396  | ---  |      |

**Diode Characteristics**

| Symbol          | Parameter                                | Conditions   | Min. | Typ. | Max. | Unit |
|-----------------|--|--|------|------|------|------|
| I <sub>s</sub>  | Continuous Source Current <sup>1,4</sup> | V <sub>G</sub> =V <sub>D</sub> =0V, Force Current              | ---  | ---  | 85   | A    |
| V <sub>SD</sub> | Diode Forward Voltage <sup>2</sup>       | V <sub>GS</sub> =0V, I <sub>s</sub> =30A, T <sub>J</sub> =25°C | ---  | ---  | 1.2  | V    |
| t <sub>rr</sub> | Reverse Recovery Time                    | I <sub>F</sub> =30A, di/dt=100A/μs, T <sub>J</sub> =25°C       | ---  | 48   | ---  | nS   |
| Q <sub>rr</sub> | Reverse Recovery Charge                  |  | ---  | 80   | ---  | nC   |

**Notes:**

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
2. EAS condition: T<sub>J</sub>=25°C, V<sub>DD</sub>=15V, V<sub>G</sub>=10V, R<sub>G</sub>=25Ω, L=0.5mH, I<sub>AS</sub>=25A
3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%



## Typical Characteristics

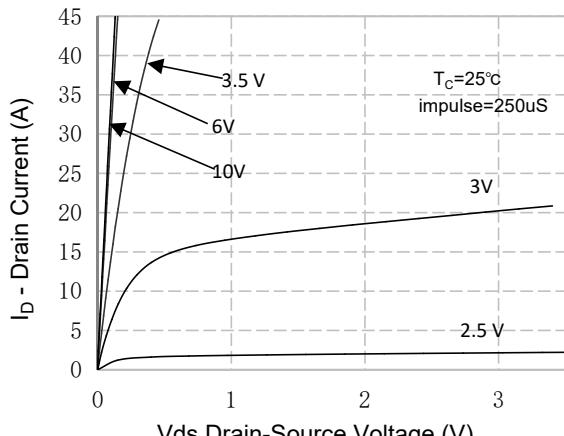


Figure 1. On-Region Characteristics

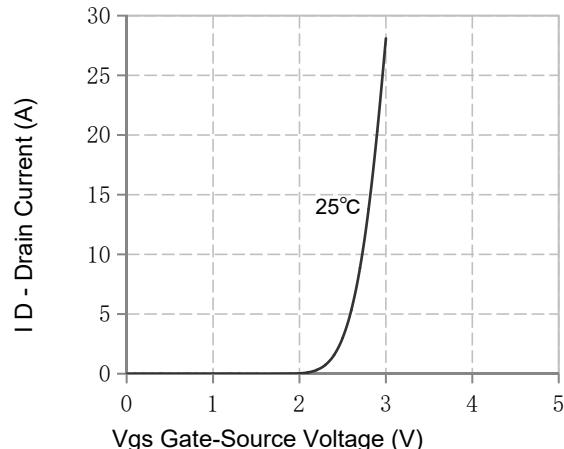


Figure 2. Transfer Characteristics

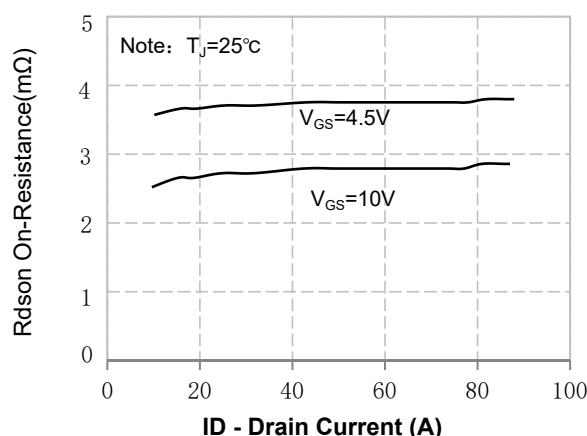


Figure 3. On-Resistance Variation vs  
Drain Current and Gate Voltage

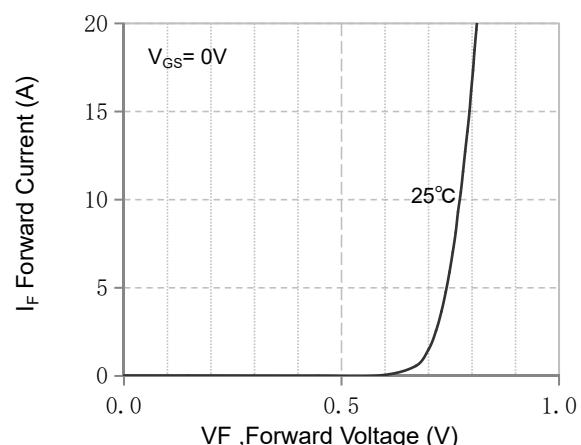


Figure 4. Body Diode Forward Voltage  
Variation with Source Current  
and Temperature

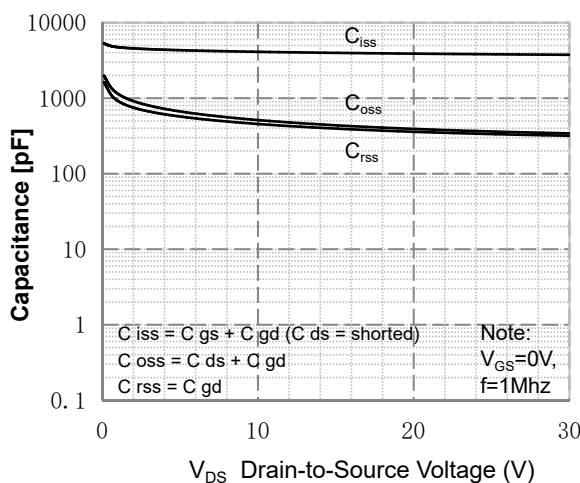


Figure 5. Capacitance Characteristics

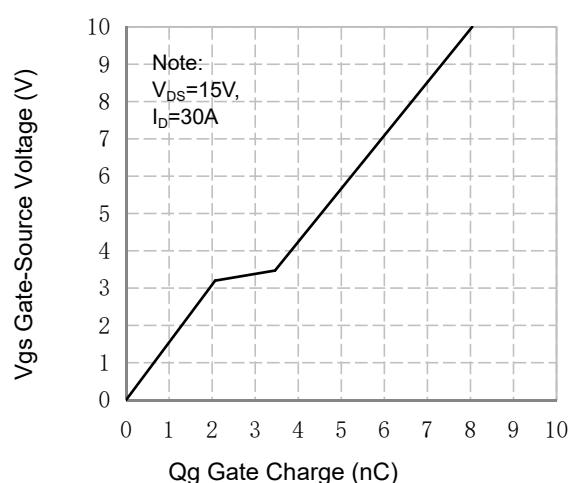
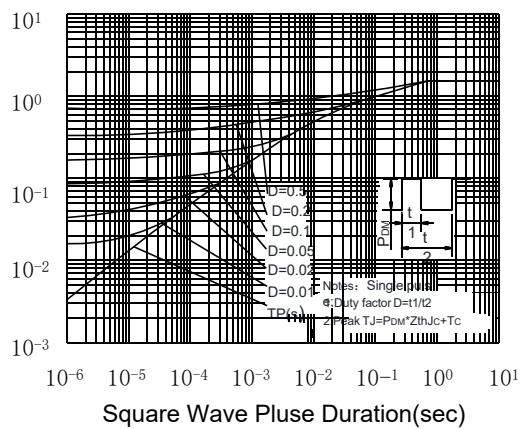
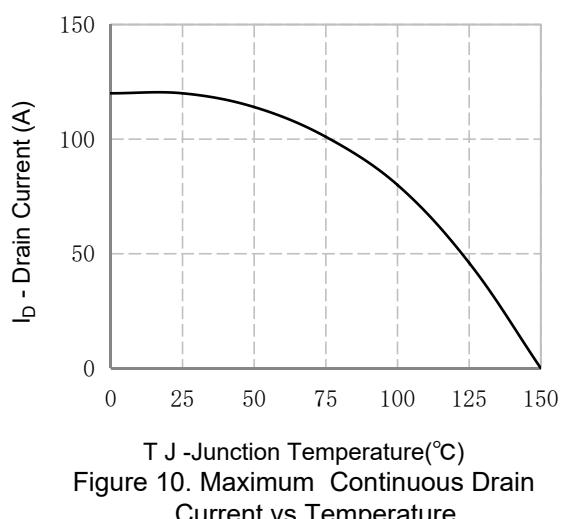
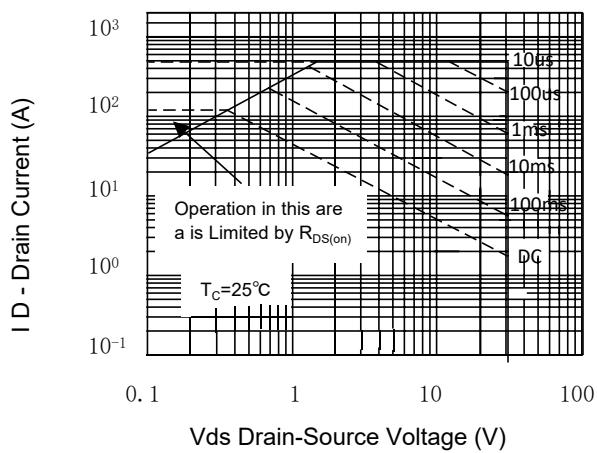
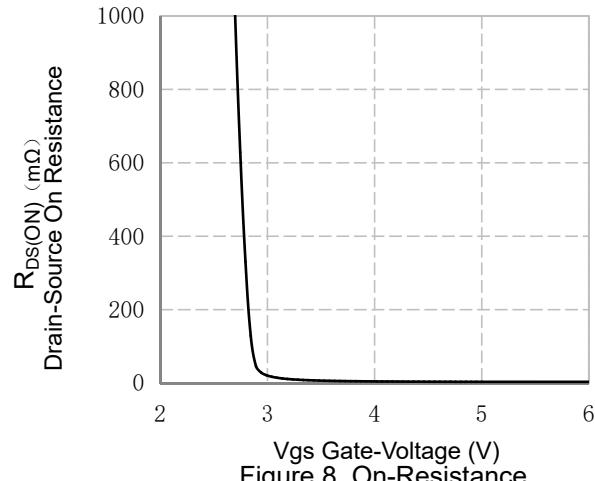
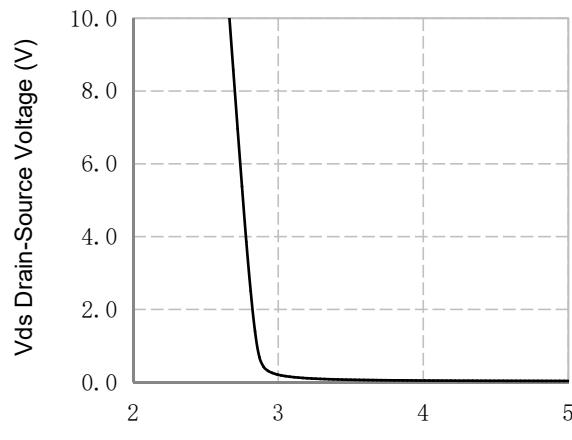


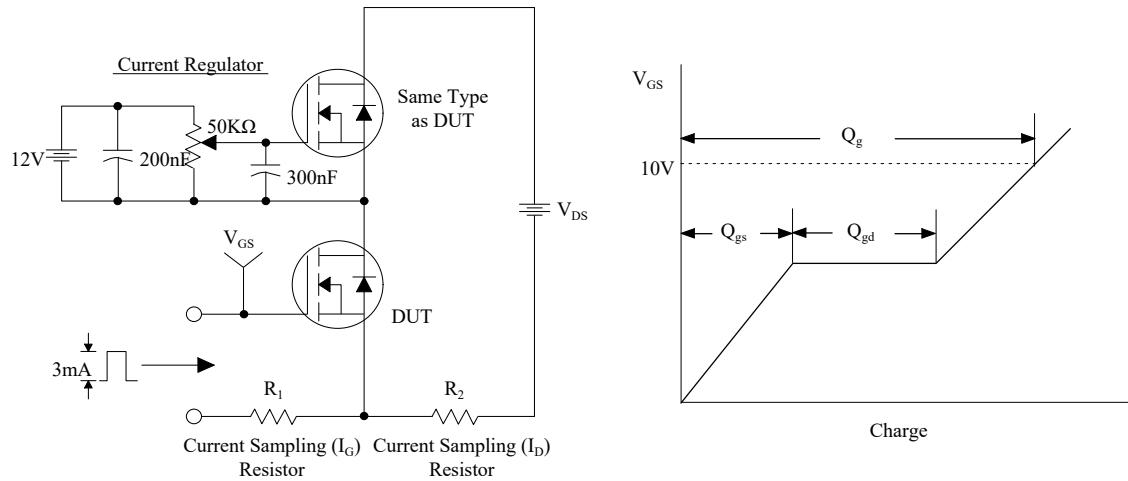
Figure 6. Gate Charge Characteristics



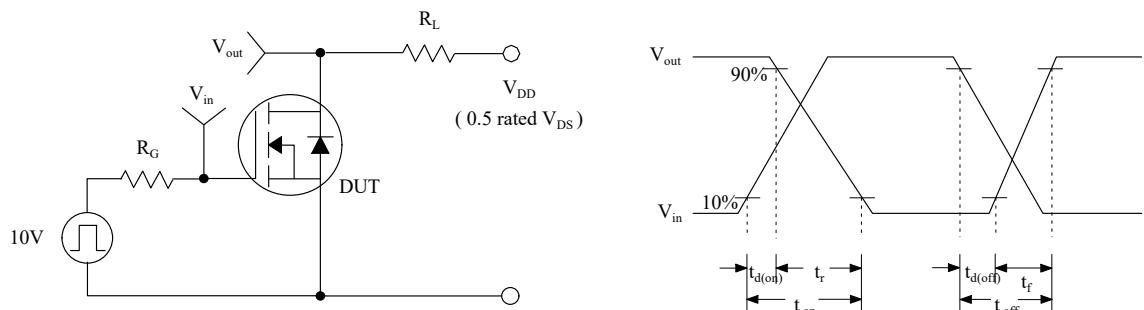


## Test Circuit

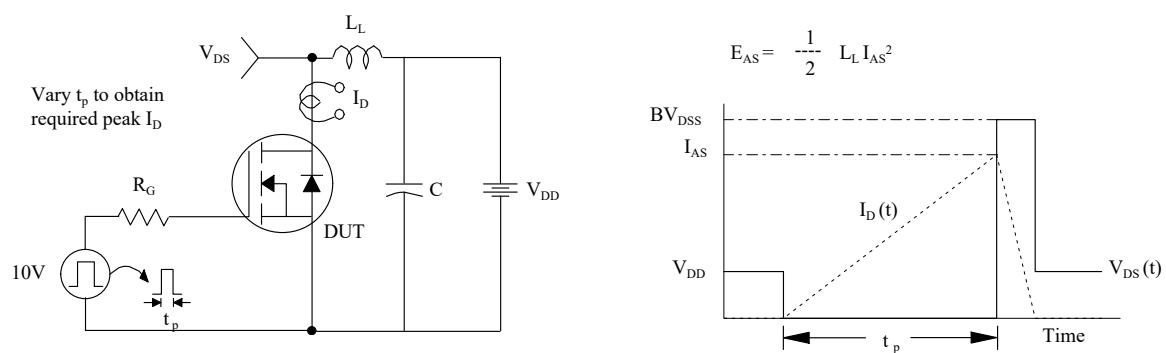
### Gate Charge Test Circuit & Waveform



### Resistive Switching Test Circuit & Waveforms

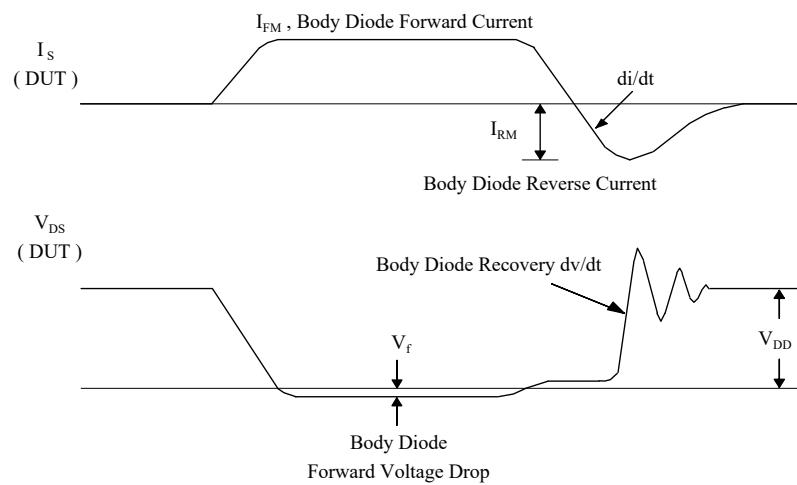
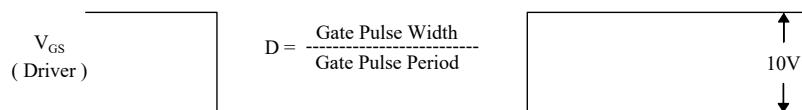
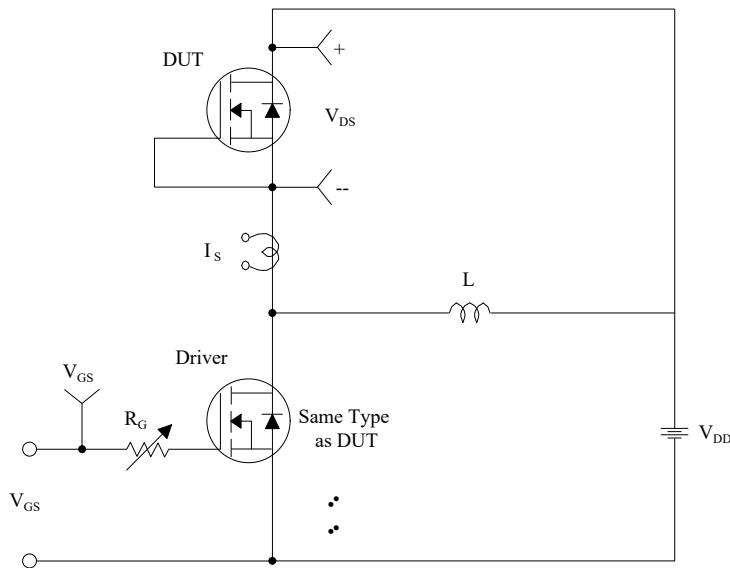


### Unclamped Inductive Switching Test Circuit & Waveforms



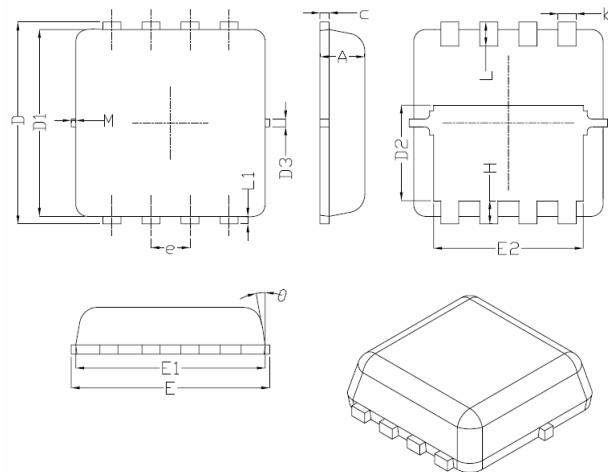


### Peak Diode Recovery dv/dt Test Circuit & Waveforms





## DFN3X3-8L Package Information



| Symbol   | Dimensions In Millimeters |      |      |
|----------|---------------------------|------|------|
|          | Min.                      | Nom. | Max. |
| A        | 0.70                      | 0.75 | 0.80 |
| b        | 0.25                      | 0.30 | 0.35 |
| c        | 0.10                      | 0.15 | 0.25 |
| D        | 3.25                      | 3.35 | 3.45 |
| D1       | 3.00                      | 3.10 | 3.20 |
| D2       | 1.48                      | 1.58 | 1.68 |
| D3       | -                         | 0.13 | -    |
| E        | 3.20                      | 3.30 | 3.40 |
| E1       | 3.00                      | 3.15 | 3.20 |
| E2       | 2.39                      | 2.49 | 2.59 |
| e        | 0.65BSC                   |      |      |
| H        | 0.30                      | 0.39 | 0.50 |
| L        | 0.30                      | 0.40 | 0.50 |
| L1       | -                         | 0.13 | -    |
| M        | *                         | *    | 0.15 |
| $\theta$ |                           | 10°  | 12°  |



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