

Description

The TPCC8093,L1Q(M uses advanced trench technology to provide excellent R_{DS(ON)}, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

 $V_{DS} = 20V I_{D} = 60A$

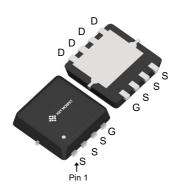
 $R_{DS(ON)} < 5m\Omega$ @ $V_{GS}=4.5V$

Application

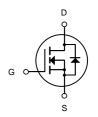
Battery protection

Load switch

Uninterruptible power supply



DFN3X3-8L (TSON-8(3.1x3.1))



N-Channel MOSFET

Package Marking and Ordering Information

Product ID	Pack	Brand	Qty(PCS)
TPCC8093,L1Q(M	DFN3X3-8L(TSON-8(3.1x3.3))	HXY MOSFET	5000

Absolute Maximum Ratings (TC=25°C unless otherwise specified)

Symbol	Parameter	Rating	Units
Vos	Drain-Source Voltage	20	V
Vgs	Gate-Source Voltage	±12	V
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	60	А
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ¹	33	А
Ідм	Pulsed Drain Current ²	220	А
EAS	Single Pulse Avalanche Energy ³	46	mJ
las	Avalanche Current	25	А
P _D @T _C =25°C	Total Power Dissipation ⁴	15	W
Тѕтс	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
Reja	Thermal Resistance Junction-ambient ¹	62	°C/W
Rejc	Thermal Resistance Junction-Case ¹	4.5	°C/W



Electrical Characteristics (T_J=25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	20	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =20V, V _{GS} =0V,	-	-	1.0	μA
I _{GSS}	Gate to Body Leakage Current	V _{DS} =0V, V _{GS} =±12V	_	-	±100	nA
V _{GS(th)}	Gate Threshold Voltage	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	0.4	0.7	1.1	V
В	Static Drain-Source on-Resistance	V _{GS} =4.5V, I _D =30A	-	4.0	5	mΩ
R _{DS(on)}		V _{GS} =2.5V, I _D =20A	-	6.0	9	11122
C _{iss}	Input Capacitance	V_{DS} =10V, V_{GS} =0V, $f = 1.0MHz$	-	2500	-	pF
Coss	Output Capacitance		-	407	-	pF
C _{rss}	Reverse Transfer Capacitance		-	386	-	pF
Q_g	Total Gate Charge	V _{DS} =10V, I _D =30A, V _{GS} =4.5V	-	32	-	nC
Q _{gs}	Gate-Source Charge		-	3	-	nC
Q _{gd}	Gate-Drain("Miller") Charge		-	11	-	nC
t _{d(on)}	Turn-on Delay Time	V_{DS} =10V, I_{D} =30A, R_{GEN} =3 Ω , V_{GS} =4.5V	-	17	-	ns
t _r	Turn-on Rise Time		-	49	-	ns
t _{d(off)}	Turn-off Delay Time		-	74	-	ns
t _f	Turn-off Fall Time		-	26	-	ns
	Maximum Continuous Drain to Source Diode Forward				7.5	^
Is	Current		-	-	75	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		_	-	300	Α
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _S =30A	-	-	1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

- 2. EAS condition: TJ=25 $^{\circ}\text{C}$, VDD=10V, VG=4.5V, L=0.5mH, RG=25 $^{\Omega}$, IAS=15A
- 3. Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%



Typical Performance Characteristics

Figure1: Output Characteristics

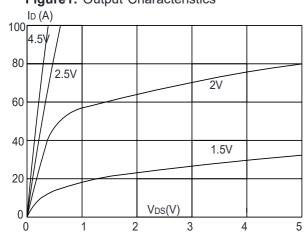


Figure 2: Typical Transfer Characteristics

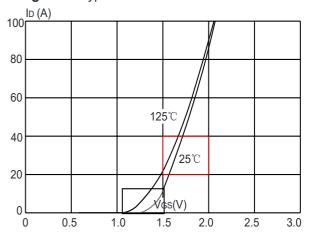


Figure 3:On-resistance vs. Drain Current $\text{RDS(ON)} \left(m\Omega \right)$

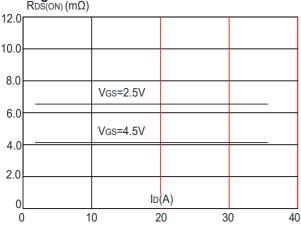


Figure 4: Body Diode Characteristics

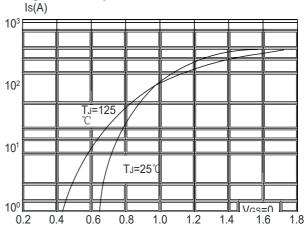


Figure 5: Gate Charge Characteristics

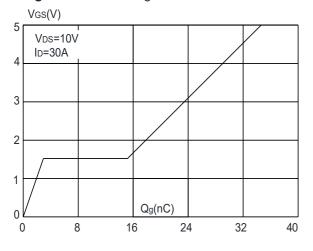


Figure 6: Capacitance Characteristics

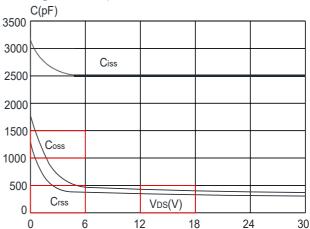




Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

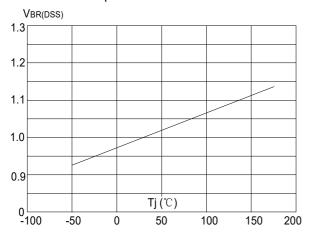


Figure 9: Maximum Safe Operating Area

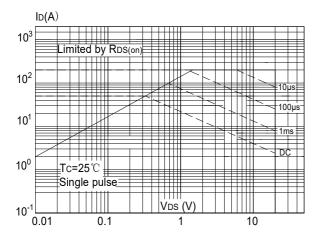


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case

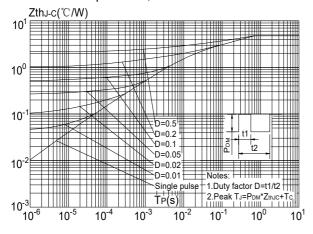


Figure 8: Normalized on Resistance vs. Junction Temperature

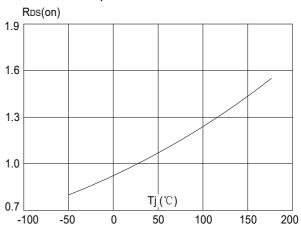
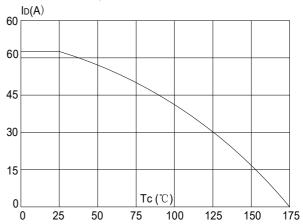
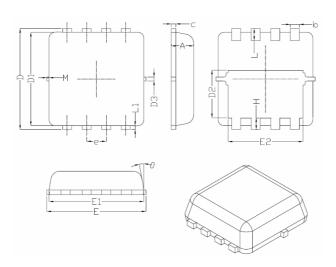


Figure 10: Maximum Continuous Drain Current vs. Case Temperature





DFN3X3-8L(TSON-8(3.1x3.1)) Package Information



Sumb at	Dimensions In Millimeters		
Symbol	Min.	Nom.	Max.
A	0.70	0.75	0.80
b	0.25	0.30	0.35
С	0.10	0.15	0.25
D	3.25	3.35	3.45
D1	3.00	3.10	3.20
D2	1.48	1.58	1.68
D3	-	0.13	-
E	3.20	3.30	3.40
E1	3.00	3.15	3.20
E2	2.39	2.49	2.59
е	0.65BSC		
Н	0.30	0.39	0.50
L	0.30	0.40	0.50
L1	-	0.13	-
M	*	*	0.15
θ		10°	12 [°]



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