

Description

The HXY50N04DF uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

 $V_{DS} = 40V I_{D} = 50A$

 $R_{DS(ON)}$ < 10m Ω @ V_{GS} =10V

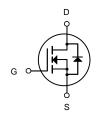
Application

Battery protection

Load switch

Uninterruptible power supply





N-Channel MOSFET

Package Marking and Ordering Information

Product ID	Pack	Brand	Qty(PCS)
HXY50N04DF	DFN3X3-8L	HXY MOSFET	5000

Absolute Maximum Ratings (Tc=25°C unless otherwise specified)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	40	V
Vgs	Gate-Source Voltage	±20	V
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	50	А
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ¹	22.1	Α
Ідм	Pulsed Drain Current ²	140	Α
P _D @T _C =25°C	Total Power Dissipation ⁴	44	W
Тѕтс	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
Reja	Thermal Resistance Junction-ambient ¹	62	°C/W
Rejc	Thermal Resistance Junction-Case ¹	2.8	°C/W



Electrical Characteristics (T_J=25 ° C , unless otherwise noted)

Symbol	Parameter	Conditions	Min	Тур	Max	Units
BV _{DSS}	Drain-Sourtce Breakdown Voltage	V _{GS} =0V,I _D =250μA	40			V
I _{DSS}	Drain-Source Leakage Current	V _{GS} =0V, V _{DS} =40V,T _J =25°C			1	μΑ
		V _{GS} =0V, V _{DS} =32V,T _J =85°C			10	μΑ
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V			±100	nA
V _{GS(th)}	GATE-Source Threshold Voltage	V _{GS} =V _{DS} , I _D =250μA	1.2	1.8	2.5	V
_	Drain-Source On Resistance	V _{GS} =10V,I _D =8A		7.4	10	mΩ
R _{DS(ON)}		V _{GS} =4.5V,I _D =4A		10.6	13.5	
gfs	Forward Transconductance	V _{DS} =10V, I _D =2A		13		S
C _{iss}	Input Capacitance			1205	2180	
C _{oss}	Output Capacitance	V _{DS} =25V, V _{GS} =0V, f=1MHz		128	245	pF
C _{rss}	Reverse Transfer Capacitance			52	106	
Rg	Gate resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz		2.2		Ω
t _{d(on)}	Turn-On Delay Time ^{2,3}			13.2	25	ns
t _r	Rise Time ^{2,3}	V _{DD} =15V ,		2.2	5	ns
t _{d(off)}	Turn-Off Delay Time ^{2,3}	V _{GS} =10V		72	130	ns
t _f	Fall Time ^{2,3}	R _G =3.3Ω,I _D =1A		4.5	10	ns
Qg	Total Gate Charge ^{2,3}			19.7	30	nC
Q_{gs}	Gate-Source Charge ^{2,3}	V _{DS} =20V , V _{GS} =10V , I _D =8A		2.8	4.2	nC
Q _{gd}	Gate-Drain "Miller" Charge ^{2,3}			5.1	7.6	nC
Is	Contiuos Source Curent				35	Α
I _{SM}	Pulsed Source Curent	V _G =V _D =0V , Force Current			70	Α
V _{SD}	Source-Drain Diode Forward Voltage	V _{GS} =0V , I _S =1A , T _J =25°C			1	V
t _{rr}	Reverse Recovery Time ³	I _S =1A ,di/dt=100A/		17		nS
Qrr	Reverse Recovery Charge ³	μs, T _J =25°C,V _{GS} =0V		2.8		nC

Notes:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- 3. Essentially independent of operating temperature.



Typical Characteristics

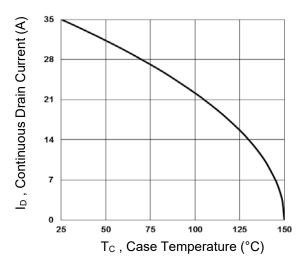


Fig.1 Continuous Drain Current vs. T_C

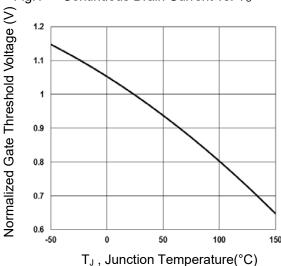


Fig.3 Normalized V_{th} vs. T_J

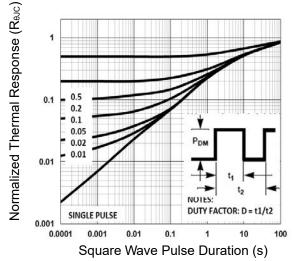


Fig.5 Normalized Transient Impedance

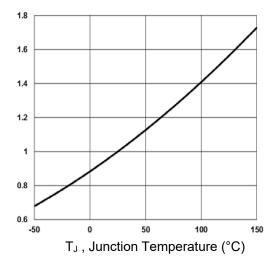


Fig.2 Normalized RDSON vs. T_J

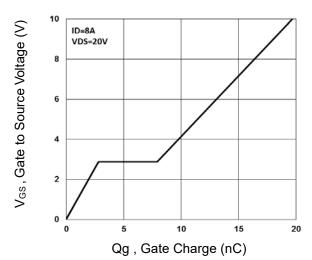


Fig.4 Gate Charge Waveform

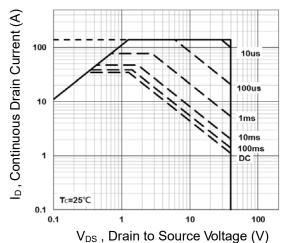
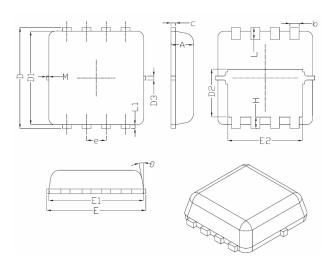


Fig.6 Maximum Safe Operation Area



DFN3X3-8L Package Information



Complete I	Dimensions In Millimeters			
Symbol	Min.	Nom.	Max.	
A	0.70	0.75	0.80	
b	0.25	0.30	0.35	
С	0.10	0.15	0.25	
D	3.25	3.35	3.45	
D1	3.00	3.10	3.20	
D2	1.48	1.58	1.68	
D3	-	0.13	-	
E	3.20	3.30	3.40	
E1	3.00	3.15	3.20	
E2	2.39	2.49	2.59	
е	0.6	5BSC		
Н	0.30	0.39	0.50	
L	0.30	0.40	0.50	
L1	-	0.13	-	
M	*	*	0.15	
θ		10°	12 [°]	

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