

## **Description**

The HXY3419ADF uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

### **General Features**

 $V_{DS} = -30V$   $I_{D} = -55A$ 

 $R_{DS(ON)}$  < 11m $\Omega$  @  $V_{GS}$ =-10V

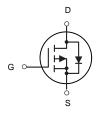
## **Application**

**Battery protection** 

Load switch

Uninterruptible power supply

DFN3X3-8L



P-Channel MOSFET

## **Package Marking and Ordering Information**

Product ID	Pack	Marking	Qty(PCS)
HXY3419ADF	DFN3X3-8L	3419 XXX YYYY	5000

#### Absolute Maximum Ratings (Tc=25°Cunless otherwise noted)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	-30	V
VGS	Gate-Source Voltage	±20	V
I <b></b> @T <sub>C</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	-55	А
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	-23	Α
IDM	Pulsed Drain Current <sup>2</sup>	-140	А
EAS	Single Pulse Avalanche Energy <sup>3</sup>	78.8	mJ
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>4</sup>	21.5	W
TSTG	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
R₀JC	Thermal Resistance Junction-Case <sup>1</sup>	5.8	°C/W



## **Electrical Characteristics** (T<sub>J</sub>=25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units	
Off Charac	Off Characteristic						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V,I <sub>D</sub> = -250μA	-30	-	-	V	
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS} = -30V, V_{GS} = 0V,$	-	-	-1	μA	
I <sub>GSS</sub>	Gate to Body Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> = ±20V	-	_	±100	nA	
On Charac	On Characteristics						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250μA	-1.0	-1.5	-2.5	V	
D.	Static Drain-Source on-Resistance	V <sub>GS</sub> =-10V, I <sub>D</sub> =-12A	-	8.5	11	mΩ	
$R_{DS(on)}$	note3	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-8A	-	13	18		
Dynamic (	Dynamic Characteristics						
C <sub>iss</sub>	Input Capacitance	15)/// 0)/	-	2800	-	pF	
Coss	Output Capacitance	$V_{DS} = -15V, V_{GS} = 0V,$ f = 1.0MHz	-	346	-	pF	
C <sub>rss</sub>	Reverse Transfer Capacitance		-	319	-	pF	
Qg	Total Gate Charge	\/ 45\/ L 004	-	30	-	nC	
Q <sub>gs</sub>	Gate-Source Charge	$V_{DS}$ = -15V, $I_D$ = -20A, $V_{GS}$ = -10V	-	5.3	-	nC	
$Q_{gd}$	Gate-Drain("Miller") Charge	VGS 10 V	-	7.6	-	nC	
Switching	Characteristics						
t <sub>d(on)</sub>	Turn-on Delay Time	$V_{DD}$ = -15V, $I_{D}$ = -20A, $V_{GS}$ =-10V, $R_{GEN}$ =2.5 $\Omega$	-	14	-	ns	
t <sub>r</sub>	Turn-on Rise Time		-	20	-	ns	
$t_{d(off)}$	Turn-off Delay Time		-	95	-	ns	
t <sub>f</sub>	Turn-off Fall Time		-	65	-	ns	
Drain-Sou	rce Diode Characteristics and Maxim	um Ratings					
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward			_	-55	Α	
18	Current			_	-55	<i>'</i>	
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-140	Α	
$V_{\text{SD}}$	Drain to Source Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>S</sub> = -35A	-	-0.8	-1.2	V	

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

- 2. EAS condition: T<sub>J</sub>=  $25^{\circ}$ C, VDD= -20V, VG= -10V, L= 0.5mH, RG=  $25^{\circ}$ Q, IAS= -17A
- 3. Pulse Test: Pulse Width≤300µs, Duty Cycle≤2%



## **Typical Performance Characteristics**

Figure1: Output Characteristics

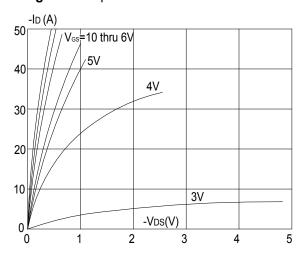


Figure 3:On-resistance vs. Drain Current

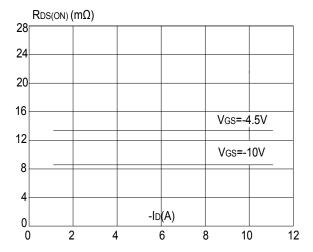


Figure 5: Gate Charge Characteristics

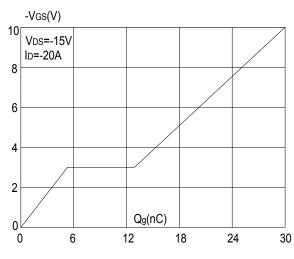


Figure 2: Typical Transfer Characteristics

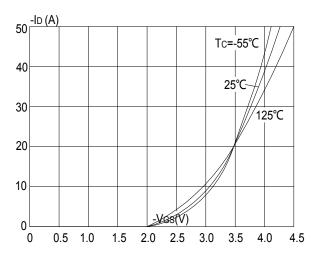


Figure 4: Body Diode Characteristics

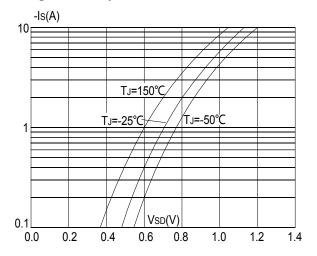
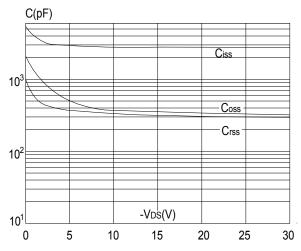


Figure 6: Capacitance Characteristics





**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature

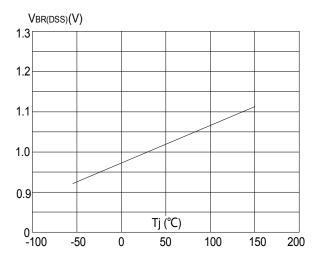
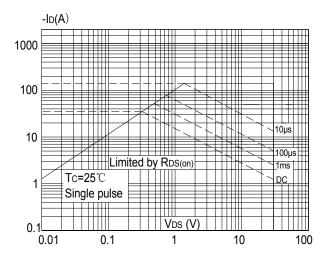
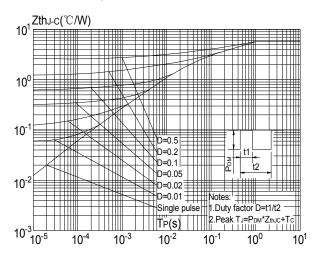


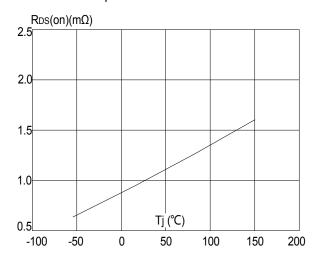
Figure 9: Maximum Safe Operating Area



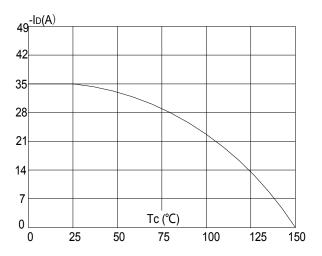
**Figure.11:** Maximum Effective Transient Thermal Impedance, Junction-to-Case



**Figure 8:** Normalized on Resistance vs. Junction Temperature

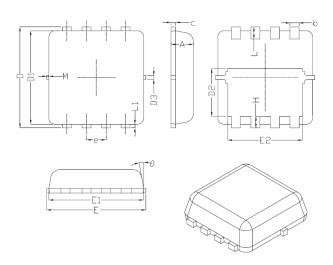


**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature





# **DFN3X3-8L Package Information**



Complete I	Dimensions In Millimeters			
Symbol	Min.	Nom.	Max.	
A	0.70	0.75	0.80	
b	0.25	0.30	0.35	
С	0.10	0.15	0.25	
D	3.25	3.35	3.45	
D1	3.00	3.10	3.20	
D2	1.48	1.58	1.68	
D3	-	0.13	-	
E	3.20	3.30	3.40	
E1	3.00	3.15	3.20	
E2	2.39	2.49	2.59	
е	0.65BSC			
Н	0.30	0.39	0.50	
L	0.30	0.40	0.50	
L1	-	0.13	-	
M	*	*	0.15	
θ		10 <sup>°</sup>	12 <sup>°</sup>	



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