

General Description

The HXYG40H03DF use advanced SGT MOSFET

technology to provide low RDS(ON), low gate charge,

fast switching and excellent avalanche characteristics.

This device is specially designed to get better ruggedness.

General Features

 $V_{DS} = 30V I_{D} = 30A$

 $R_{DS(ON)}$ < 6.3m Ω @ V_{GS} =10V

Applications

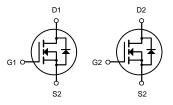
Consumer electronic power supply Motor control

Synchronous-rectification Isolated DC

Synchronous-rectification applications



DFN3X3B-8L



Dual N-Channel MOSFET

Package Marking and Ordering Information

Product ID	Pack	Brand	Qty(PCS)
HXYG40H03DF	DFN3X3B-8L	HXY MOSFET	5000

Absolute Maximum Ratings at Ta = 25°C unless otherwise noted

Parameter	Symbol	Value	Unit
Drain source voltage	VDS	30	V
Gate source voltage	VGS	±20	V
Continuous drain current ¹⁾	I _D @Tc=25 ℃	30	А
ContinuousDrainCurrent,VGS@10V1	I <u>⊳@</u> Tc=100°C	22	Α
PulsedDrainCurrent ²⁾	IDM	135	Α
Power dissipation ³⁾	P _D	30	W
Single pulsed avalanche energy ⁵⁾	EAS	39.8	mJ
AvalancheCurrent	LAS	27	Α
Operation and storage temperature	Tstg, Tj -55 to 150		°C
Thermal resistance, junction-case	RθJC	4.6	°C/W
Thermal resistance, junction-ambient ⁴⁾	RθJA	50	°C/W

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	30			V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V , I _D =20A		5	6.3	mΩ
NDS(ON)		V _{GS} =4.5V , I _D =15A		6.9	9	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250uA$	1.2		2.5	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =24V , V _{GS} =0V , T _J =25°C			1	uA
IDSS		V _{DS} =24V , V _{GS} =0V , T _J =55°C			5	
Igss	Gate-Source Leakage Current	$V_{GS}=\pm20V$, $V_{DS}=0V$			±100	nA
gfs	Forward Transconductance	V _{DS} =5V , I _D =20A		67		S
Rg	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz		1.7		Ω
Qg	Total Gate Charge (4.5V)			8		
Qgs	Gate-Source Charge	V _{DS} =15V , V _{GS} =4.5V , I _D =15A		2.4		nC
Q_{gd}	Gate-Drain Charge			3.2		
T _{d(on)}	Turn-On Delay Time			7.1		
Tr	Rise Time	V_{DD} =15V , V_{GS} =10V , R_{G} =3.3 Ω		40		
T _{d(off)}	Turn-Off Delay Time	I _D =15A		15		ns
Tf	Fall Time			6		
Ciss	Input Capacitance			814		
Coss	Output Capacitance	V _{DS} =15V , V _{GS} =0V , f=1MHz		208		pF
C _{rss}	Reverse Transfer Capacitance			201		

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current ^{1,6}	V _G =V _D =0V , Force Current	-	-	30	Α
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =1A , T _J =25℃			1	V
t _{rr}	Reverse Recovery Time	IF=20A , di/dt=100A/μs ,		15		nS
Qrr	Reverse Recovery Charge	TJ=25℃	-	25		nC

Note:

- 1. The data tested by surface mounted on a 1 inch $^2\,\text{FR-4}$ board with 2OZ copper.
- 2.The data tested by pulsed , pulse width \leqq 300us , duty cycle \leqq 2%
- 3. The EAS data shows Max. rating . The test condition is V_{DD} =25V, V_{GS} =10V, L=0.1mH, I_{AS} =24A
- 4. The power dissipation is limited by 150°C junction temperature
- 5. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.



Typical Characteristics

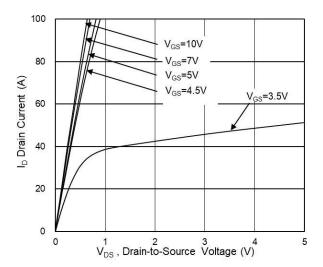


Fig.1 Typical Output Characteristics

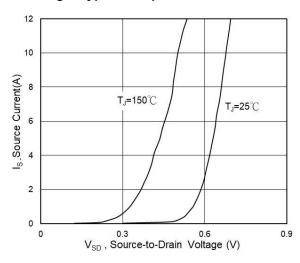


Fig.3 Source Drain Forward Characteristics

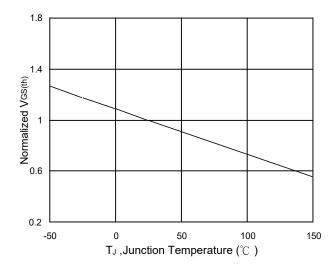


Fig.5 Normalized $V_{GS(th)}vs\ T_J$

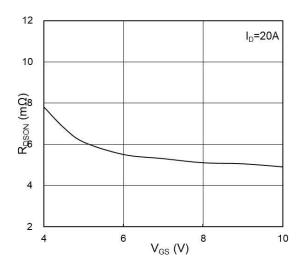


Fig.2 On-Resistance vs G-S Voltage

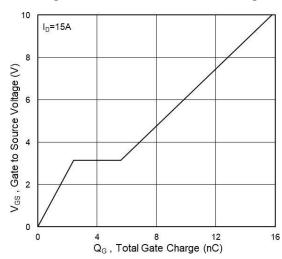


Fig.4 Gate-Charge Characteristics

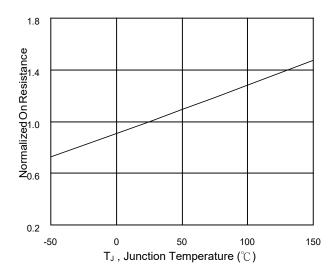
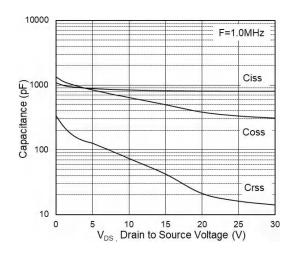


Fig.6 Normalized RDSON vs TJ



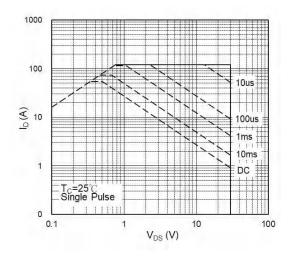


Fig.7 Capacitance

Fig.8 Safe Operating Area

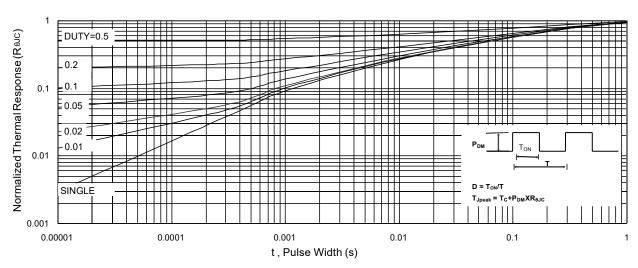


Fig.9 Normalized Maximum Transient Thermal Impedance

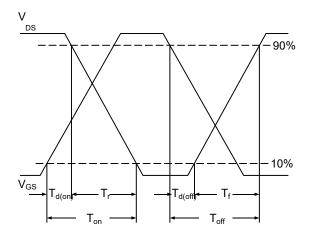


Fig.10 Switching Time Waveform

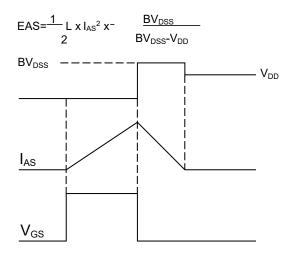
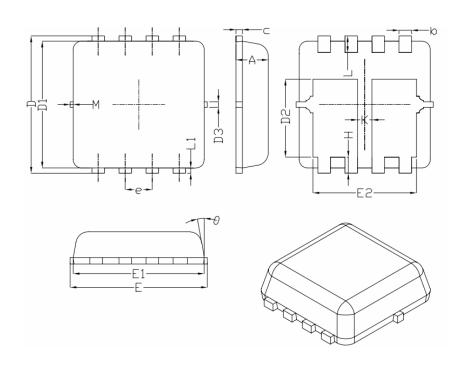
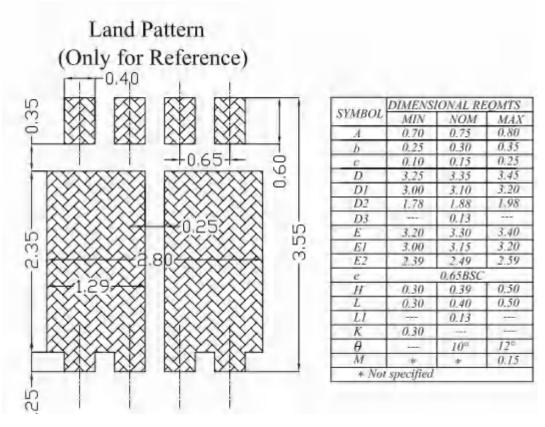


Fig.11 Unclamped Inductive Switching Waveform

DFN3X3B-8L Package Information







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