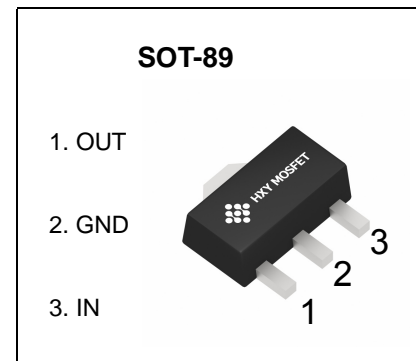




FEATURES

- Maximum output current
 I_{OM} : 0.15A
- Output voltage
 V_O : 5V
- Continuous total dissipation
 P_D : 0.5W ($T_a = 25^\circ\text{C}$)



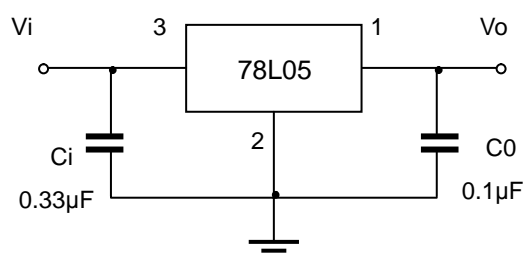
ABSOLUTE MAXIMUM RATINGS (Operating temperature range applies unless otherwise specified)

Parameter	Symbol	Value	Unit
Input Voltage	V_i	35	V
Operating Junction Temperature Range	T_{OPR}	-0 ~ +125	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-40 ~ +150	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS AT SPECIFIED VIRTUAL JUNCTION TEMPERATURE ($V_i=10\text{V}$, $I_o=40\text{mA}$, $C_i=0.33\mu\text{F}$, $C_o=0.1\mu\text{F}$, unless otherwise specified)

Parameter	Symbol	Test conditions	MIN	TYP	MAX	UNIT
Output voltage	V_O	$T_j=25^\circ\text{C}$ $7\text{V} \leq V_i \leq 20\text{V}$, $I_o=1\text{mA}-40\text{mA}$ $V_i=10\text{V}$, $I_o=1\text{mA}-70\text{mA}$	4.8 4.75 4.75	5.0	5.2 5.25 5.25	V
Load regulation	Reg_{load}	$T_j=25^\circ\text{C}$, $I_o=1\text{mA}-100\text{mA}$ $T_j=25^\circ\text{C}$, $I_o=1\text{mA}-40\text{mA}$		11 5	60 30	mV
Line regulation	Reg_{line}	$7\text{V} \leq V_i \leq 20\text{V}$, $T_j=25^\circ\text{C}$ $8\text{V} \leq V_i \leq 20\text{V}$, $T_j=25^\circ\text{C}$		55 45	150 100	mV
Input Bias Current	I_{IB}	$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$		3.8	6.0 5.5	mA
Input Bias Current Change	ΔI_{IB}	$8\text{V} \leq V_i \leq 20\text{V}$ $1\text{mA} \leq I_o \leq 40\text{mA}$			1.5 0.1	mA
Output noise voltage	V_N	$10\text{Hz} \leq f \leq 100\text{KHz}$		40		μV
Ripple rejection	RR	$I_o=40\text{mA}$, $8\text{V} \leq V_i \leq 18\text{V}$, $f=120\text{Hz}$, $T_j=25^\circ\text{C}$	41	49		dB
Dropout voltage	$V_i - V_O$	$T_j=25^\circ\text{C}$		1.7		V

TYPICAL APPLICATION



Note: Bypass capacitors are recommended for optimum stability and transient response and should be located as close as possible to the regulators.



TYPICAL CHARACTERISTICS @ $T_a=25^\circ\text{C}$ unless otherwise specified

Figure 1. Dropout Characteristics

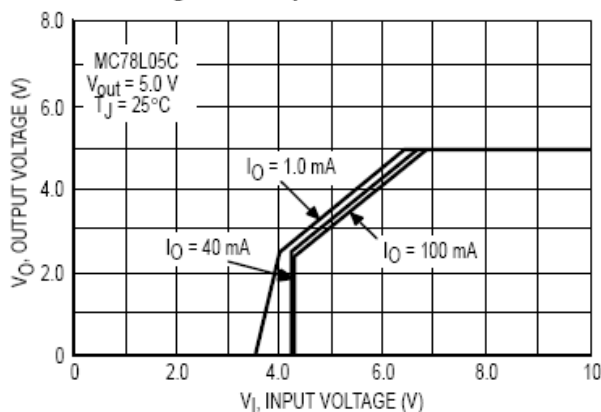


Figure 2. Dropout Voltage versus Junction Temperature

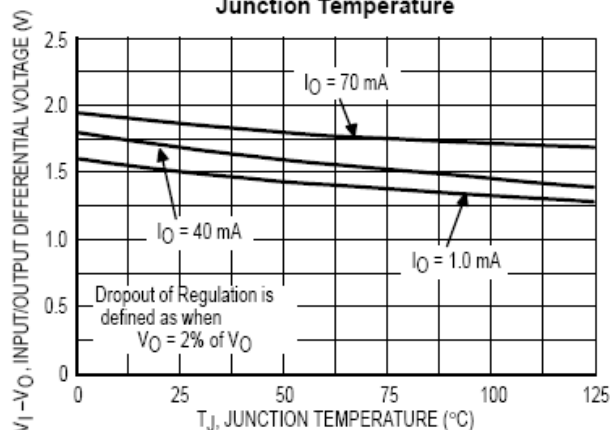


Figure 3. Input Bias Current versus Ambient Temperature

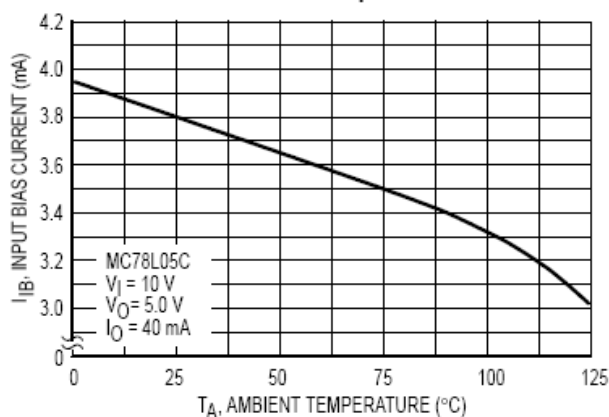


Figure 4. Input Bias Current versus Input Voltage

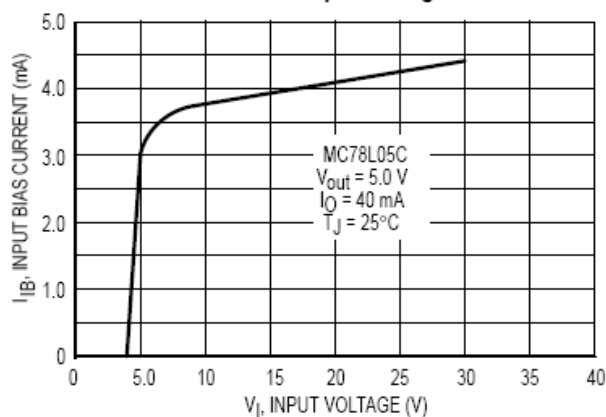


Figure 5. Maximum Average Power Dissipation versus Ambient Temperature – TO-92 Type Package

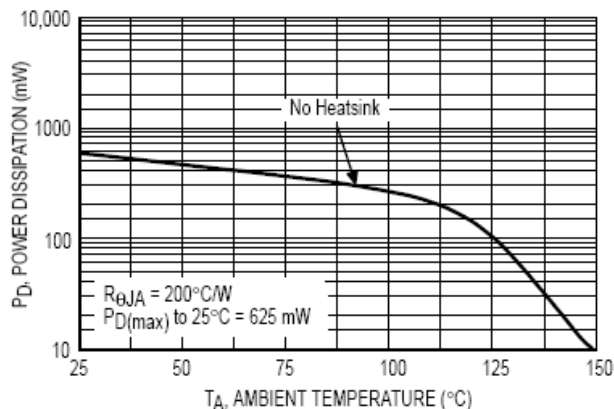
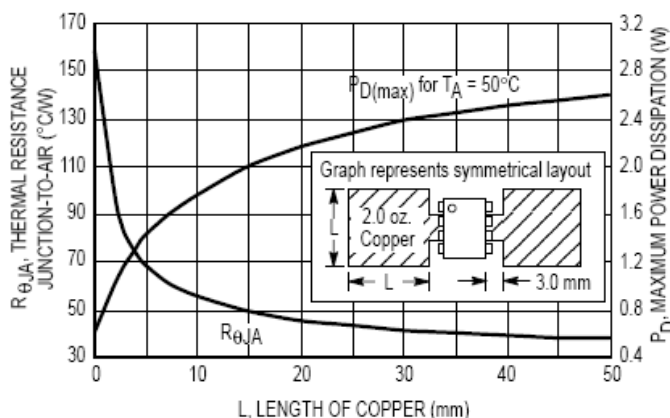
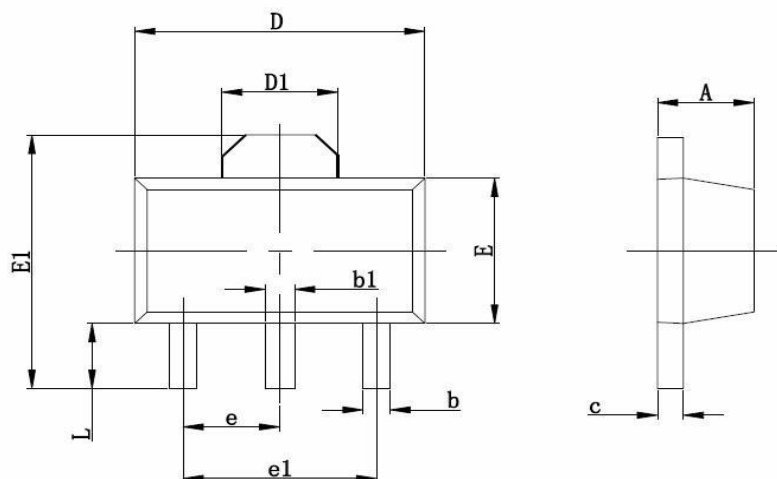


Figure 6. SOP-8 Thermal Resistance and Maximum Power Dissipation versus P.C.B. Copper Length





SOT-89 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.400	1.600	0.055	0.063
b	0.350	0.520	0.013	0.197
b1	0.400	0.580	0.016	0.023
c	0.350	0.440	0.014	0.017
D	4.400	4.600	0.173	0.181
D1	1.550 REF		0.061 REF	
E	2.350	2.550	0.091	0.102
E1	3.940	4.250	0.155	0.167
e	1.500 TYP		0.060TYP	
e1	3.000 TYP		0.118TYP	
L	0.900	1.100	0.035	0.047



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