

## **General Description**

The XC6221Axx2MR-G is a high accuracy, low noise, high speed CMOS Linear regulator with low power consumption and low dropout voltage, which provide large output currents even when the difference of the input-output voltage is small. The devices offer a new level of cost effective performance in cellular phones, laptop and ebook computers, and other portable devices.

The current limiter's fold-back circuit also operates as a short circuit protection and an output current limiter at the output pin.

The XC6221Axx2MR-G regulators are available in standard SOT-23-5L packages. Standard products are Pb-free and Halogen-free.

#### **Features**

Input voltage:1.5V~6.5VOutput range:1.2V~5.0V

Maximum output current: 400mA@VOUT=3.3V

PSRR: 60dB@1KHz

Dropout voltage:180mV @ IOUT=100mA

Quiescent current: 0.5uA Typ
 Shut-down current: <1µA</li>
 Recommend capacitor:1uF

Built-in Short-Circuit Protection, Current Limiter

## **Application**

- Radio control systems
- Cellphones, radiophone, digital cameras
- Bluetooth, wireless handsets
- Others portable consumer equipments

# Pin Configuration And Descriptions

SOT-23-5L(Top View)

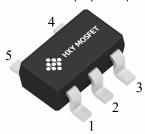


Table1:XC6221Axx2MR-G series (SOT-23-5L PKG)

| PIN NO | PIN NAME | FUNCTION                          |
|--------|----------|-----------------------------------|
| 1      | VIN      | Input                             |
| 2      | GND      | Ground                            |
| 3      | EN       | Enable(Active high, not floating) |
| 4      | NC       | Not connected                     |
| 5      | VOUT     | Output                            |

#### **Order Information**

| Orderable Device | Package   | Output Voltage | Packing Option |
|------------------|-----------|----------------|----------------|
| XC6221Axx2MR-G   | SOT-23-5L | 1.2V-5.0V      | 3000/Reel      |

xx:From 12-50



## **Absolute Maximum Ratings**

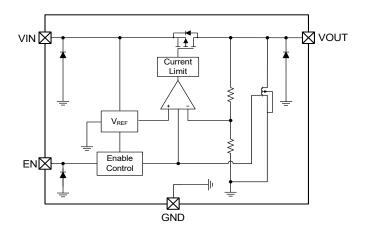
| Description                             | Symbol | Value Range               | Unit |
|---|--------|---------------------------|------|
| Input Voltage                           | Vin    | -0.3∼+8                   | V    |
| Storage Temperature Range               | Тѕтс   | <b>-</b> 55∼+150          | °C   |
| Operating Free-air<br>Temperature Range | TA     | <b>-</b> 40∼ <b>+</b> 125 | °C   |
| Output Current                          |        | 450                       | mA   |
| Package Lead<br>Soldering Temperature   |        | 260                       | °C   |

Note:Stresses greater than those listed under "Absolute Maximum Ratingsmay" cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditionsis" not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

## **Heat Dissipation**

| Description        | Symbol | Package   | Value Range | Unit |
|--------------------|--------|-----------|-------------|------|
| Thermal resistance | ÈJΑ    | SOT-23-5L | 260         | °C/W |
| Power dissipation  | Pw     | SOT-23-5L | 0.4         | W    |

## **Block Diagram**



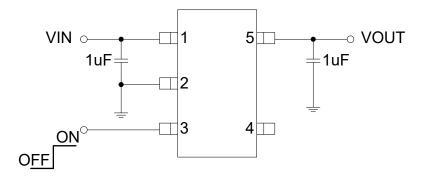


## DC Characteristics (unless otherwise noted T<sub>A</sub>= 25°C)

| Symbol               | Items                       | Conditions  | Min                  | Тур              | Max                  | Unit |
|----------------------|-----------------------------|---|----------------------|------------------|----------------------|------|
| V <sub>IN</sub>      | Input Voltage               |   | 1.5                  |                  | 6.5                  | V    |
| V <sub>оит</sub>     | Output Range                | V <sub>OUT</sub> ≤2.5V , I <sub>OUT</sub> =1mA          | V <sub>оит</sub> -50 | Vouт             | V <sub>оит</sub> +50 | mV   |
|                      |                             | 2.5V≤Vouт≤5V , louт=1mA                                 | -2                   | V <sub>OUT</sub> | 2                    | %    |
| ΙQ                   | Quiescent Current           | V <sub>ОUТ</sub> =3.3V, I <sub>ОUТ</sub> =0             |                      | 0.5              |                      | μA   |
| ILIMIT               | Current Limit               | V <sub>IN</sub> =V <sub>EN</sub> =4.5V                  |                      | 400              |                      | mA   |
| VDROP                | Dropout Voltage             | V <sub>OUT</sub> =3.3V, I <sub>OUT</sub> =100mA         |                      | 180              |                      | mV   |
| V DROP               |                             | Vоит=3.3V, Iоит=200mA                                   |                      | 400              |                      |      |
| $\triangle V_{LINE}$ | Line Regulation             | V <sub>IN</sub> =2.7~5.5V, I <sub>OUT</sub> =1mA        |                      | 0.01             | 0.15                 | %/V  |
| $\triangle V_{LOAD}$ | Load Regulation             | V <sub>ОUТ</sub> =3.3V, I <sub>ОUТ</sub> =1~300mA       |                      | 40               |                      | mV   |
| I <sub>SHORT</sub>   | Short Current               | $V_{EN}=V_{IN}$ , $V_{OUT}$ Short to GND with $1\Omega$ |                      | 35               |                      | mA   |
| I <sub>SHDN</sub>    | Shut-down Current           | V <sub>EN</sub> =0V                                     |                      |                  | 1                    | μA   |
| DCDD                 | Power Supply Rejection Rate | V <sub>IN</sub> =5V <sub>DC</sub> +0.5V <sub>P-P</sub>  |                      | 60               |                      | 40   |
| PSRR                 |                             | F=1KHz, I <sub>OUT</sub> =10mA                          |                      |                  |                      | dB   |
| V <sub>ENH</sub>     | EN logic high voltage       | V <sub>IN</sub> =5.5V, I <sub>OUT</sub> =1mA            | 1.2                  |                  | V <sub>IN</sub>      | V    |
| V <sub>ENL</sub>     | EN logic low voltage        | V <sub>IN</sub> =5.5V, V <sub>OUT</sub> =0V             |                      |                  | 0.4                  | V    |
| I <sub>EN</sub>      | EN Input Current            | V <sub>EN</sub> = 0 to 5.5V                             |                      |                  | 1                    | μA   |

# **Application Circuit**

### **Basic Circuits**





## **Function Description**

In general, all the capacitors need to be low leakage, Any leakage the capacitors have will reduce efficiency, will reduce efficiency, increase the quiescent current.

A recent trend in the design of portable deviceshas been to use ceramic capacitors to filterDC-DC converter inputs. Ceramic capacitors are often chosen because of their small sizelow equivalent series resistance (ESR) and high RMS current capability. Also, recently, designers have been looking to ceramic capacitors due to shortages of tantalum capacitors.

Unfortunately, using ceramic capacitors for input filtering can cause problems. Applying a voltage step to a ceramic capacitor causes alarge current surge that stores energy in the inductance of the power leads. A large voltage spike is created when the stored energy is transferred from these inductance into the ceramic capacitor. These voltage spikes caneasily be twice the amplitude of the input voltage step.

Many types of capacitors can be used for input by-pass,however, caution must be exercised when using multi layer ceramic capacitors (MLCC). Because of the self-resonant be generated under some start-up conditions, such as connecting the LDO input to a live power source.

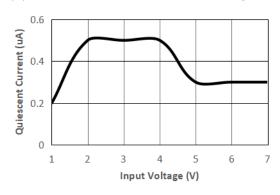
The LDO also requires an output capacitor for loop stability. Connect a 1uF tantalum capacitor from OUT to GND close to the pins. For improved transient response, this output capacitor may be ceramic.

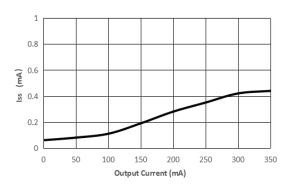
## **Typical Characteristics**

C<sub>IN</sub>=1uF, C<sub>OUT</sub>=1uF, V<sub>IN</sub>=4.5V, V<sub>OUT</sub>=3.3V ,SOT-23-5L,T<sub>A</sub>=25°C

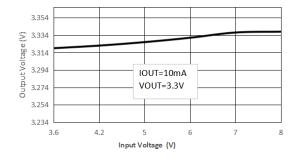
(Unless specified otherwise.Package:SOT-23-5L)

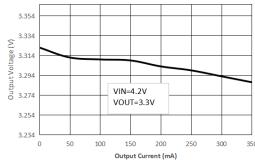
#### (1) Quiescent current vs Input voltage





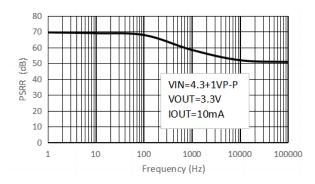
#### (2) Output Voltage vs Input voltage



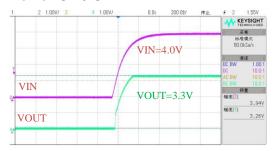


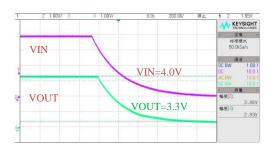


## (3) PSRR vs Frequency

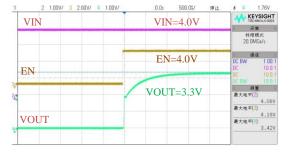


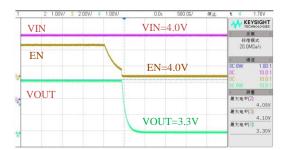
#### Power ON / OFF





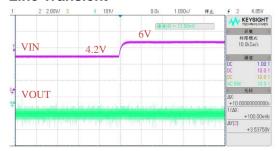
#### **EN ON/OFF**

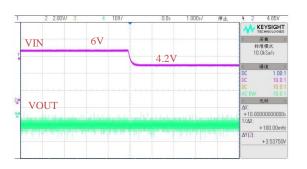




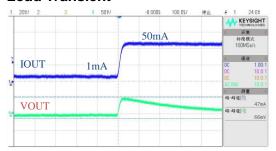
# XC6221Axx2MR-G 400mA CMOS Voltage Regulator

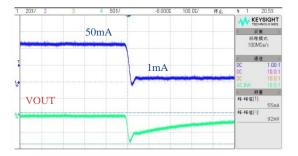
#### **Line Transient**





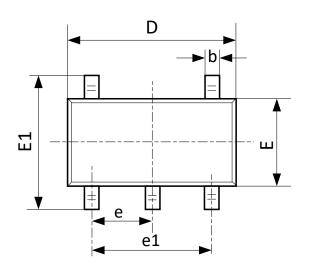
#### **Load Transient**

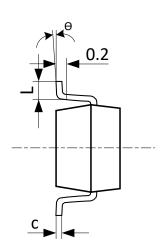


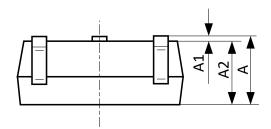




# Package Outline Dimensions SOT-23-5L







| Symbol | Dimensions In Millimeters |       | Dimensions In Inches |       |
|--------|---------------------------|-------|----------------------|-------|
|        | Min                       | Max   | Min                  | Max   |
| А      | 1.050                     | 1.250 | 0.041                | 0.049 |
| A1     | 0.000                     | 0.100 | 0.000                | 0.004 |
| A2     | 1.050                     | 1.150 | 0.041                | 0.045 |
| b      | 0.300                     | 0.500 | 0.012                | 0.020 |
| С      | 0.100                     | 0.200 | 0.004                | 0.008 |
| D      | 2.820                     | 3.020 | 0.111                | 0.119 |
| E      | 1.500                     | 1.700 | 0.059                | 0.067 |
| E1     | 2.650                     | 2.950 | 0.104                | 0.116 |
| е      | 0.950(BSC)                |       | 0.037(BSC)           |       |
| e1     | 1.800                     | 2.000 | 0.071                | 0.079 |
| L      | 0.300                     | 0.600 | 0.012                | 0.024 |
| θ      | 0℃                        | 8℃    | 0℃                   | 8℃    |



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