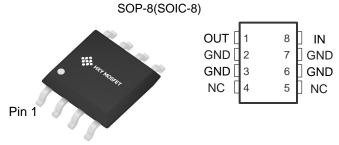
#### **FEATURES**

- Wide range of available, fixed output voltage.
- Low cost.
- Internal short-circuit current limiting.
- Internal thermal overload protection.
- No extermal components required.

# PIN DESCRIPTION



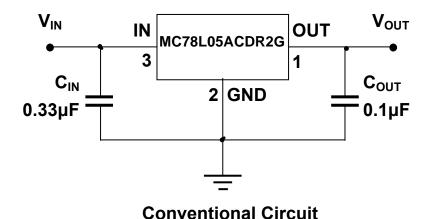
### **APPLICATIONS**

Three-terminal positive voltage regulator.

### MAXIMUM RATING operating temperature range applies unless otherwise specified

Symbol	Parameter	Value	Units
Vı	Input voltage	30	V
Ісм	Maximum output current	100	mA
P <sub>D</sub>	Power dissipation	500	mW
T <sub>OPR</sub>	Operating junction temperature	0 to +125	$^{\circ}$
$T_{j},T_{stg}$	Storage temperature range	-40 to +150	${\mathbb C}$

#### TYPICAL APPLICATION CIRCUIT



Low Dropout Linear Regulator

## **ELECTRICAL CHARACTERISTICS**

Devementer	Symbol	Took oon diki oo	78L05		LINUT	
Parameter		Test conditions	MIN	TYP	MAX	UNIT
		T <sub>j</sub> =25℃	4.8	5.0	5.2	
Output voltage	Vo	7V≤V <sub>i</sub> ≤20V,I <sub>O</sub> =1mA-40mA	4.75		5.25	V
		V <sub>1</sub> =10V,I <sub>O</sub> =1mA-70mA	4.75		5.25	
Load regulation	Reg <sub>load</sub>	T <sub>j</sub> =25℃, I <sub>O</sub> =1mA-100mA		11	60	mV
Load regulation		T <sub>j</sub> =25℃, I <sub>O</sub> =1mA-40mA		5	30	
Line regulation	Reg <sub>line</sub>	7V≤V <sub>i</sub> ≤20V, T <sub>j</sub> =25°C		55	150	mV
Line regulation		8V≤V <sub>i</sub> ≤20V, T <sub>j</sub> =25°C		45	100	
Input Bias Current	I <sub>IB</sub>	T <sub>j</sub> =25℃		3.8	6.0	mA
input Bias Current		T <sub>j</sub> =125℃			5.5	
Input Piga Current Change	$\triangle I_{IB}$	8V≤V <sub>i</sub> ≤20V			1.5	mA
Input Bias Current Change		1mA≤l <sub>O</sub> ≤40mA			0.1	
Output noise voltage	V <sub>N</sub>	10Hz ≤f≤100KHz		40		μV
Ripple rejection	RR	I <sub>O</sub> =40mA,8V≤V <sub>i</sub> ≤18V,f=120Hz ,T <sub>j</sub> =25℃	41	49		dB
Dropout voltage	V <sub>I</sub> -V <sub>O</sub>	T <sub>j</sub> =25℃		1.7		V

Low Dropout Linear Regulator

## TYPICAL CHARACTERISTICS @ Ta=25℃ unless otherwise specified

Figure 1. Dropout Characteristics

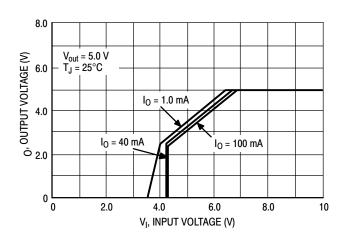


Figure 2. Dropout Voltage versus
Junction Temperature

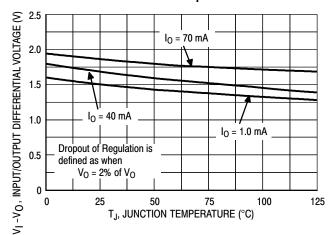


Figure 3. Input Bias Current versus Ambient Temperature

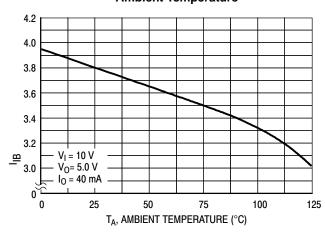


Figure 4. Input Bias Current versus Input Voltage

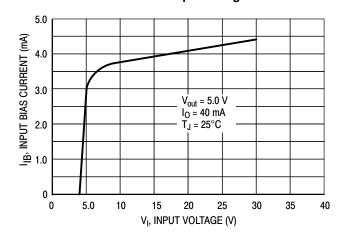
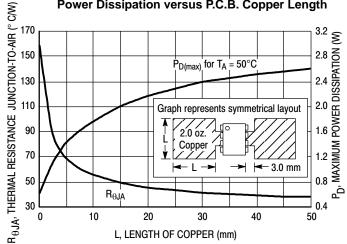


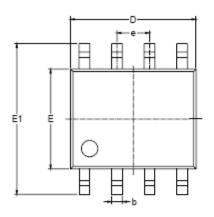
Figure 6. SOP-8 Thermal Resistance and Maximum Power Dissipation versus P.C.B. Copper Length

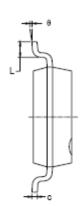


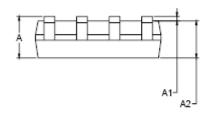
Low Dropout Linear Regulator

## PACKAGE OUTLINE DIMENSIONS

SOP-8(SOIC-8)







Symbol	Dimensions In Millimeters		Dimensions In Inches		
_	MIN	MAX	MIN	MAX	
Α	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
С	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
e	1.27	1.27 BSC		BSC	
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	

Low Dropout Linear Regulator

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