

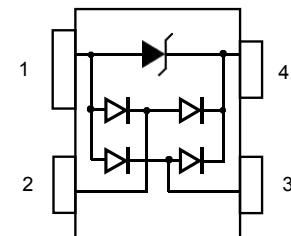


## Description

The HESDLC5VU2HI-A is a 2-channel ultra low capacitance rail clamp ESD protection diodes array. Each channel consists of a pair of ESD diodes that steer positive or negative ESD current to either the positive or negative rail. A zener diode is integrated in to the array between the positive and negative supply rails. In the typical applications, the negative rail pin (assigned as GND) is connected with system ground. The Positive ESD current is steered to the ground through an ESD diode and Zener diode and the positive ESD voltage is clamped to the zener voltage.



SOT-143



Circuit Diagram

## Features

- 100 W Peak Power per Line ( $t_p = 8/20\mu s$ )
- SOT-143 package
- Unidirectional configurations
- Protects 2 I/O Ports & Power Supply
- Low Capacitance: 0.8pF
- Low clamping voltage
- RoHS Compliant in Lead-Free Versions
- Transient protection for data lines to IEC 61000-4-2(ESD)  
 $\pm 30KV$ (air)  $\pm 30KV$ (contact); IEC61000-4-4(EFT) 40A (5/50ns)

## Ordering Information

Product ID	Pack	Qty(PCS)
HESDLC5VU2HI-A	SOT-143	3000

## Absolute Ratings ( $T_{amb}=25^\circ C$ )

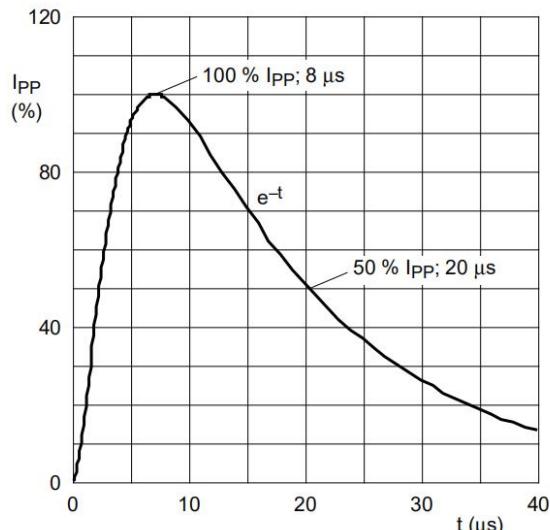
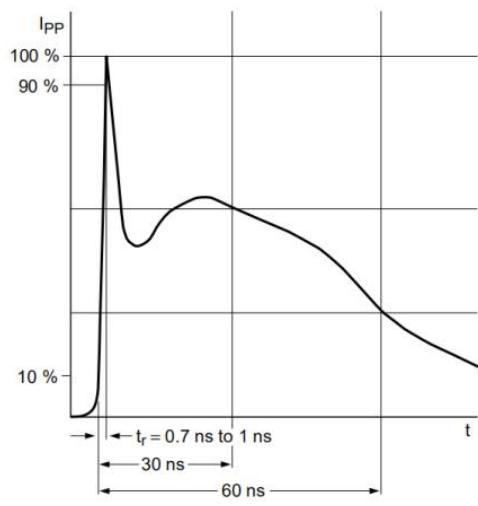
Symbol	Parameter	Value	Units
$P_{PP}$	Peak Pulse Power ( $t_p = 8/20\mu s$ )	100	W
$T_L$	Maximum lead temperature for soldering during 10s	260	°C
$T_{stg}$	Storage Temperature Range	-55 to +150	°C
$T_{op}$	Operating Temperature Range	-40 to +125	°C
$T_j$	Maximum junction temperature	150	°C
	IEC61000-4-2 (ESD) air discharge contact discharge	$\pm 30$ $\pm 30$	KV



## Electrical Characteristics

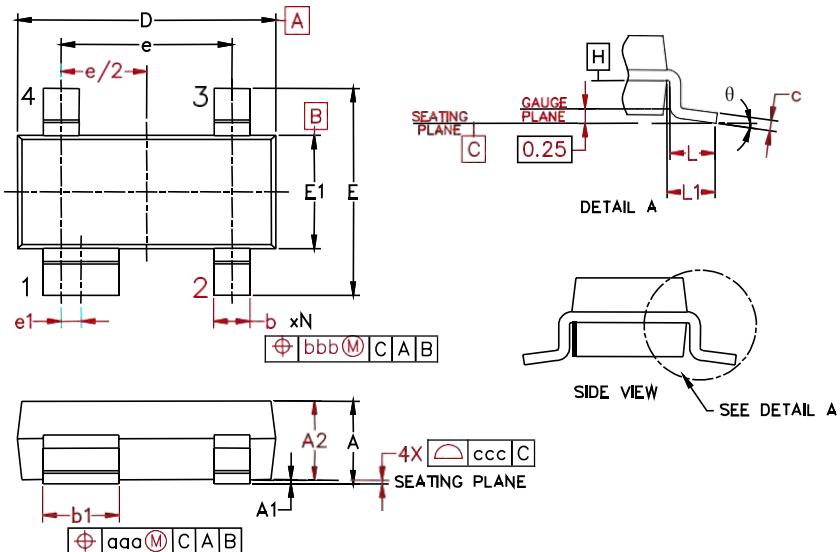
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Reverse Stand-off Voltage	$V_{RWM}$				5	V
Reverse Breakdown Voltage	$V_{BR}$	$I_t = 1\text{mA}$	6		8.5	V
Reverse Leakage Current	$I_R$	$V_{RWM} = 5.0\text{V}, T=25^\circ\text{C}$			1	$\mu\text{A}$
Clamping Voltage(I/O-GND)	$V_C$	$I_{PP} = 1\text{A}, t_P = 8/20\mu\text{s}$			14.0	V
Clamping Voltage(VDD-GND)	$V_C$	$I_{PP} = 9\text{A}, t_P = 8/20\mu\text{s}$			14.0	V
Capacitance Between IO and GND	$C_J$	$V_R=0\text{V}, f = 1\text{MHz}$		0.4	0.45	pF
Capacitance Between IO and I/O	$C_J$	$V_R=0\text{V}, f = 1\text{MHz}$		0.8	0.9	pF

## Typical Characteristics





## Package Mechanical Data



Symbol	Inches			Millimeters		
	Min.	Nom.	Max.	Min.	Nom.	Max.
<b>A</b>	0.031	-	0.048	0.80	-	1.22
<b>A1</b>	0.000	-	0.008	0.013	-	0.15
<b>A2</b>	0.020	0.035	0.042	0.75	0.90	1.07
<b>b</b>	0.011	-	0.020	0.30	-	0.51
<b>b1</b>	0.029	-	0.037	0.76	-	0.94
<b>c</b>	0.003	-	0.008	0.08	-	0.20
<b>D</b>	0.110	0.114	0.120	2.80	2.90	3.04
<b>E</b>	0.082	0.093	0.104	2.10	2.37	2.64
<b>E1</b>	0.047	0.051	0.055	1.20	1.30	1.40
<b>e</b>	0.075			1.92 BSC		
<b>e1</b>	0.008			0.20 BSC		
<b>L</b>	0.015	0.020	0.024	0.40	0.50	0.60
<b>L1</b>	(0.021)			(0.54)		
<b>N</b>	4			4		
<b>θ</b>	0°	-	8°	0°	-	8°
<b>aaa</b>	0.006			0.15		
<b>bbb</b>	0.008			0.20		
<b>ccc</b>	0.004			0.10		



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