



Discription

The HESDNC5VU1EL-A protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. Excellent clamping capability, low leakage, low capacitance, and fast response time provide best in class protection on designs that are exposed to ESD.

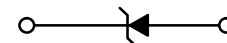
It gives designer the flexibility to protect one unidirectional line in applications where arrays are not practical.



SOD-323

Features

- ★ Small Body Outline Dimensions
- ★ Low Body Height
- ★ Stand-off Voltage: 5 V
- ★ Peak Power up to 200 Watts @ 8 x 20 μ s Pulse
- ★ Low Leakage
- ★ Response Time is Typically < 1 ns
- ★ ESD Rating of Class 3 (> 16 kV) per Human Body Model
- ★ IEC61000-4-2 Level 4 ESD Protection
- ★ IEC61000-4-4 Level 4 EFT Protection
- ★ We declare that the material of product compliance with RoHS requirements.



Circuit Diagram

Ordering Information

Product ID	Pack	Qty(PCS)
HESDNC5VU1EL-A	SOD-323	3000

Absolute Ratings ($T_{amb}=25^{\circ}\text{C}$)

Symbol	Parameter	Value	Units
P_{PP}	Peak Pulse Power ($t_p = 8/20\mu\text{s}$)	135	W
T_L	Maximum lead temperature for soldering during 10s	260	$^{\circ}\text{C}$
T_{stg}	Storage Temperature Range	-55 to +150	$^{\circ}\text{C}$
T_{op}	Operating Temperature Range	-40 to +125	$^{\circ}\text{C}$
T_j	Maximum junction temperature	150	$^{\circ}\text{C}$
	IEC61000-4-2 (ESD) air discharge	± 15	KV
	contact discharge	± 8	
	IEC61000-4-4 (EFT)	40	A
	ESD Voltage Per Human Body Model	16	KV



Electrical Characteristics Ratings at 25°C ambient temperature unless otherwise specified. VF = 0.9V at IF = 10mA

Device	V_{RWM} (V)	I_R (μ A) @ V_{RWM}	V_{BR} (V)@ I_T (Note 1)	I_T	V_C (V) @ $I_{PP}=5$ A*	V_C (V) @ Max I_{PP} *	I_{PP} (A)*	P_{PK} (W)*	C (pF)
	Max	Max	Min	mA	Typ	Max	Max	Max	Typ
HESDNC5VU1EL-A	5.0	1.0	6.2	1.0	11.6	15	9	135	60

*Surge current waveform per Figure 1.

1. V_{BR} is measured with a pluse test current I_T at an ambient temperature of 25°C.

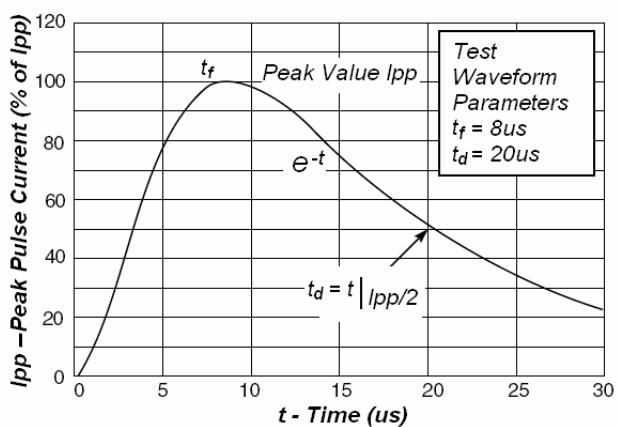


Fig1. Pulse Waveform

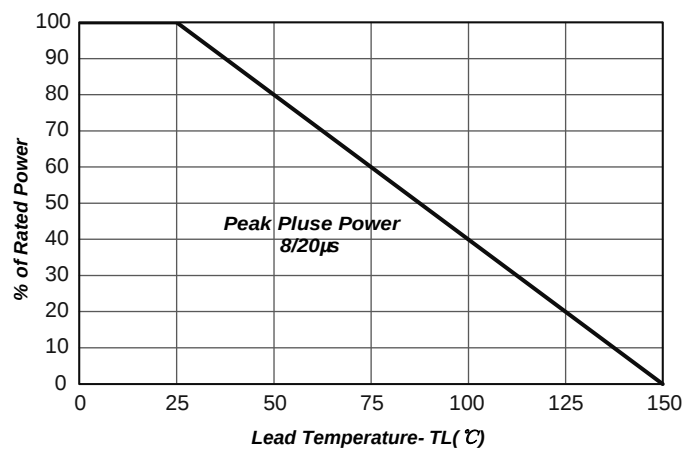
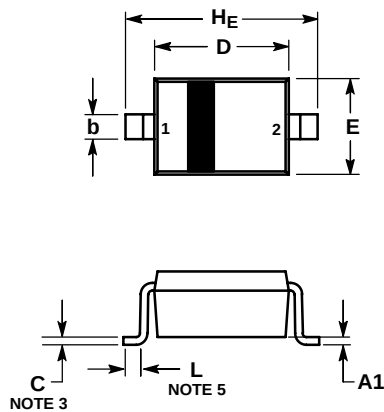


Fig2. Power Derating



SOD-323

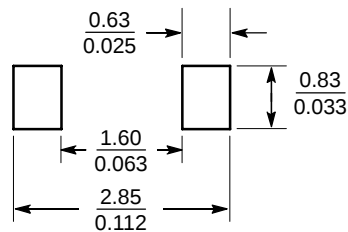


NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. LEAD THICKNESS SPECIFIED PER L/F DRAWING WITH SOLDER PLATING.
4. DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
5. DIMENSION L IS MEASURED FROM END OF RADIUS.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.80	0.90	1.00	0.031	0.035	0.040
A1	0.00	0.05	0.10	0.000	0.002	0.004
A3	0.15 REF			0.006 REF		
b	0.25	0.32	0.4	0.010	0.012	0.016
C	0.089	0.12	0.177	0.003	0.005	0.007
D	1.60	1.70	1.80	0.062	0.066	0.070
E	1.15	1.25	1.35	0.045	0.049	0.053
L	0.08			0.003		
H _E	2.30	2.50	2.70	0.090	0.098	0.105

SOLDERING FOOTPRINT*





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