### **Discription**

The HESDLC5VB1GF-A protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. Excellent clamping capability, low leakage, low capacitance, and fast response time provide best in class protection on designs that are exposed to ESD.

It gives designer the flexibility to protect one bi-directional line in applications where arrays are not practical.



DFN0603-2L

## **Specification Features:**

- ★ Ultra Low Capacitance 3 pF
- ★ Low Clamping Voltage
- ★ Small Body Outline Dimensions:

(0.61 mm x 0.31 mm)

★ Low Body Height: 0.28 mm

★ Stand-off Voltage: 5 V

★ Low Leakage

★ Response Time is Typically < 1.0 ns

★ IEC61000-4-2 Level 4 ESD Protection

★ This is a Pb-Free Device



Circuit Diagram

## **Ordering information**

Product ID	Pack	Qty(PCS)		
HESDLC5VB1GF-A	DFN0603-2L	15000		

#### Absolute Ratings (T<sub>amb</sub>=25°C)

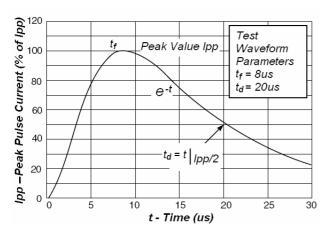
Symbol	Parameter	Value	Units
P <sub>PP</sub>	Peak Pulse Power (tp = 8/20µs)	30	W
TL	Maximum lead temperature for soldering during 10s	260	°C
T <sub>stg</sub>	Storage Temperature Range	-55 to +150	°C
T <sub>op</sub>	Operating Temperature Range	-40 to +125	°C
Tj	Maximum junction temperature	150	°C
	IEC61000-4-2 (ESD) air dischar contact dischar	<u> </u>	KV

#### **Electrical Characteristics** (T<sub>A</sub> = 25°C unless otherwise noted)

	V <sub>RWM</sub> (V)	I <sub>R</sub> (uA) @ V <sub>RWM</sub>	V <sub>BR</sub> (V) @ I <sub>T</sub> = 1mA (Note 2)		C (pF)	V <sub>C</sub> (V) @ l <sub>PP</sub> = 3.5 A (Note 3)	I <sub>PP</sub> (A) t <sub>p</sub> =8/20μs	P <sub>PP</sub> (W)	V <sub>C</sub>
Device	Max	Max	Min	Max	Тур	Max	Max	Max	Per IEC61000-4-2 (Note4)
HESDLC5VB1GF-A	5.0	1	5.5	9.6	2.5	15	2	30	Figures 1 and 2 See Below

- V<sub>BR</sub> is measured with a pulse test current I<sub>T</sub> at an ambient temperature of 25°C.
- 3. Surge current waveform per Figure 4.
- 4. For test procedure see Figures 3.

## **Typical Characteristics**



100 90 80 70 % of Rated Power 60 50 Peak Pluse Power 40 8/20µs 30 20 10 25 125 150 Lead Temperature-  $TL(\mathcal{C})$ 

Fig1. Pulse Waveform

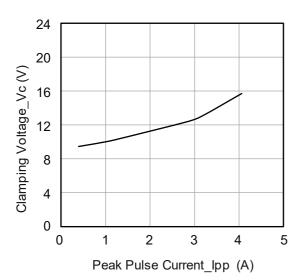


Fig 3.Clamping Voltage vs.Peak Pulse Current



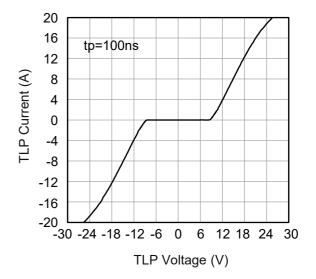
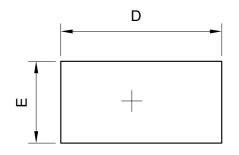
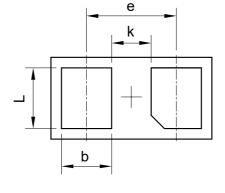


Fig 4. TLP Measurement



# **Package Outline Dimension**





DFN0603-DL Dim Min Тур. Max 0.58 D 0.61 0.64 Ε 0.28 0.31 0.34 0.34 е 0.20 0.23 0.26 b 0.16 0.19 0.22 Α 0.25 0.28 0.31 k 0.12 0.15 0.18 All Dimensions in mm

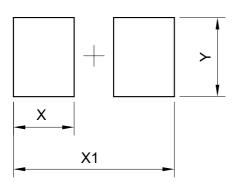
**TOP VIEW** 





SIDE VIEW

## **Suggested Pad layout**



DFN0603-DL			
DIM	(mm)		
Χ	0.23		
X1	0.61		
Υ	0.30		



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